

FIG. 1A

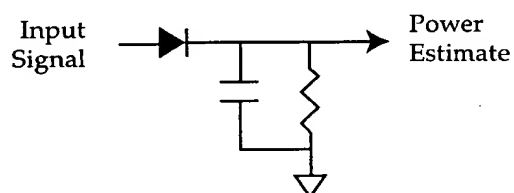


FIG. 1B

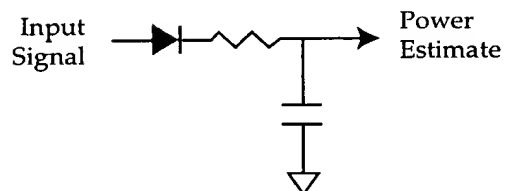


FIG. 1C

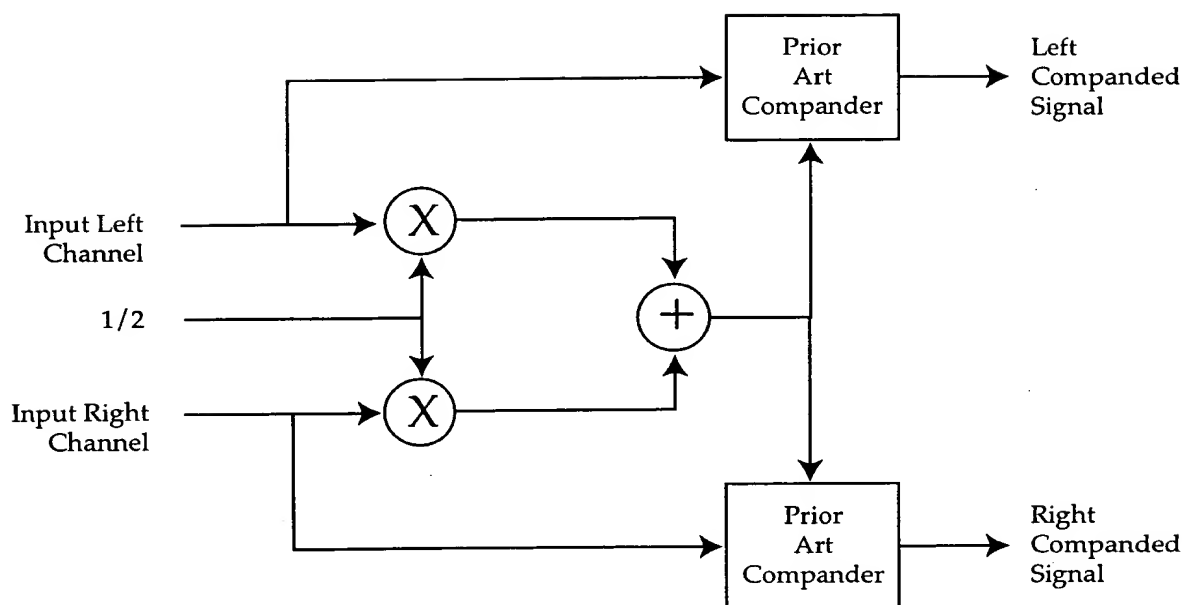


FIG. 1D

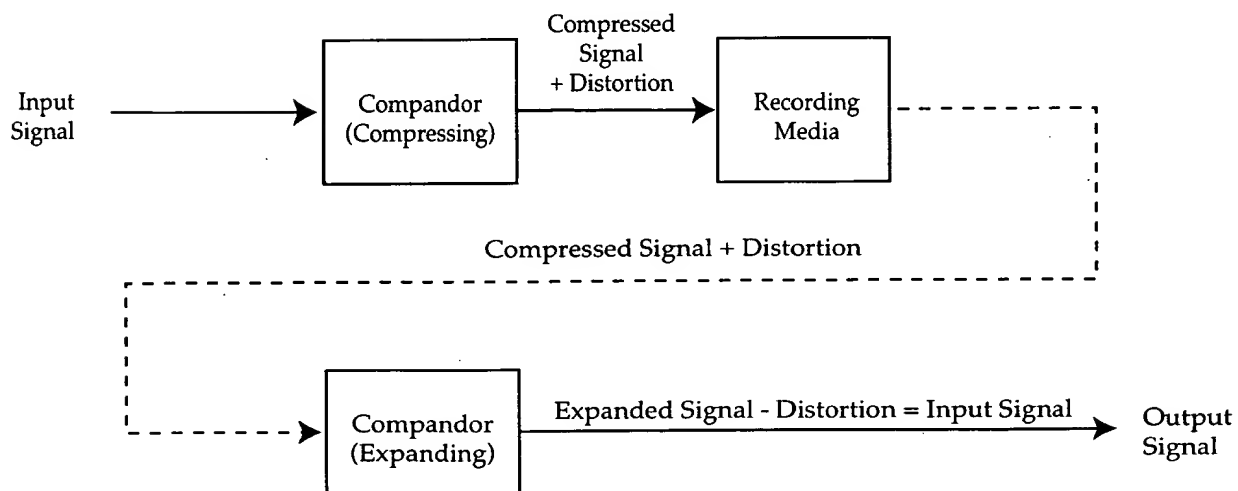


FIG. 1E

000000-11282/60

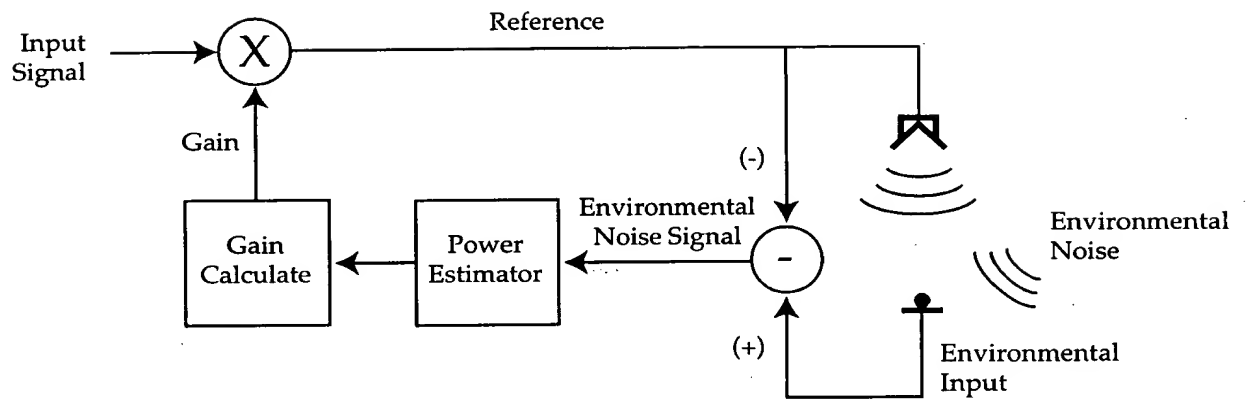


FIG. 2

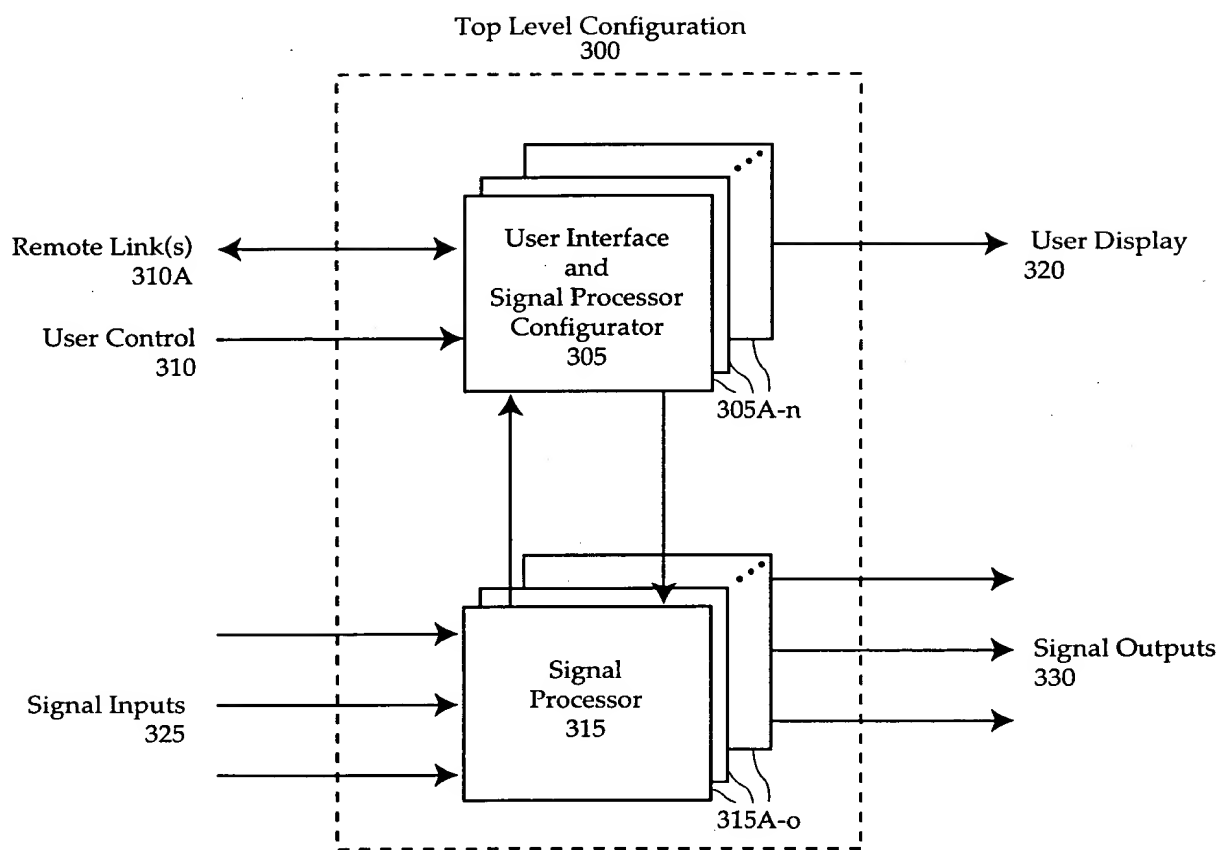


FIG. 3A

006277-5782/60

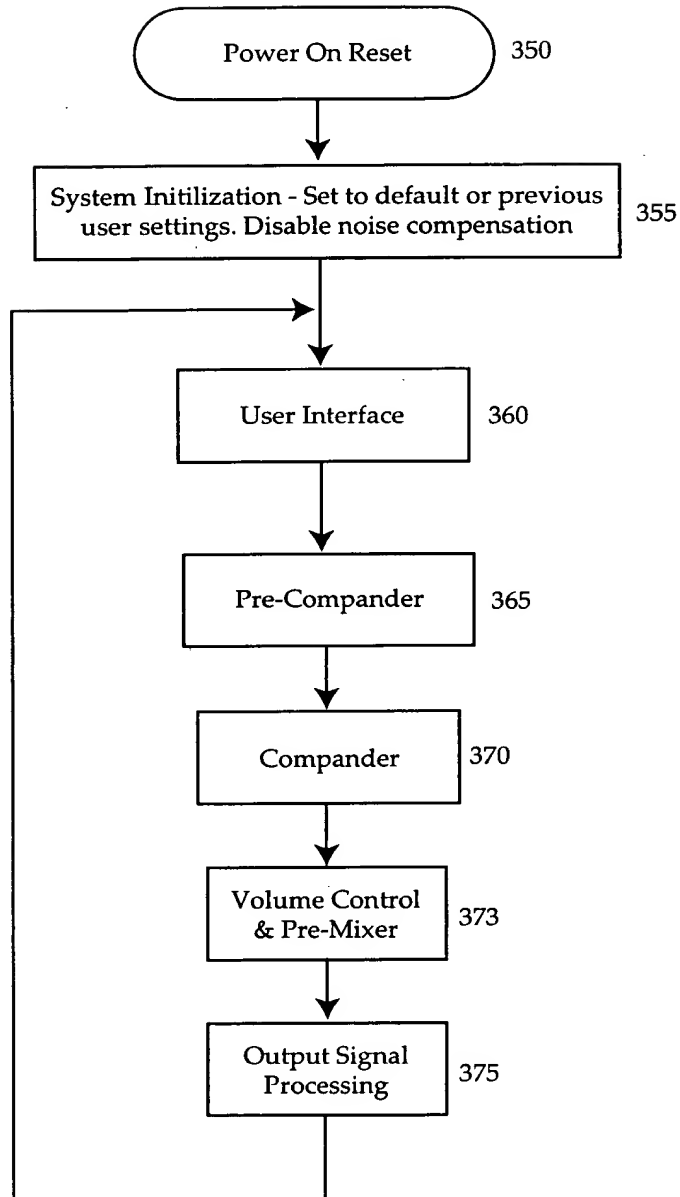


FIG. 3B

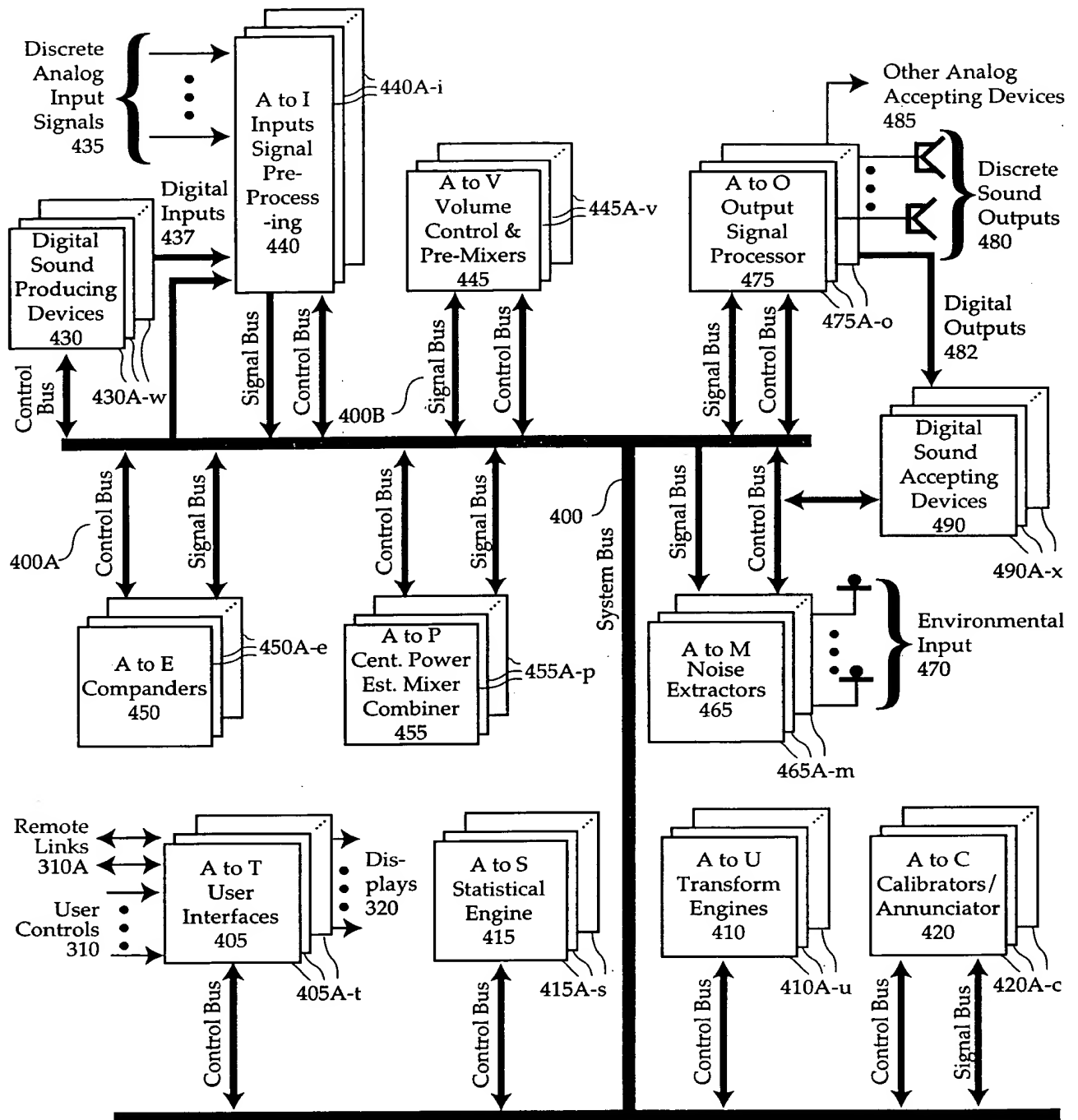


FIG. 4

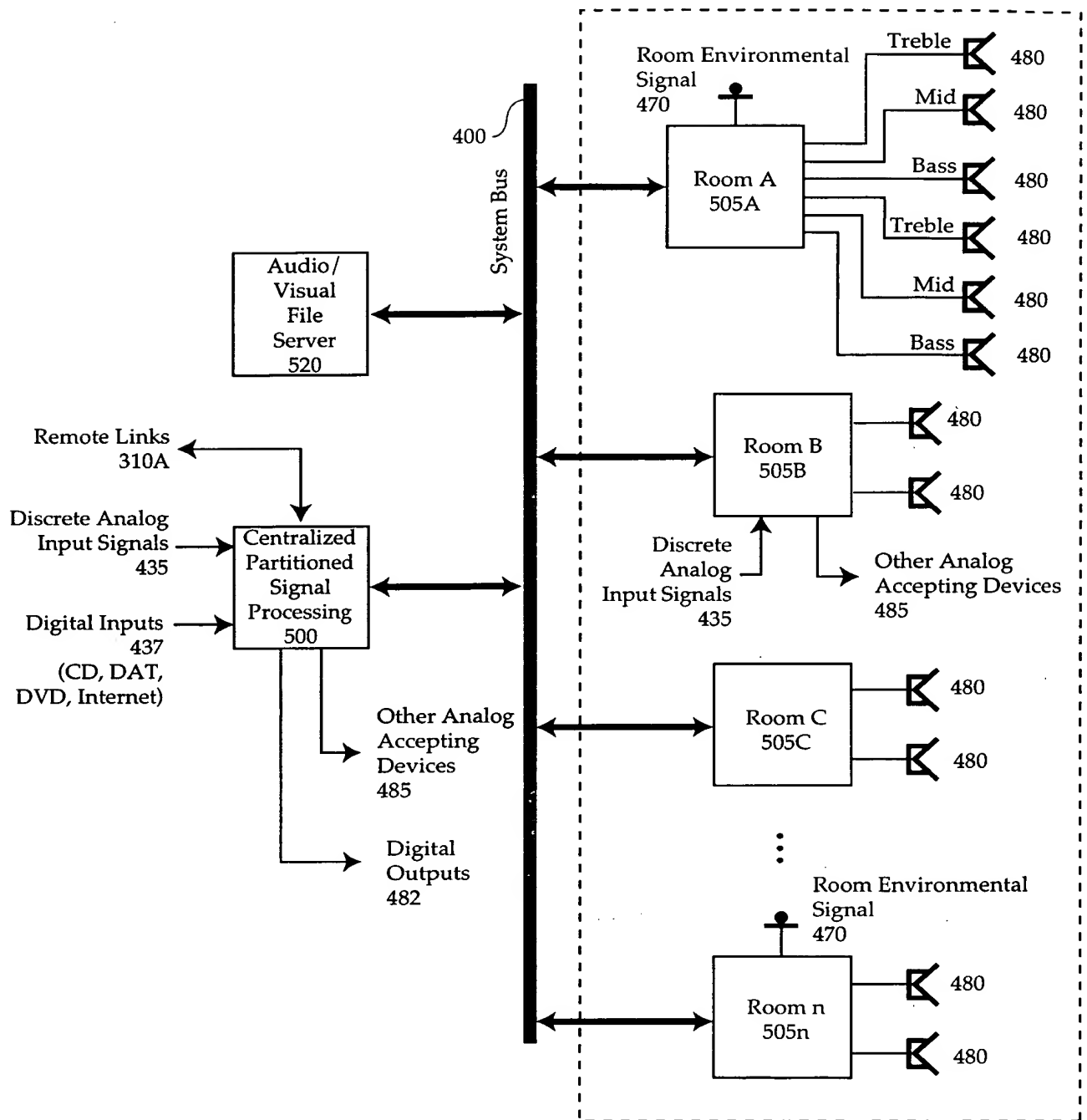


FIG. 5A

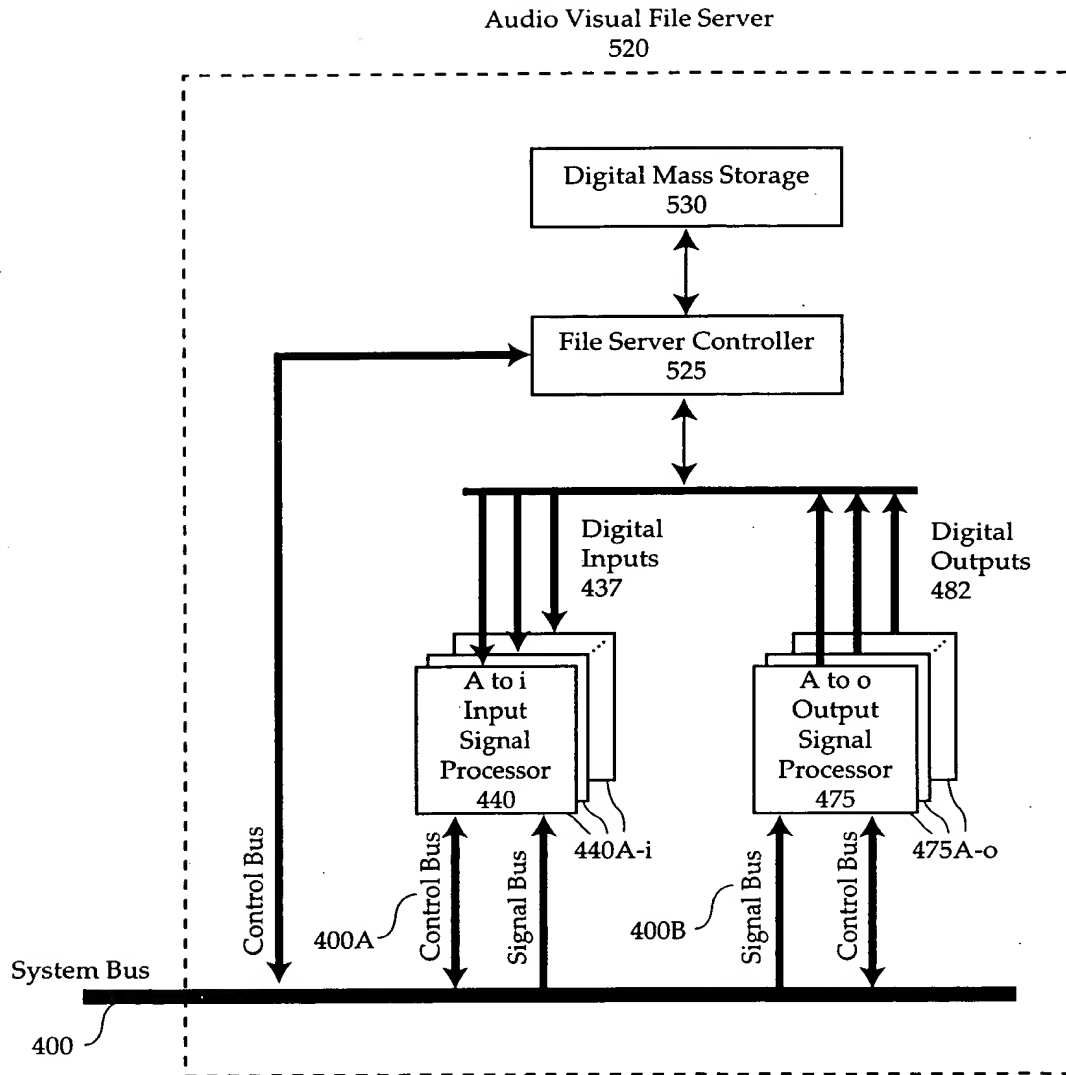


FIG. 5B



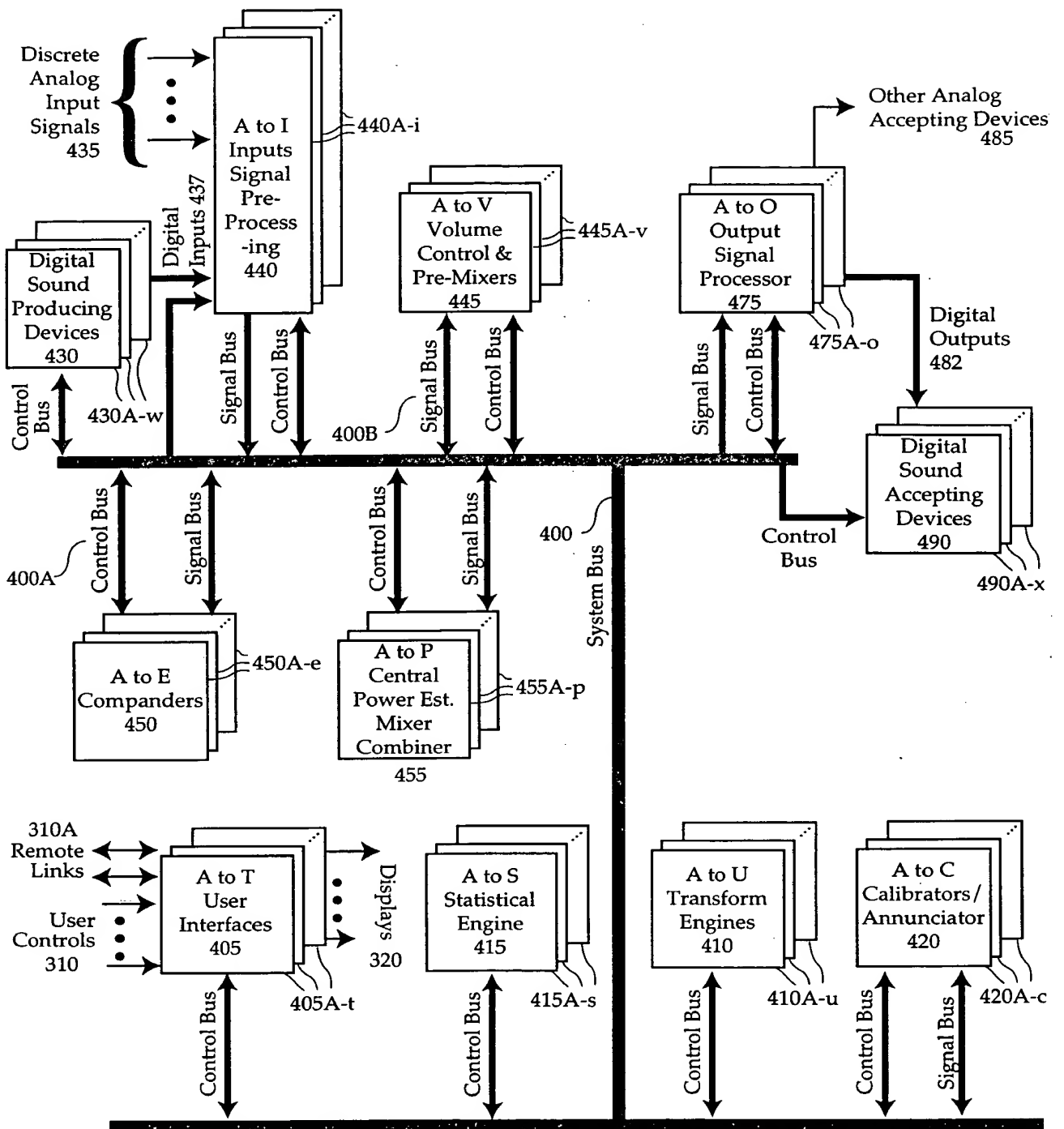


FIG. 5C



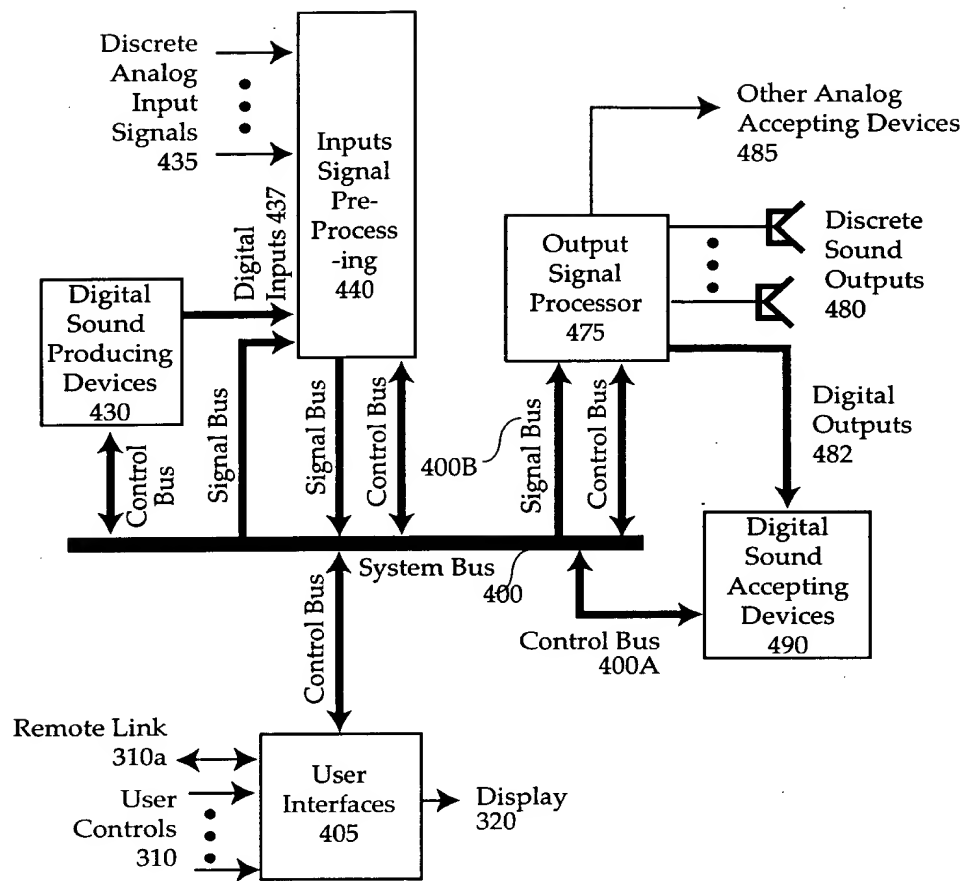


FIG. 5E

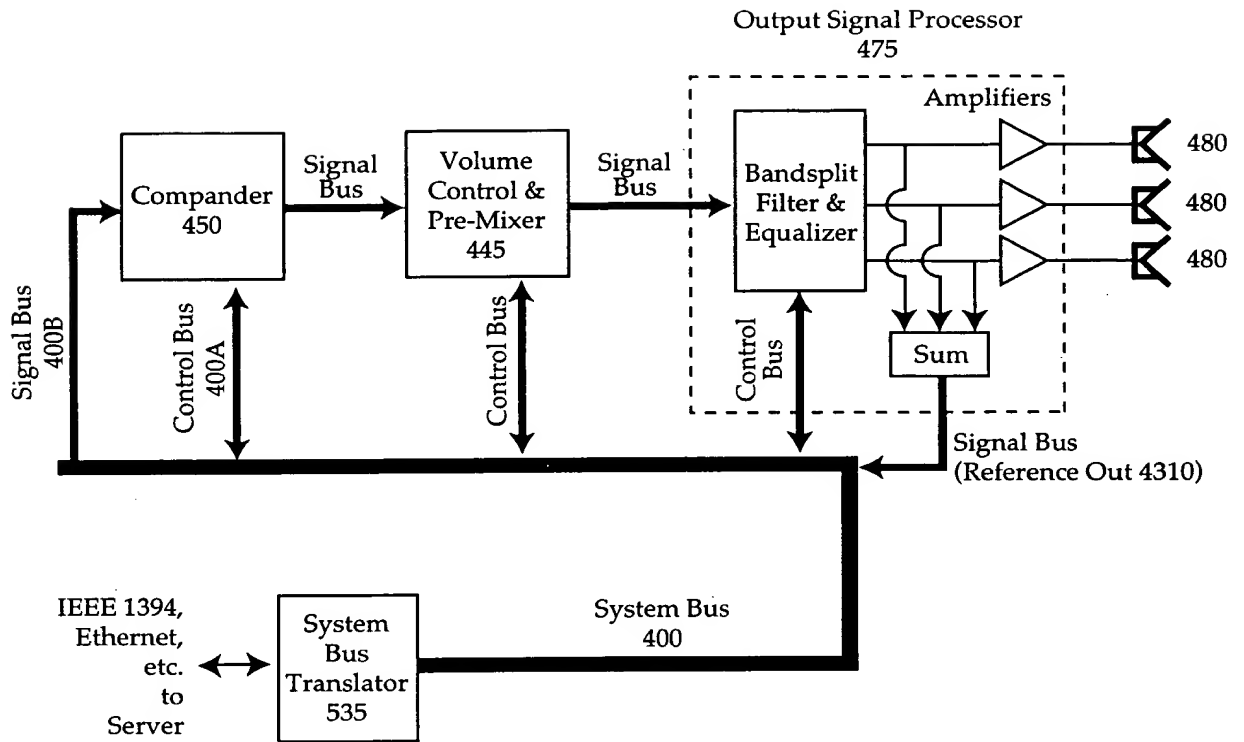


FIG. 5F

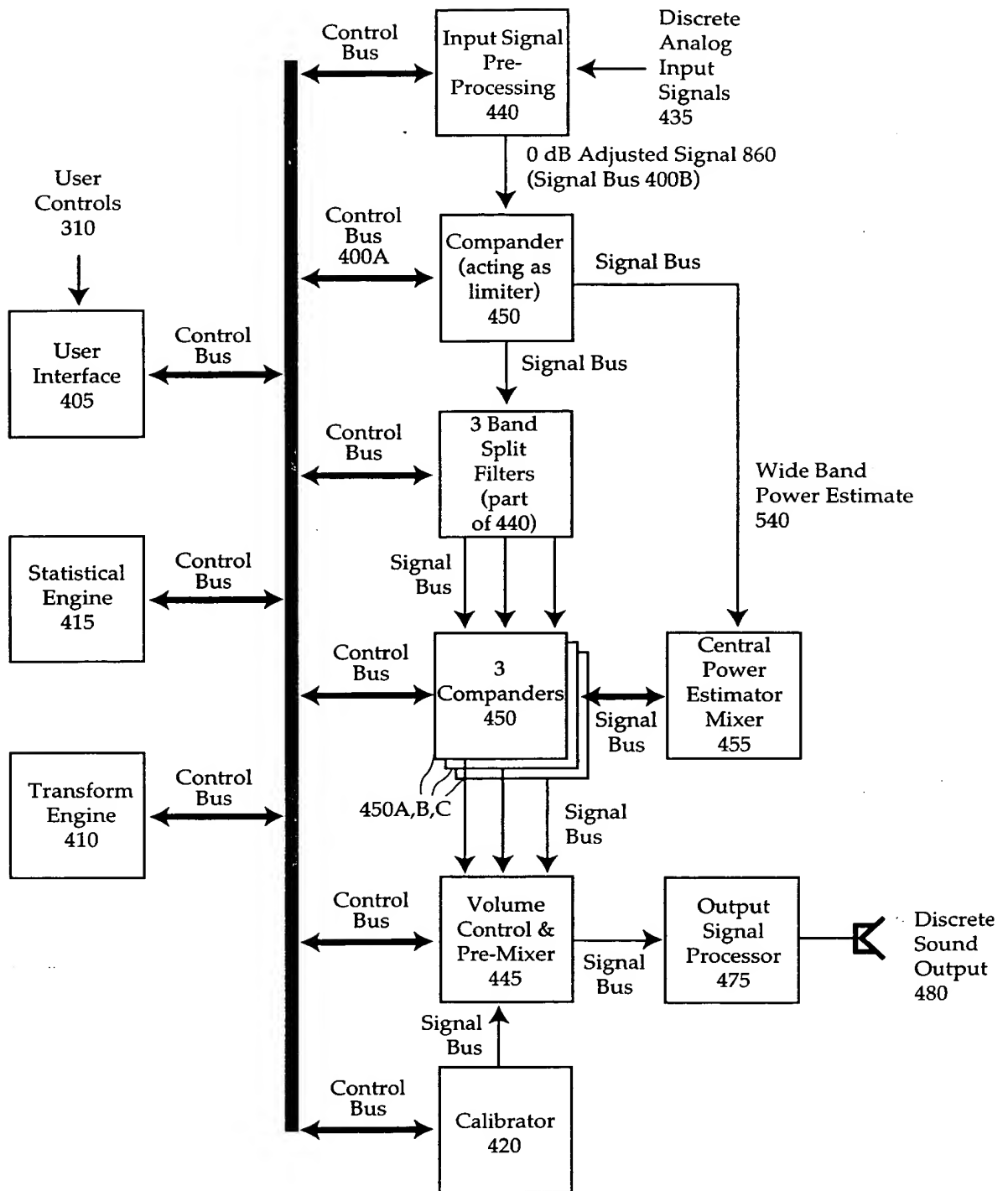


FIG. 5G

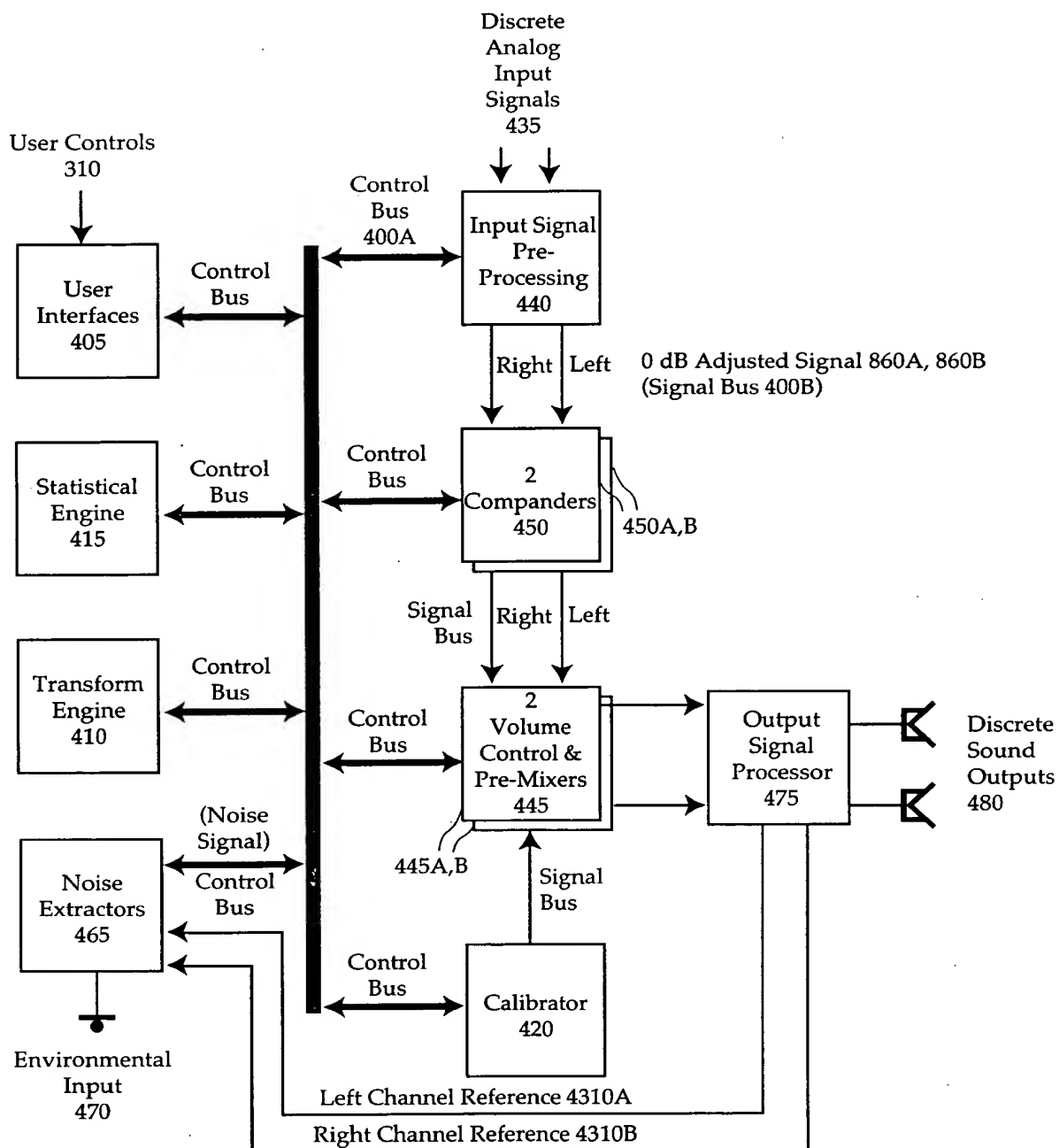


FIG. 5H

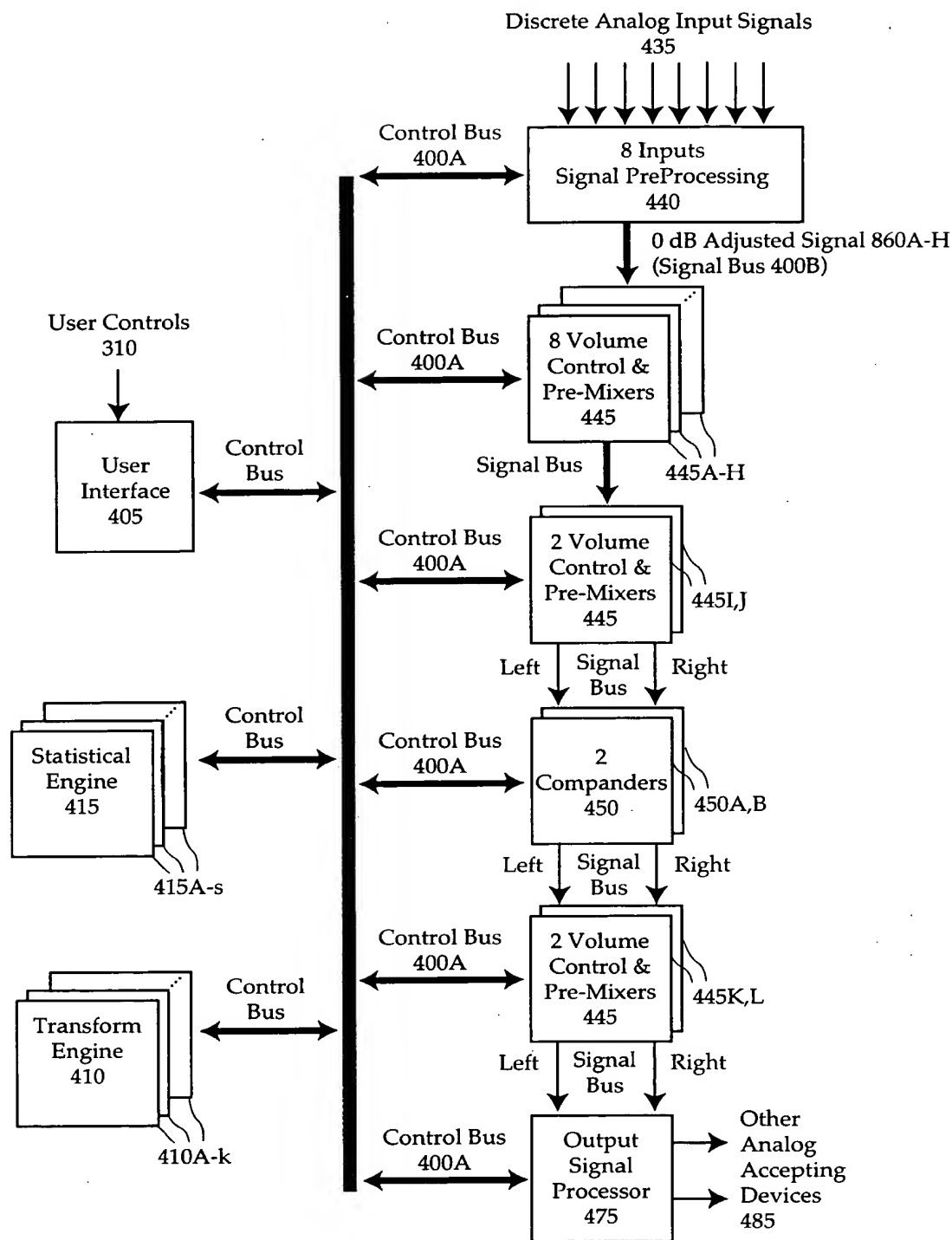


FIG. 5I

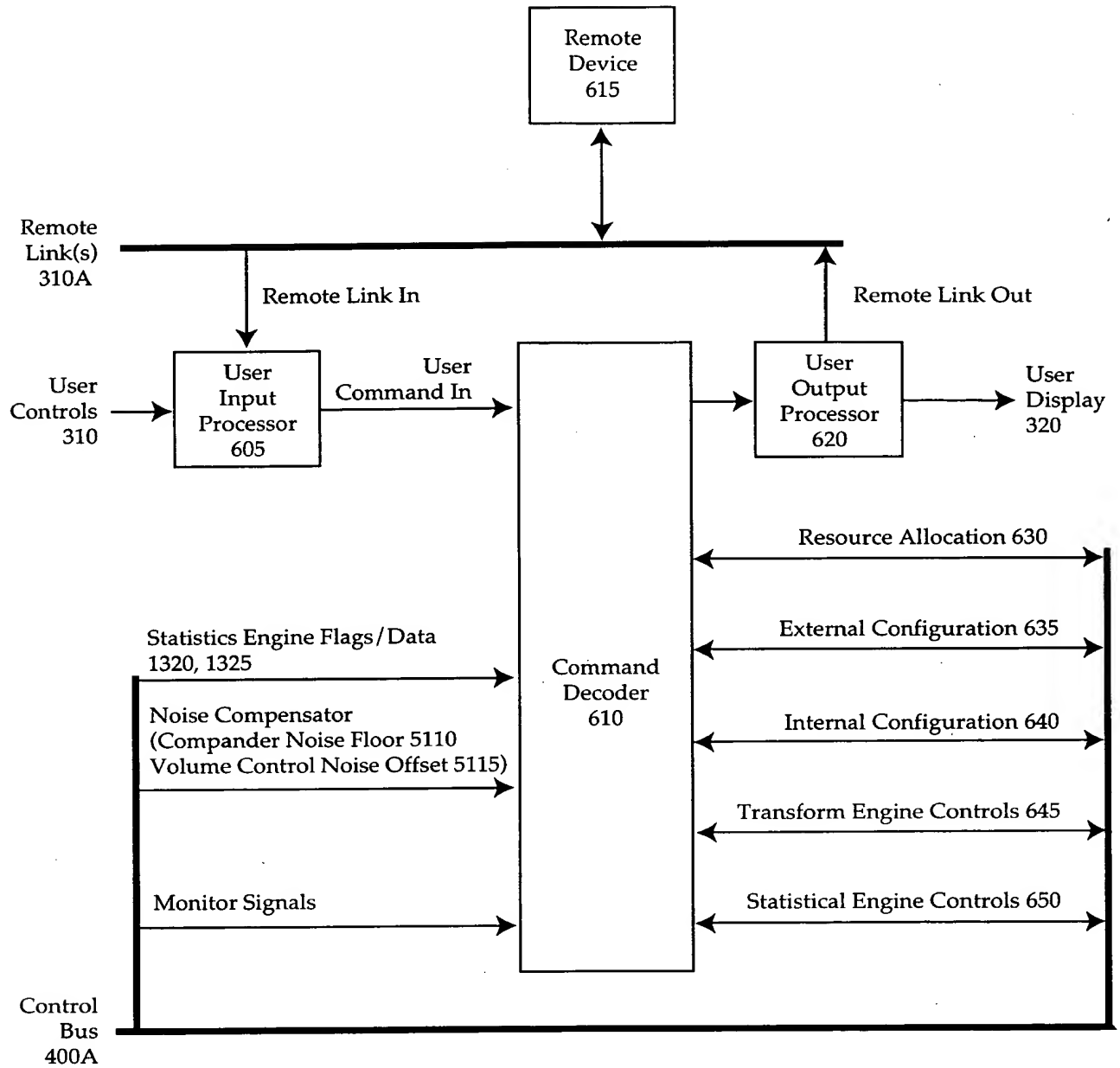


FIG. 6A



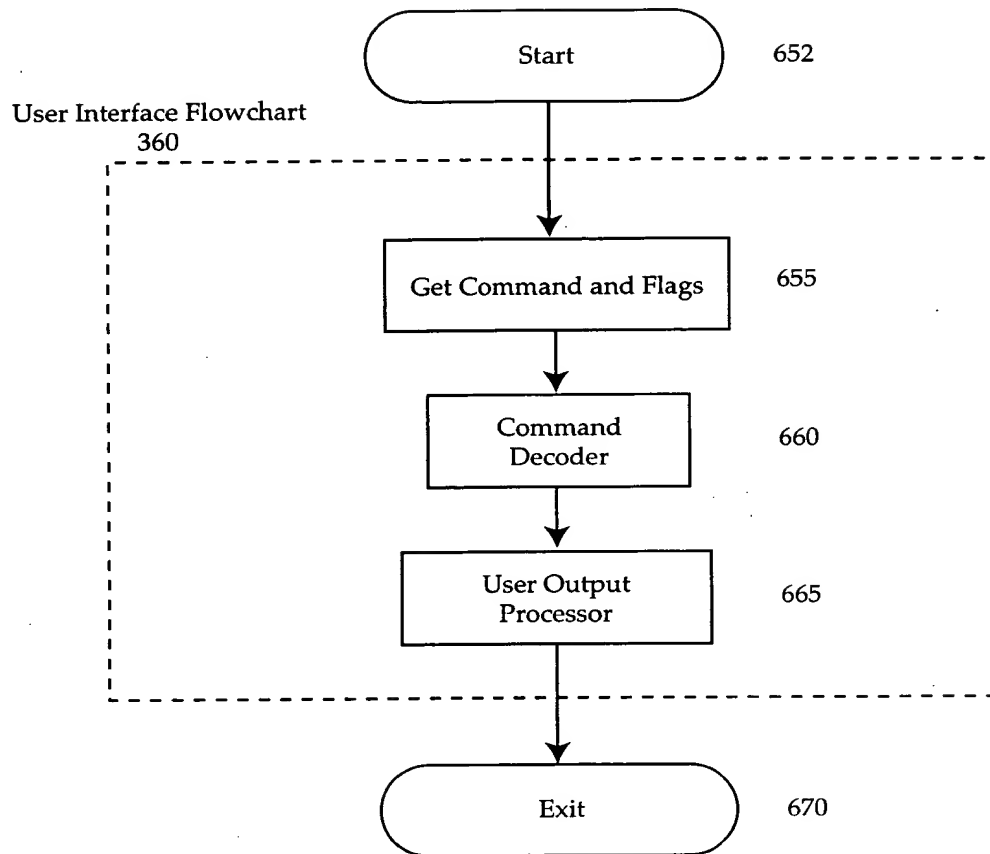


FIG. 6B

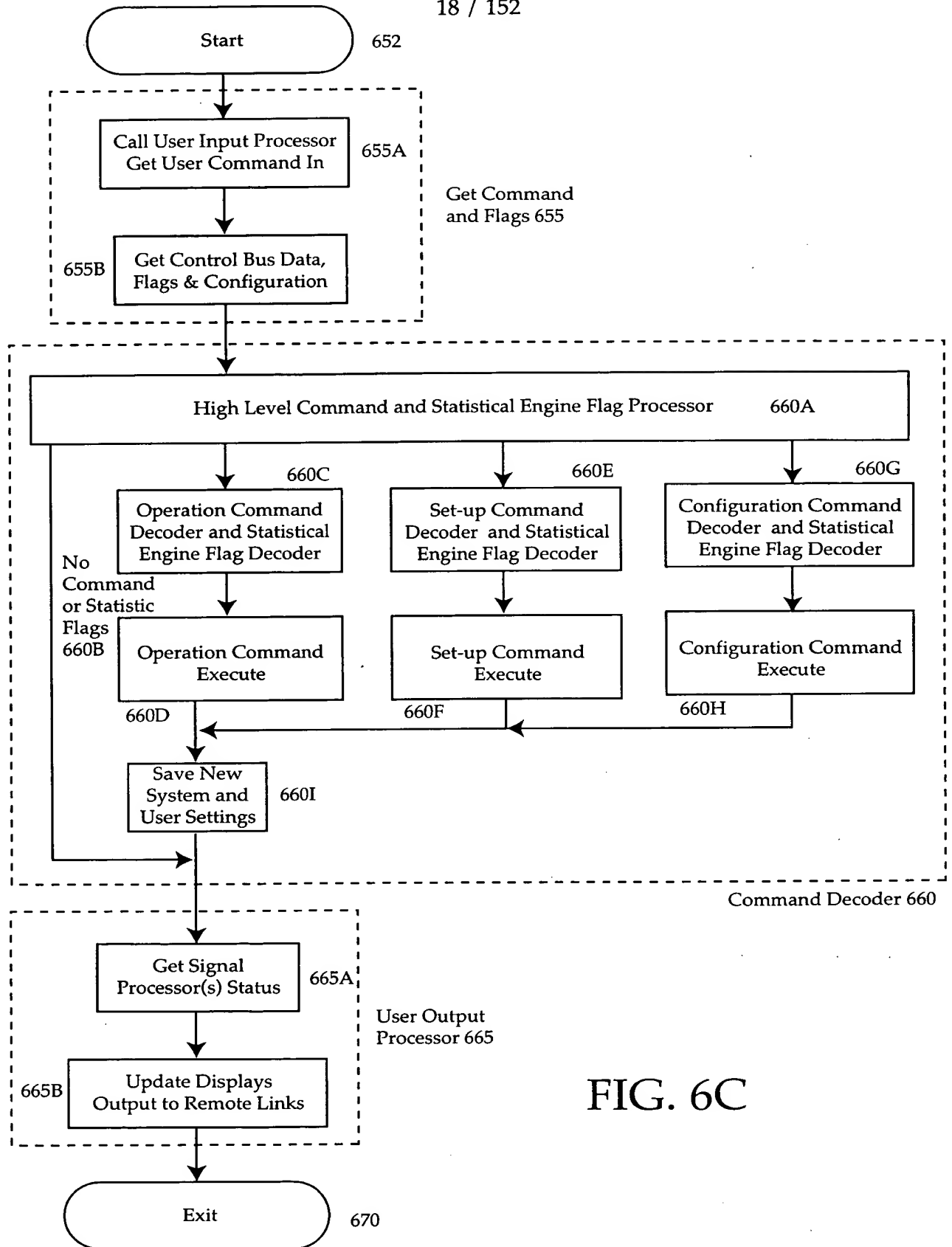


FIG. 6C

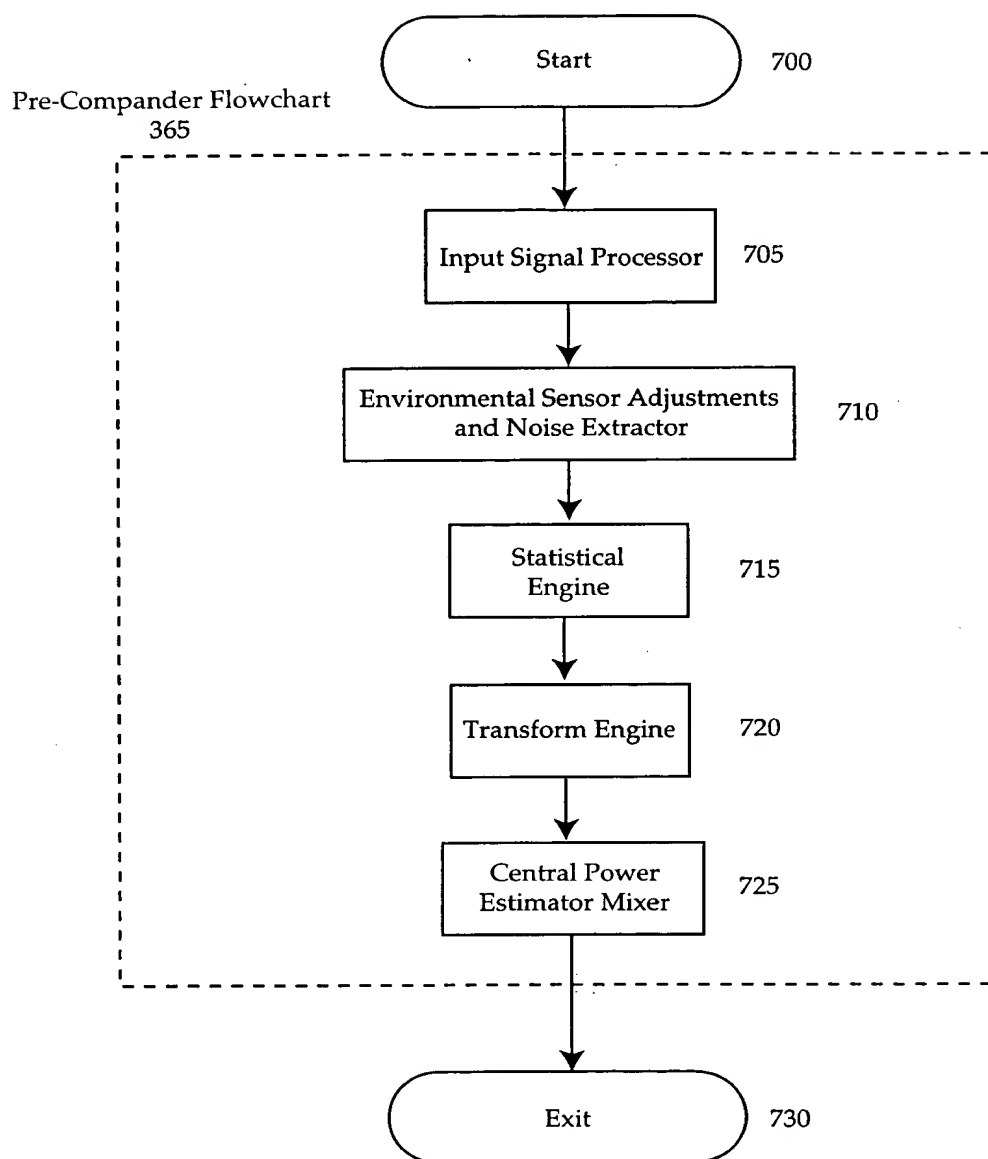


FIG. 7



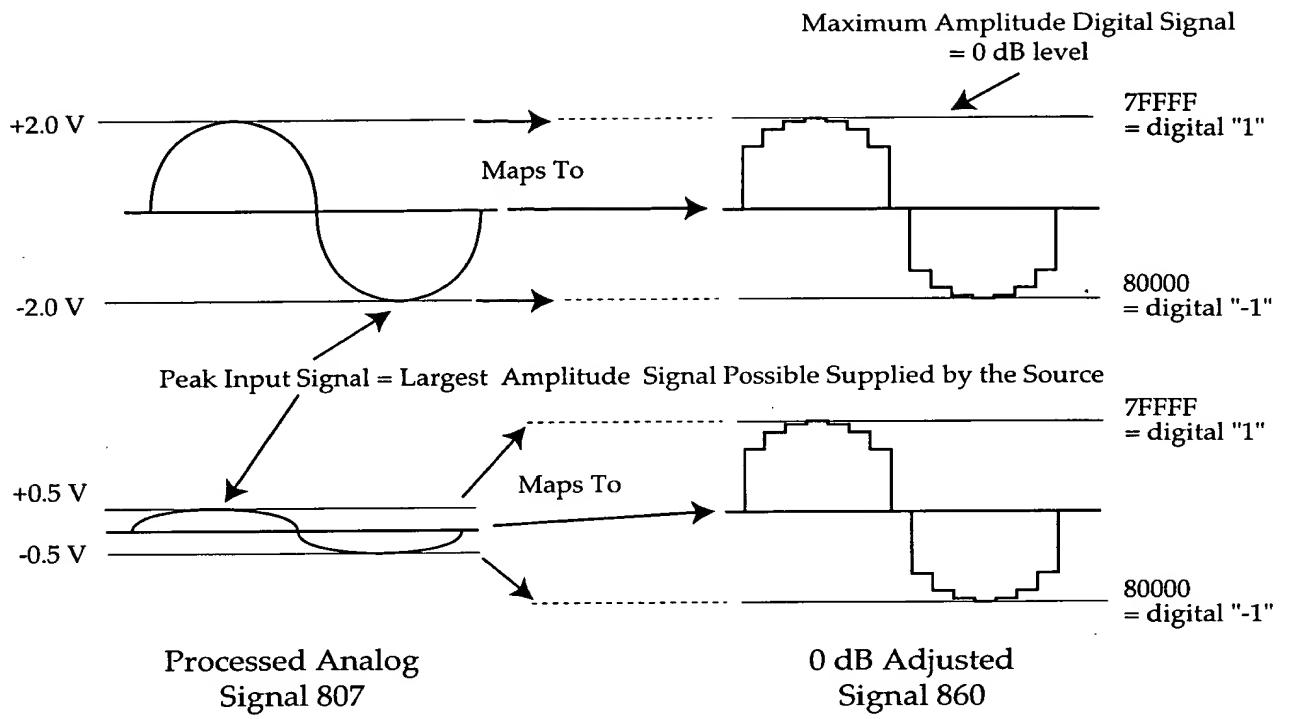


FIG. 8B

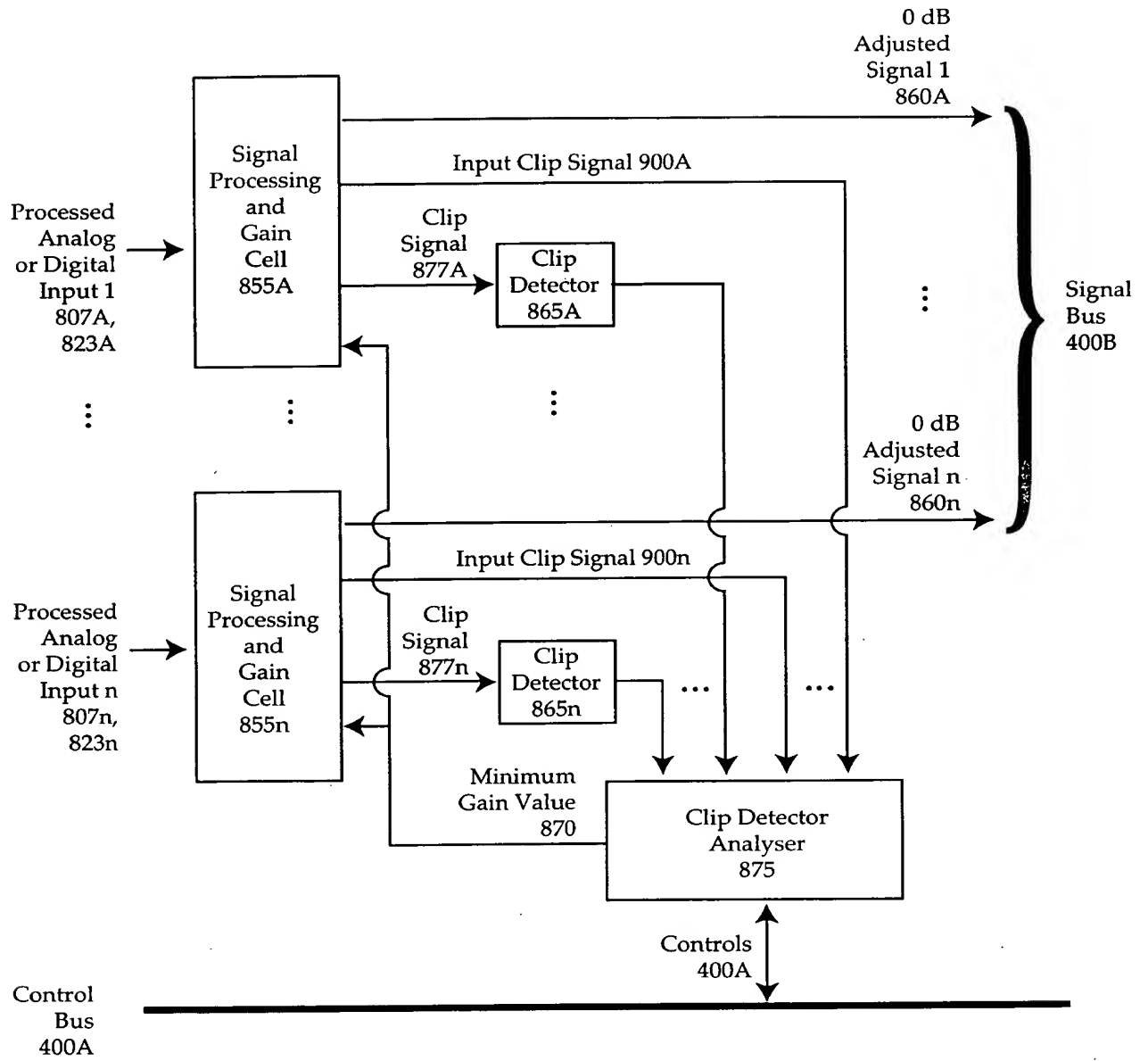


FIG. 8C

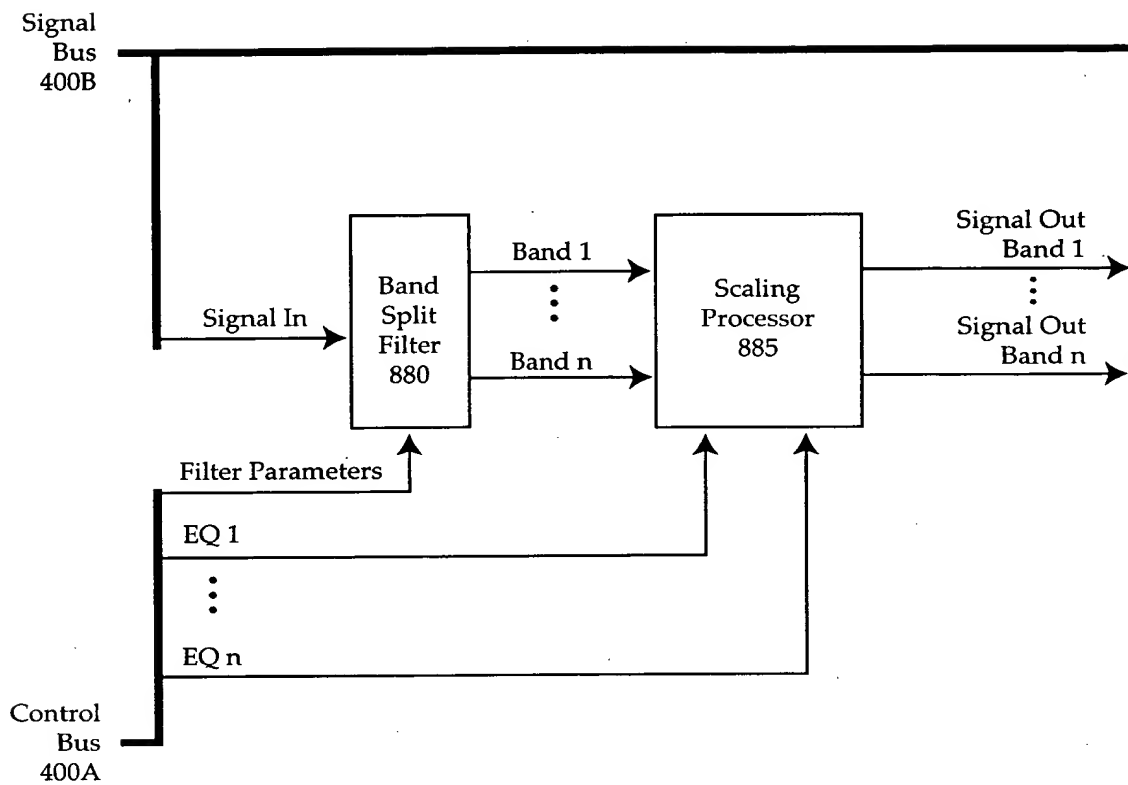


FIG. 8D

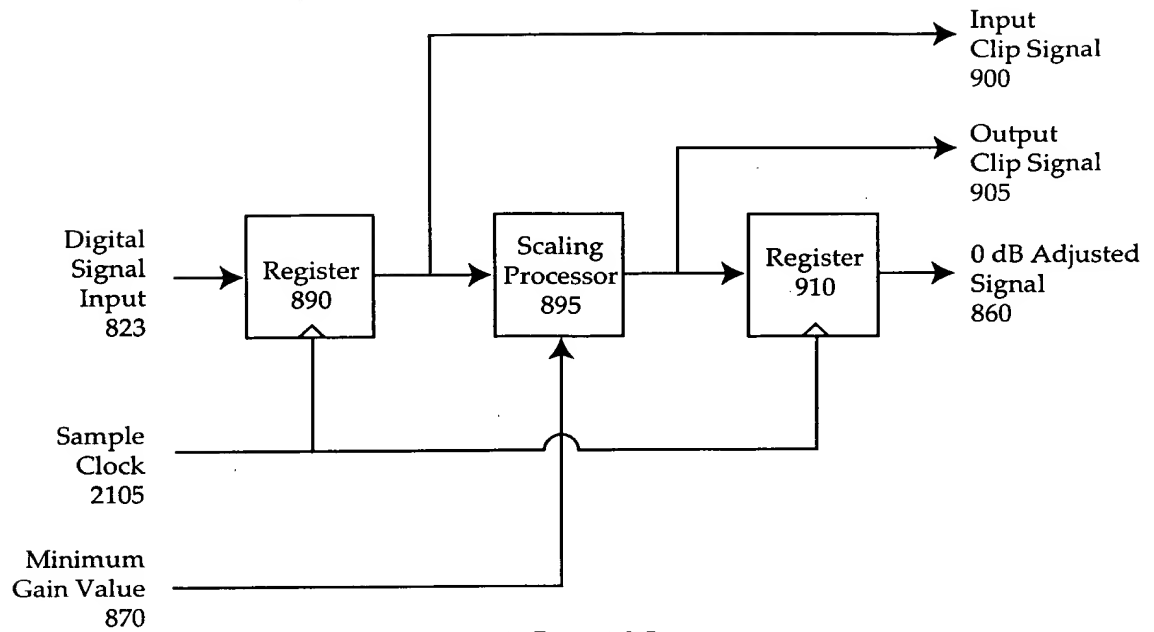


FIG. 8E

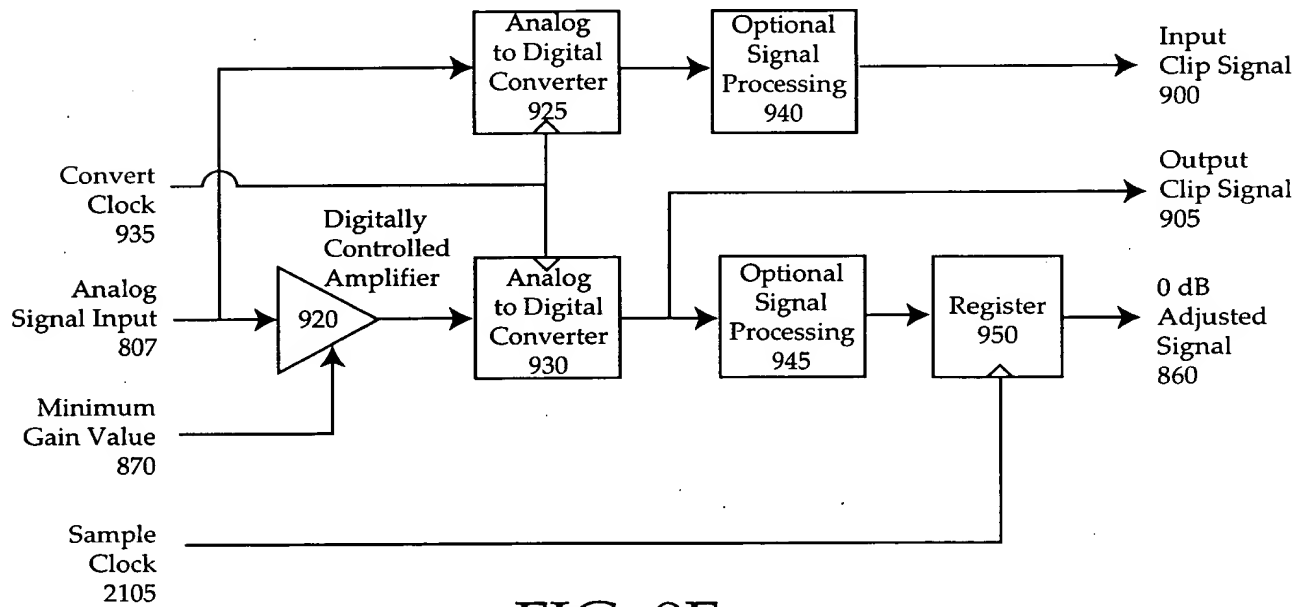


FIG. 8F



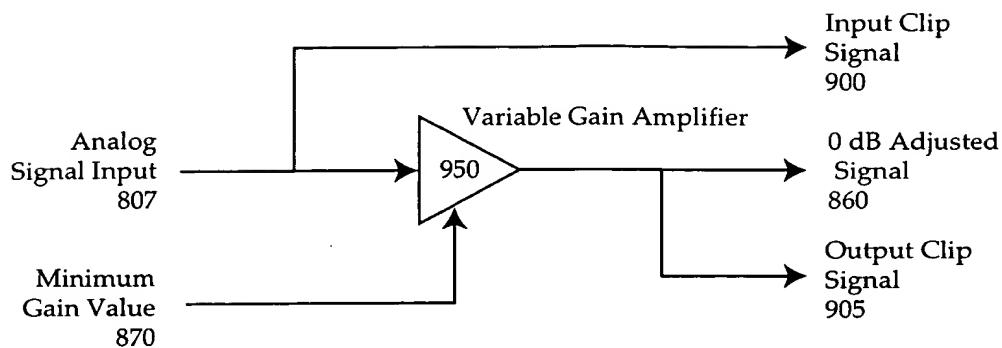


FIG. 8G

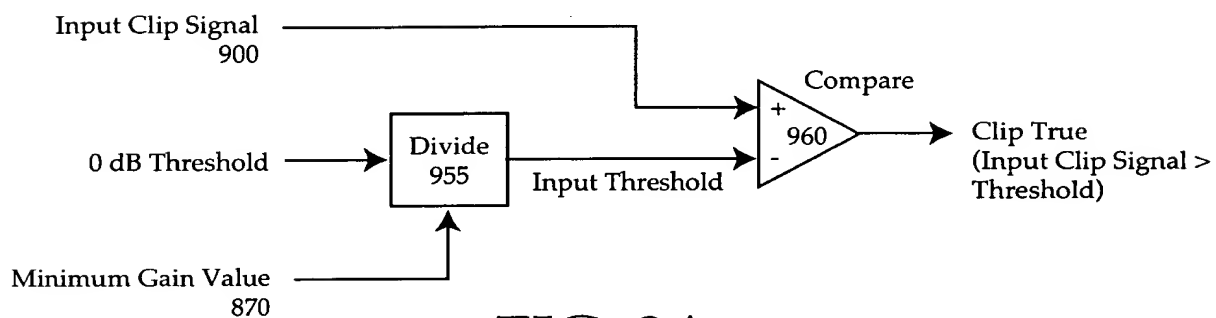


FIG. 9A

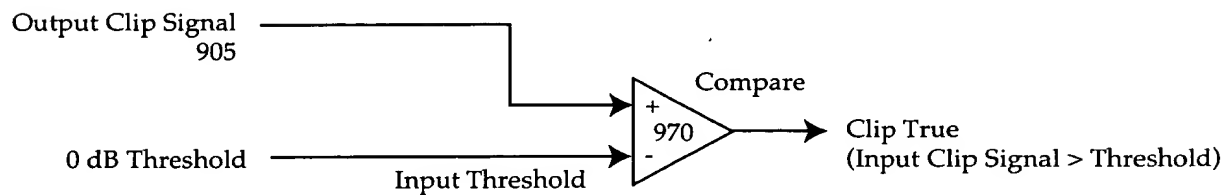


FIG. 9B

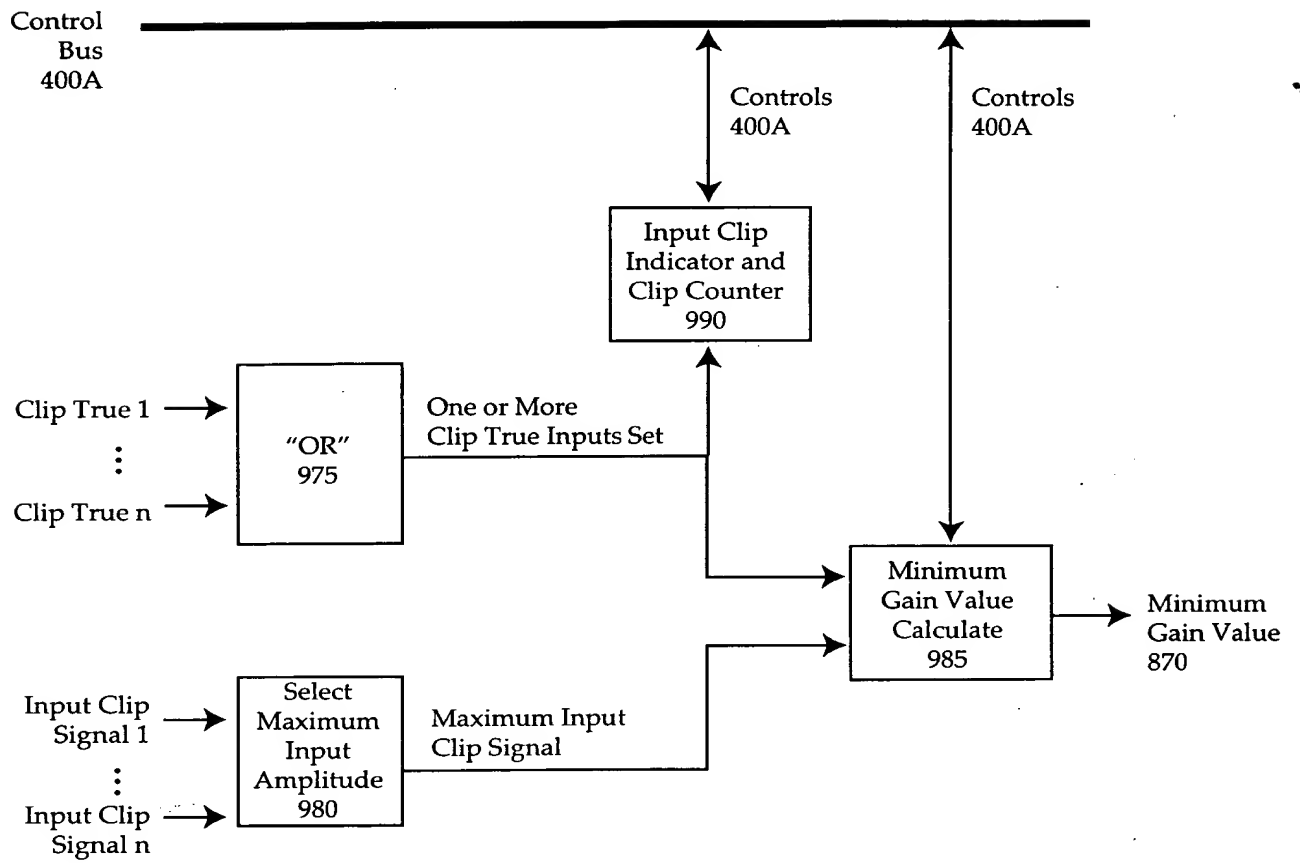
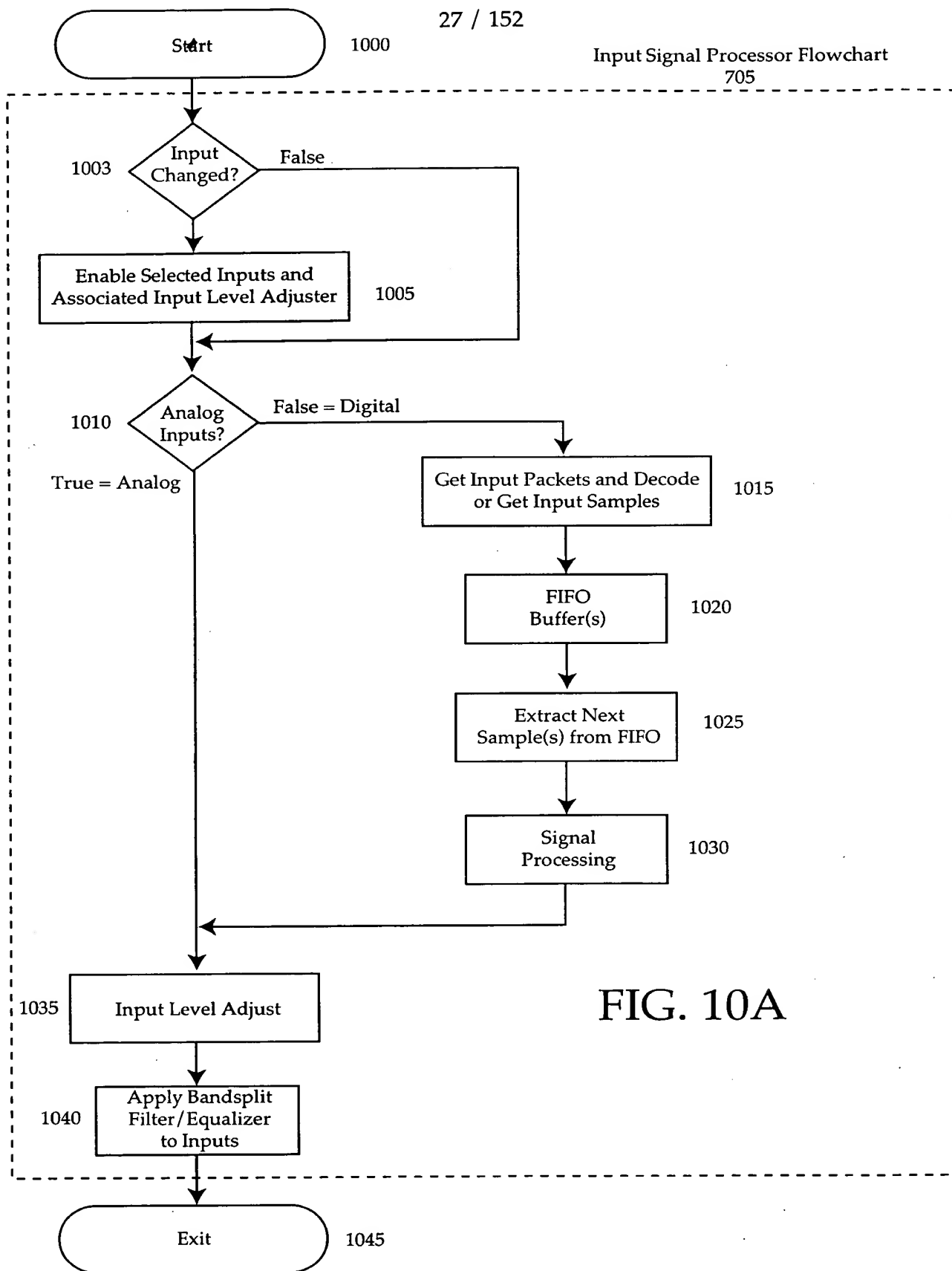
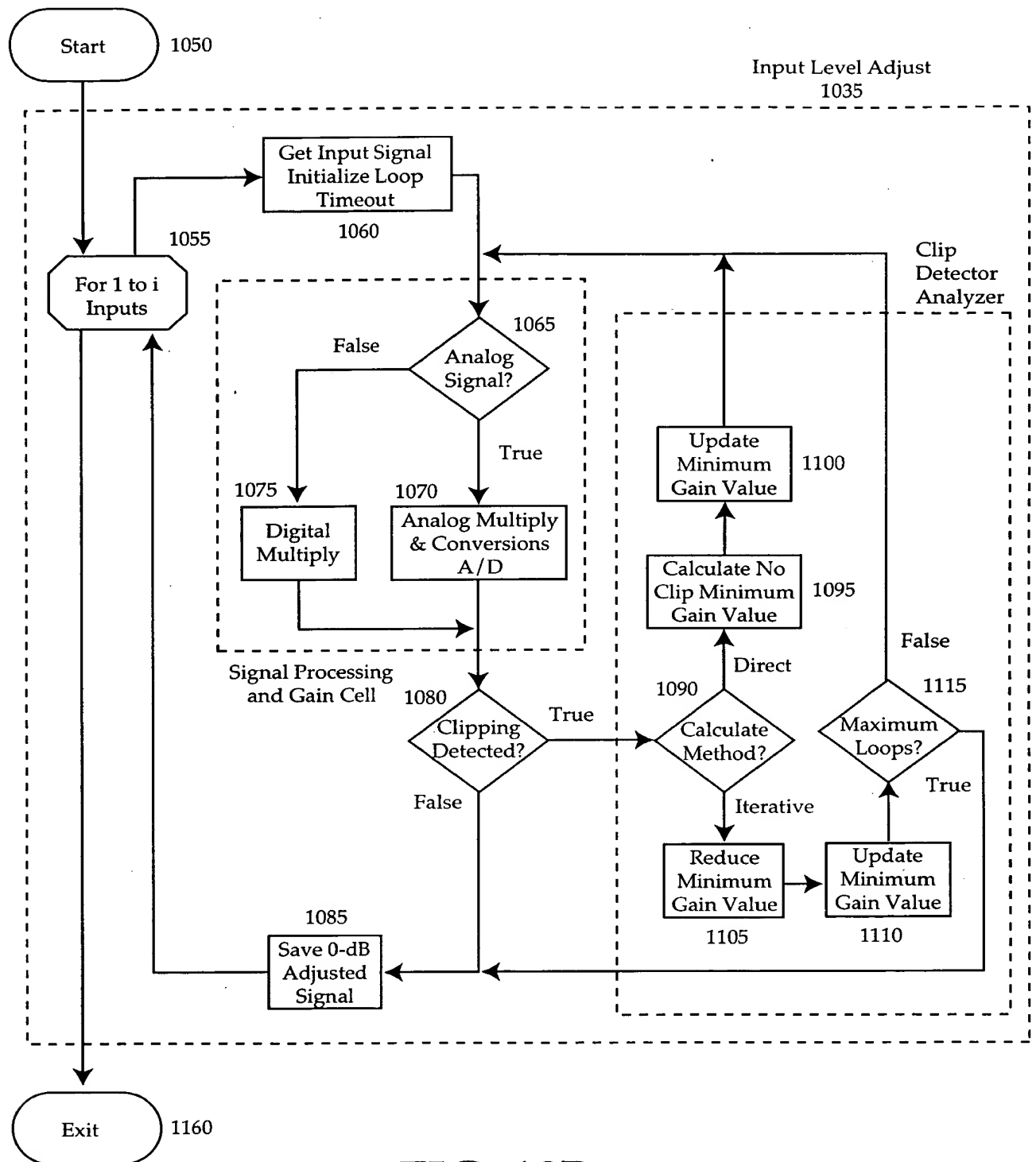


FIG. 9C





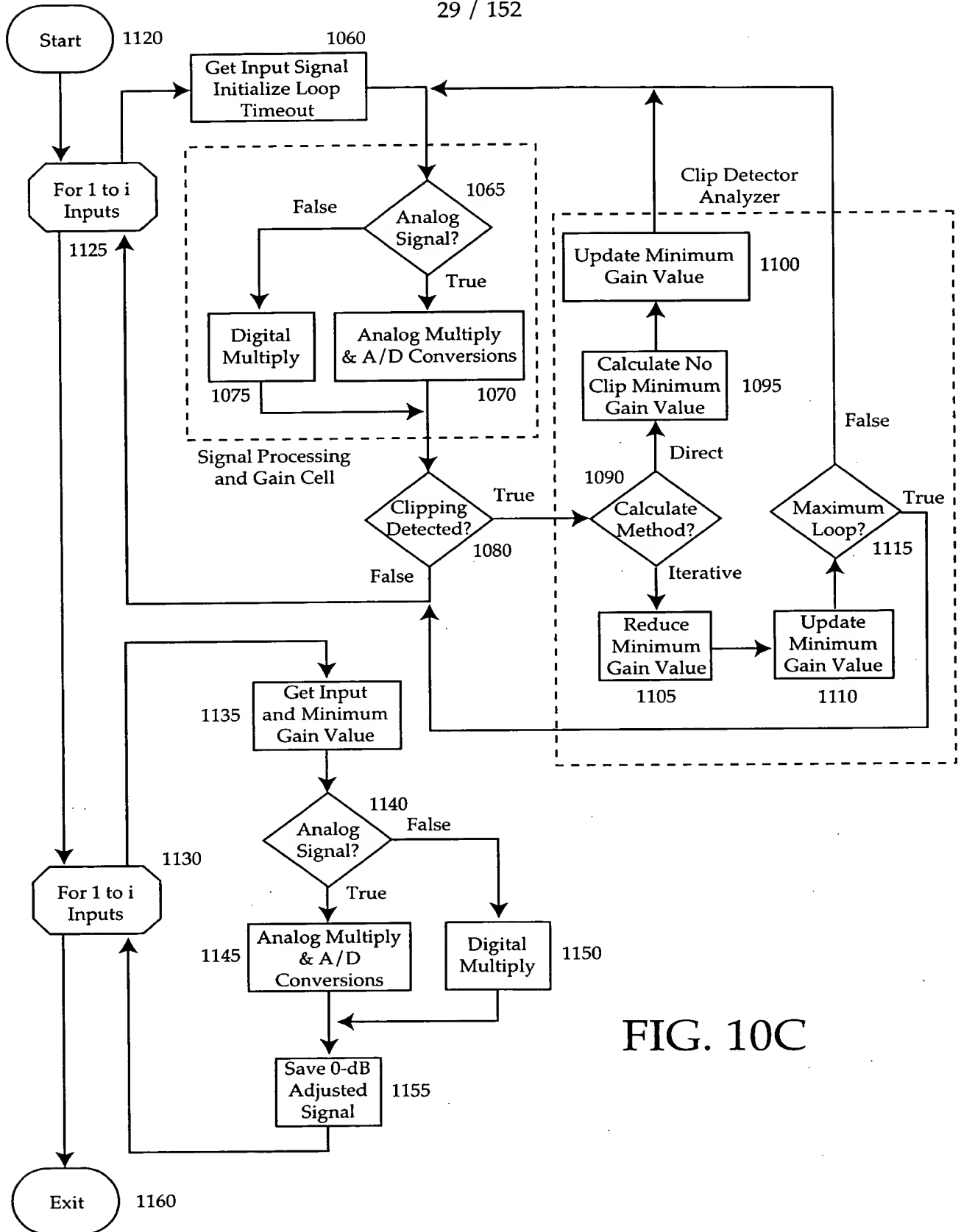
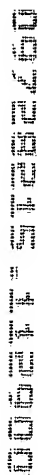


FIG. 10C



1

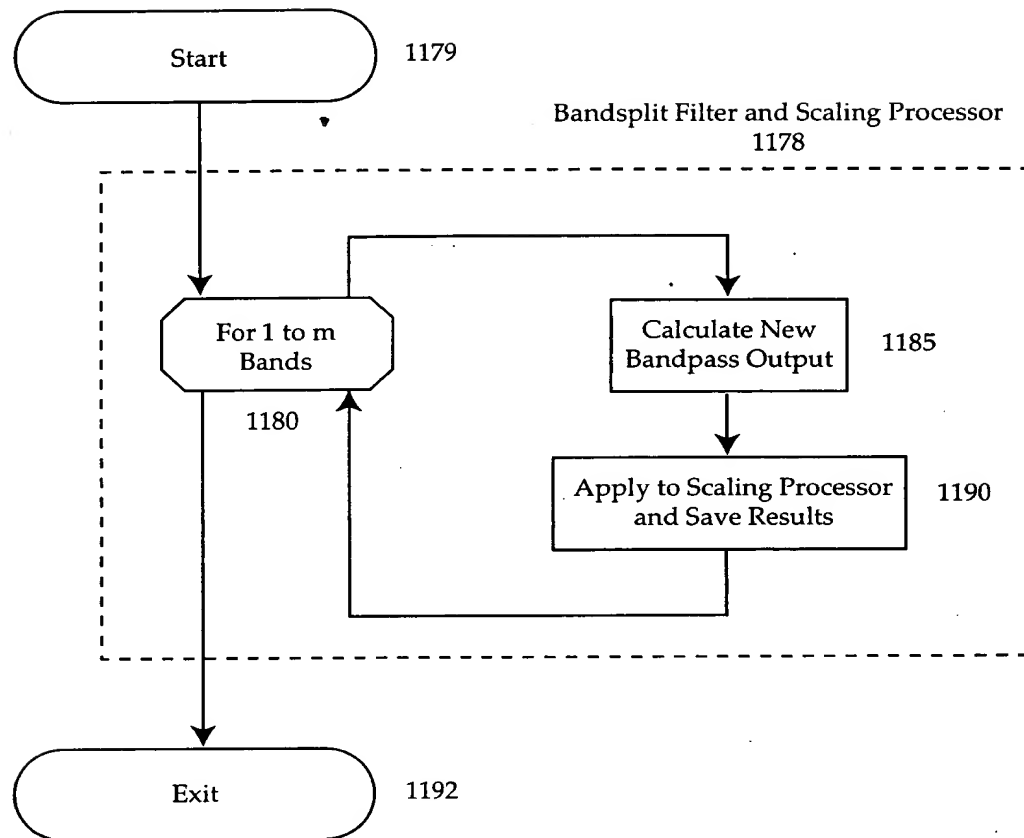


FIG. 10E

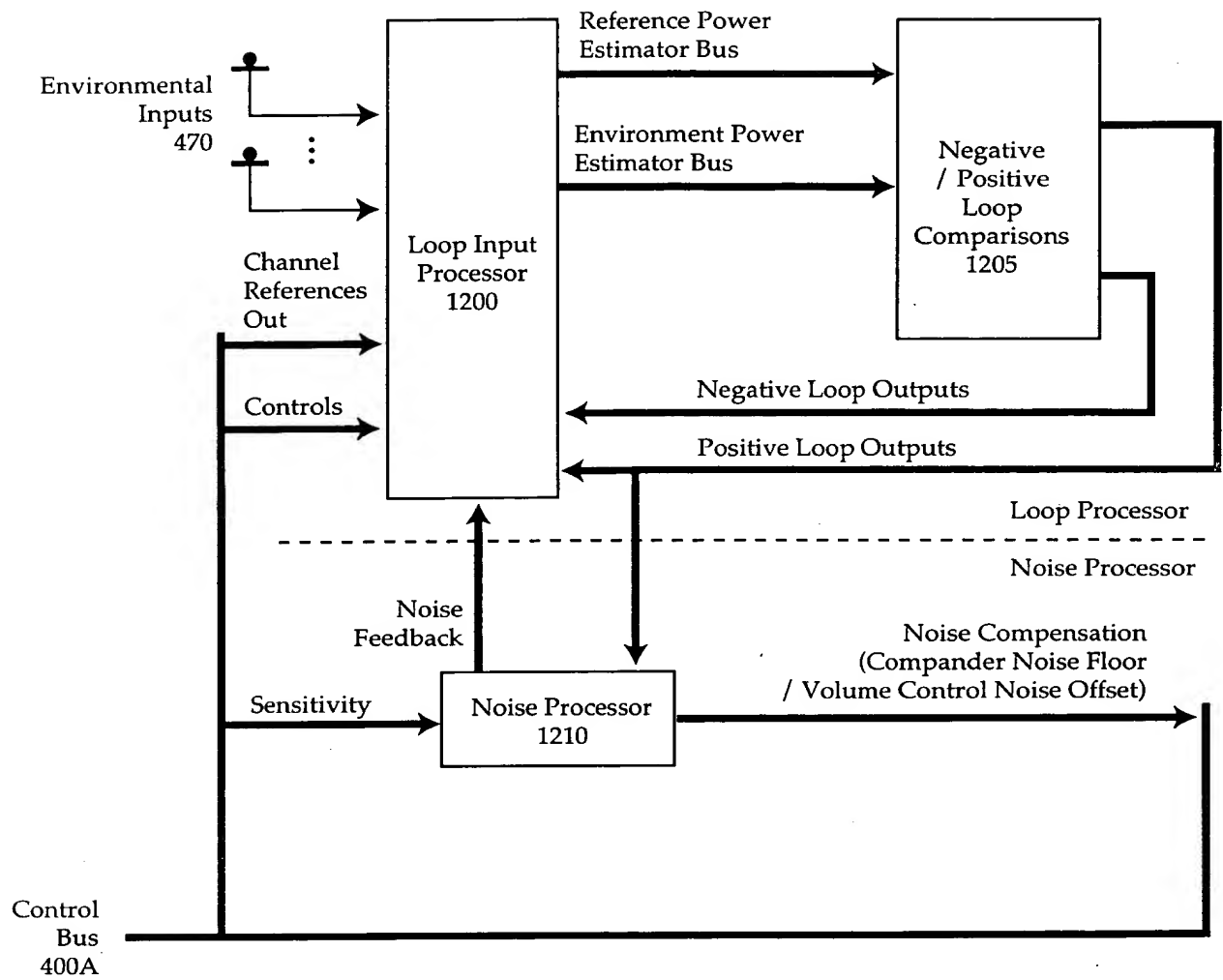


FIG. 11



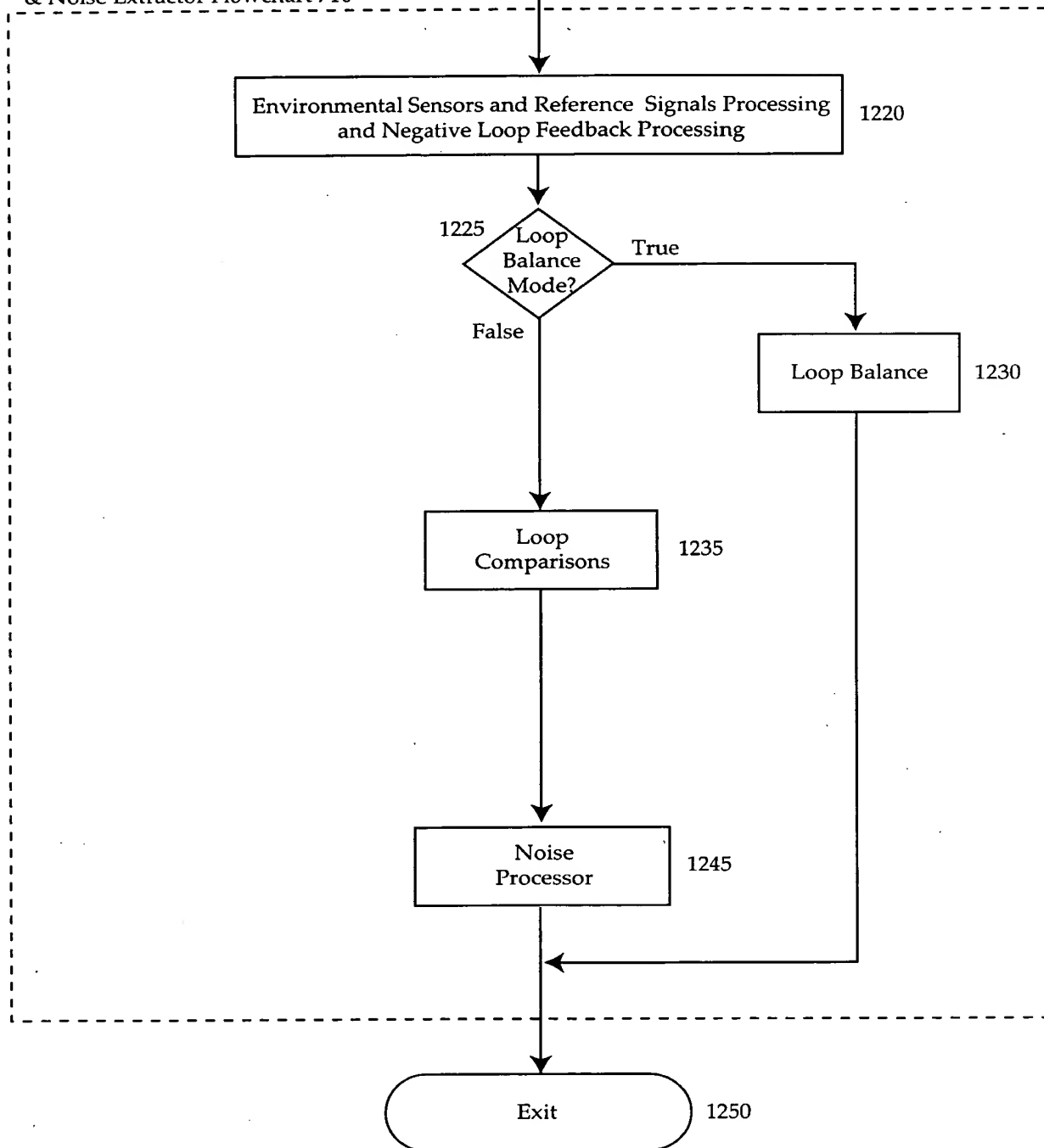
Environmental Sensor Adjustments  
& Noise Extractor Flowchart 710

FIG. 12

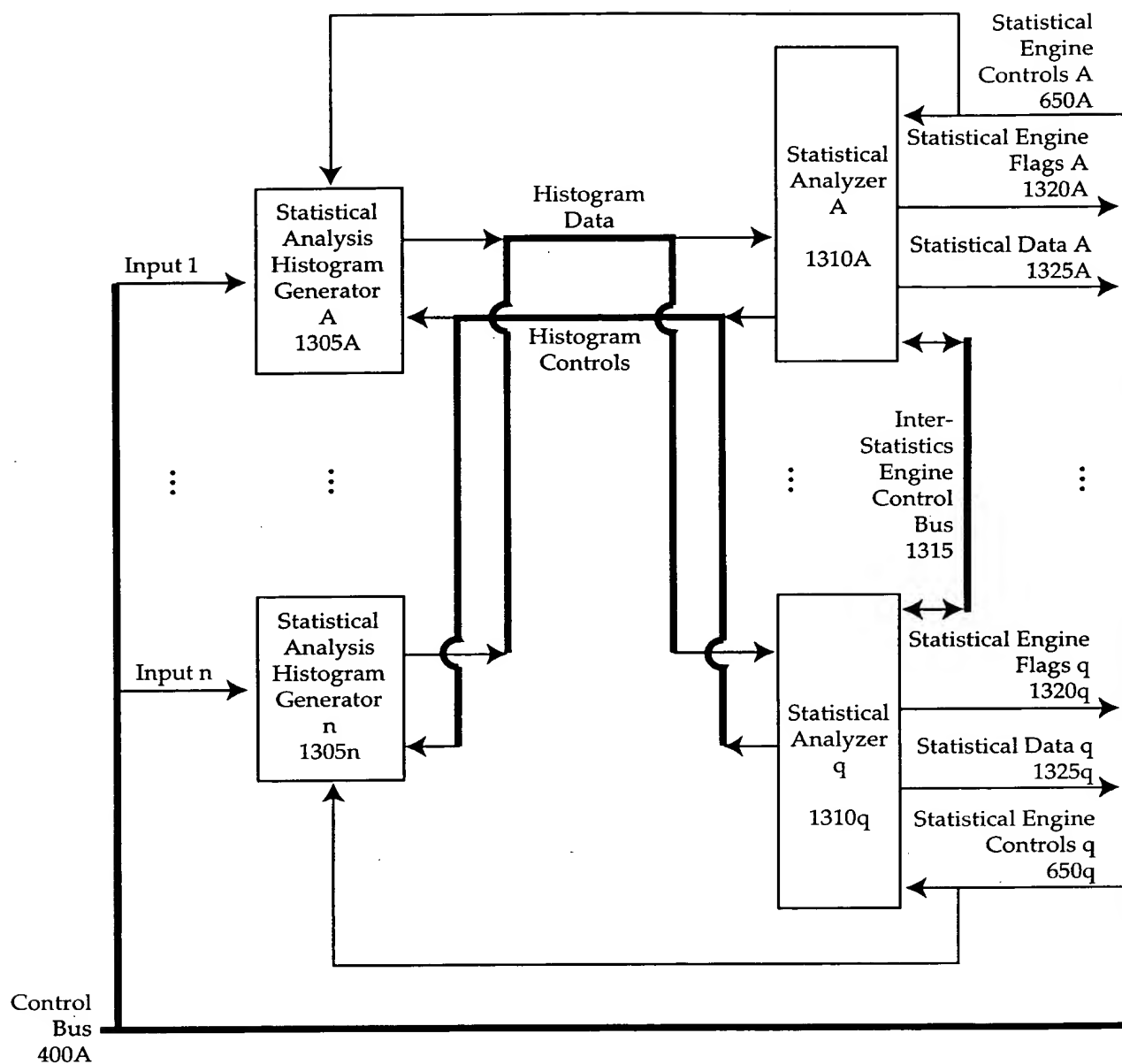


FIG. 13

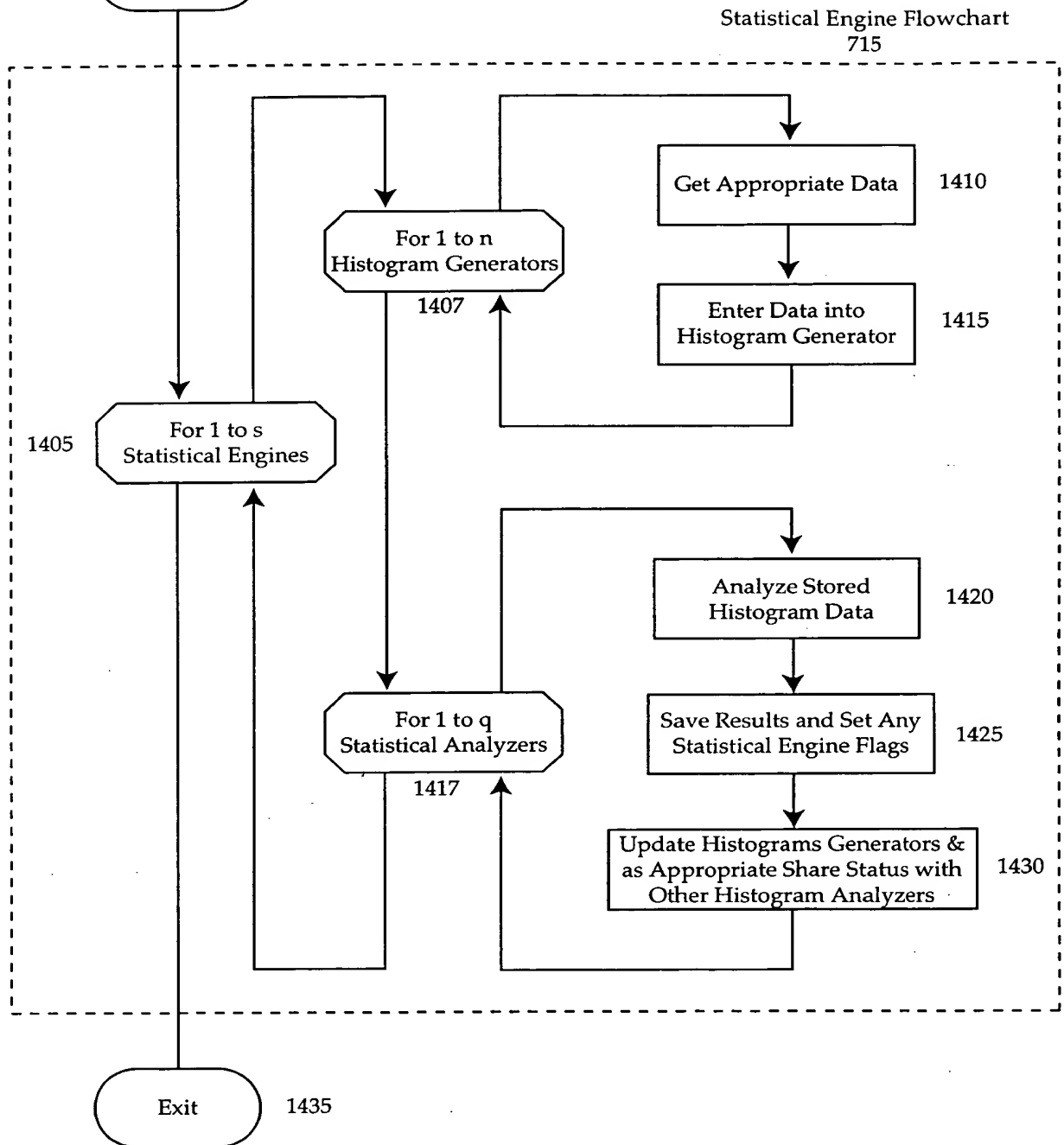


FIG. 14

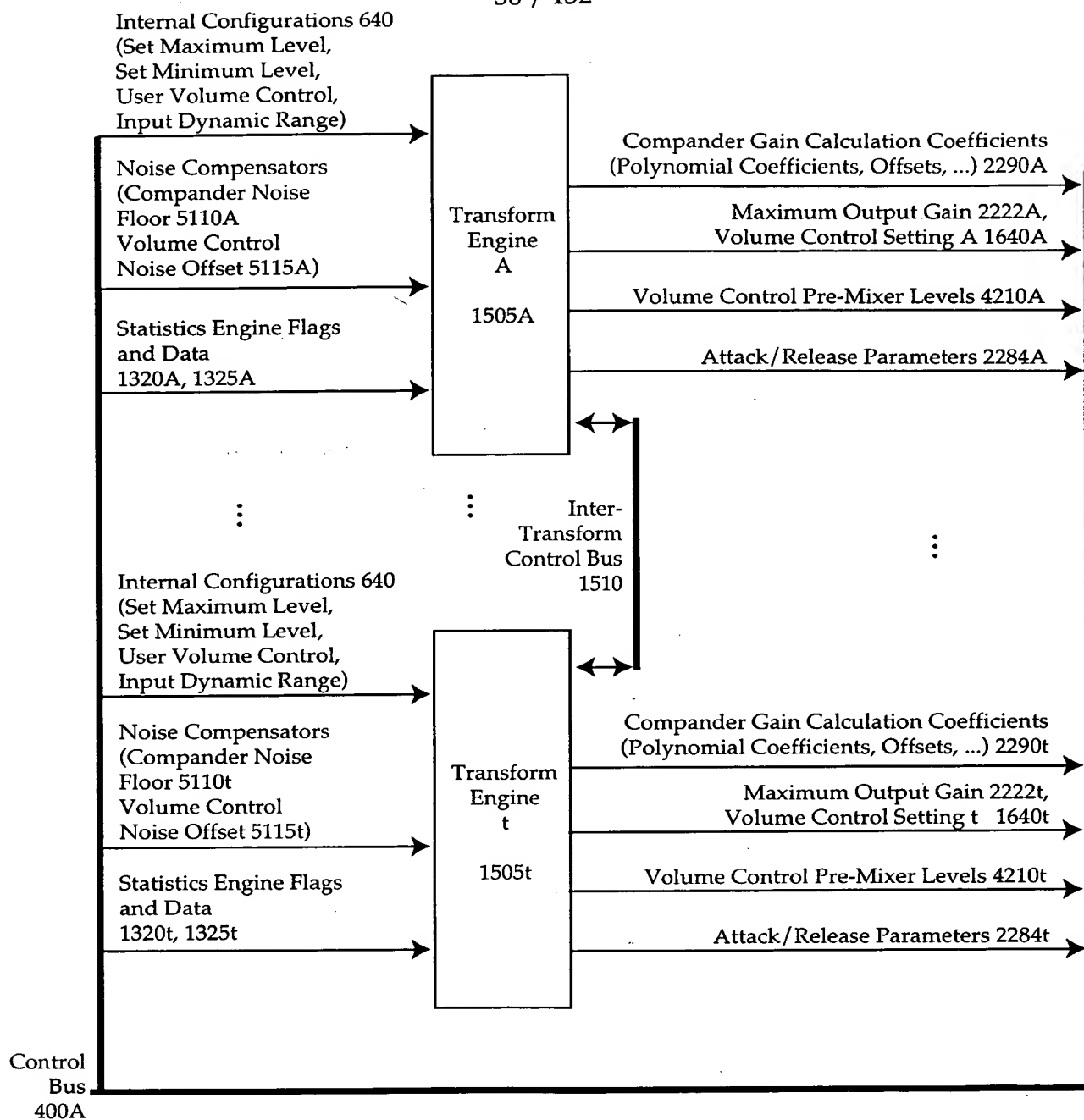


FIG. 15

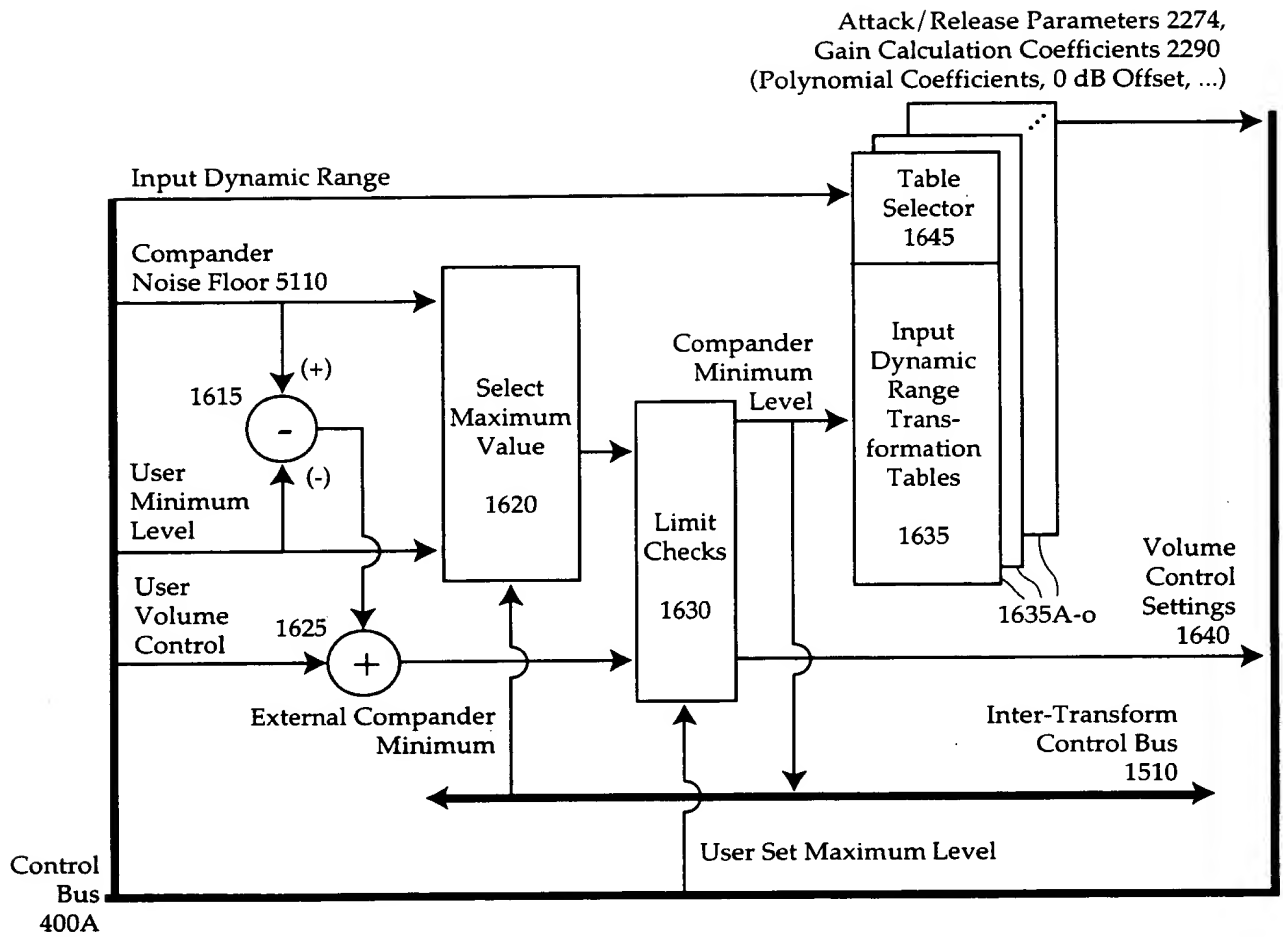


FIG. 16

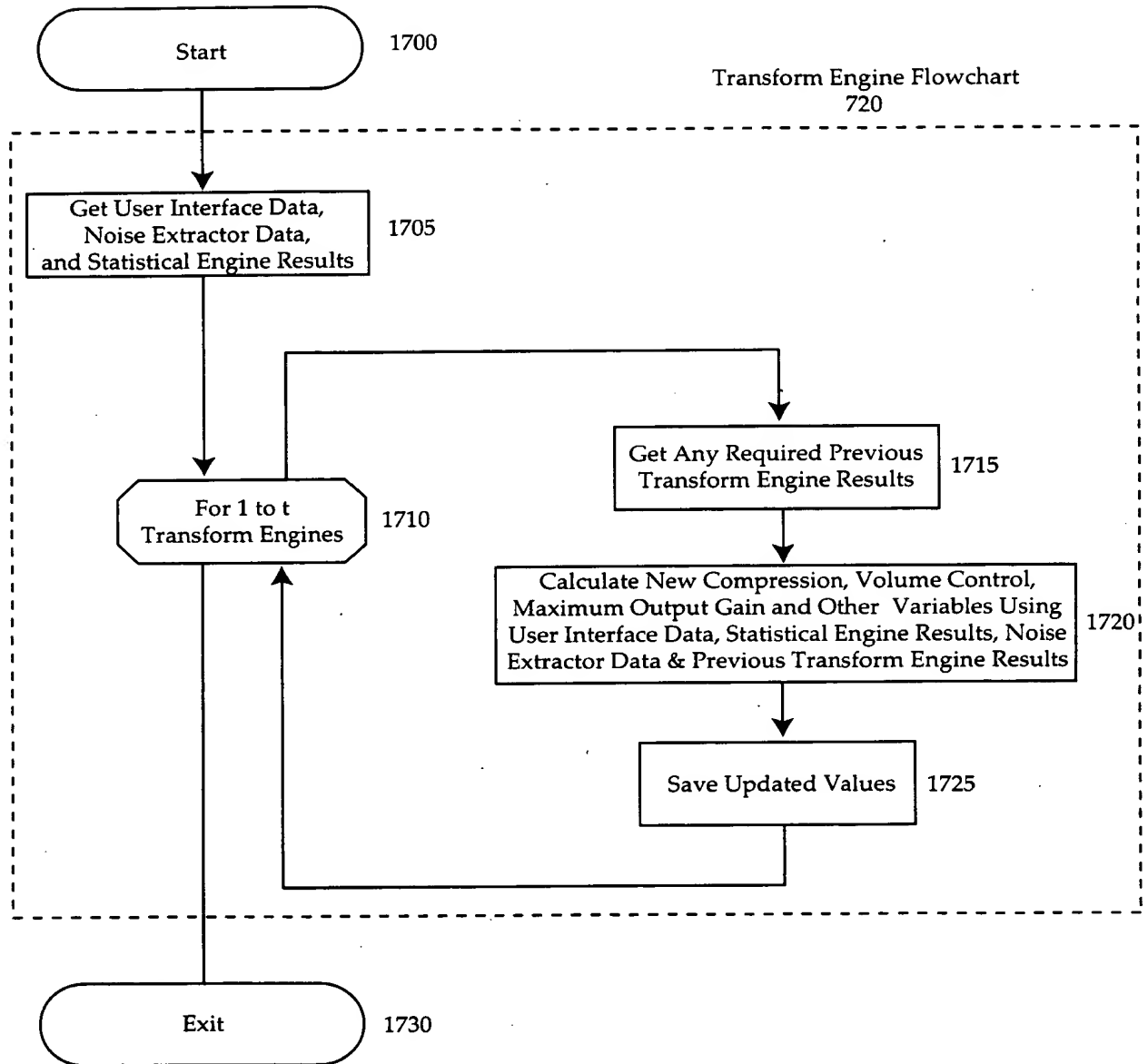


FIG. 17

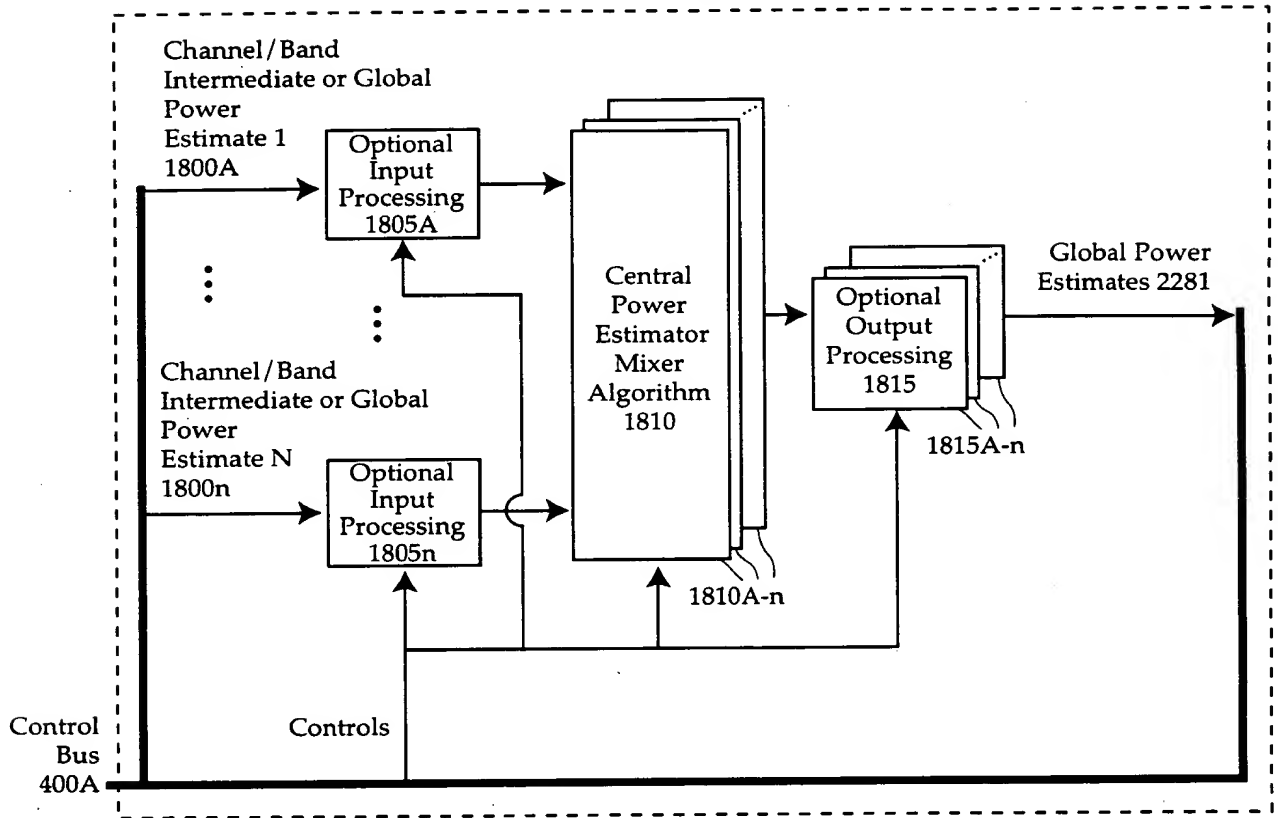
Central Power Estimator Mixer Combiner  
455

FIG. 18

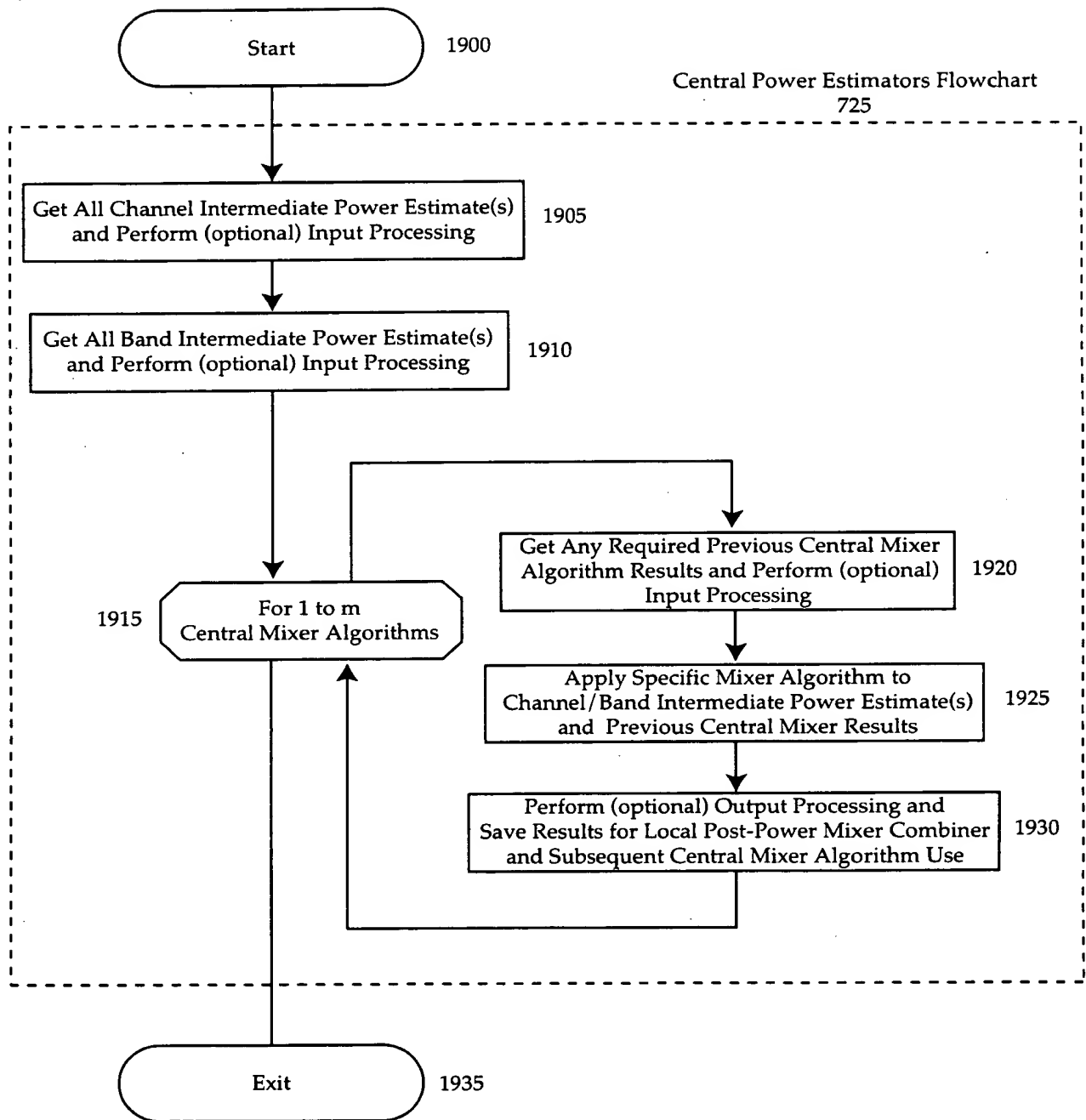
Central Power Estimators Flowchart  
725

FIG. 19



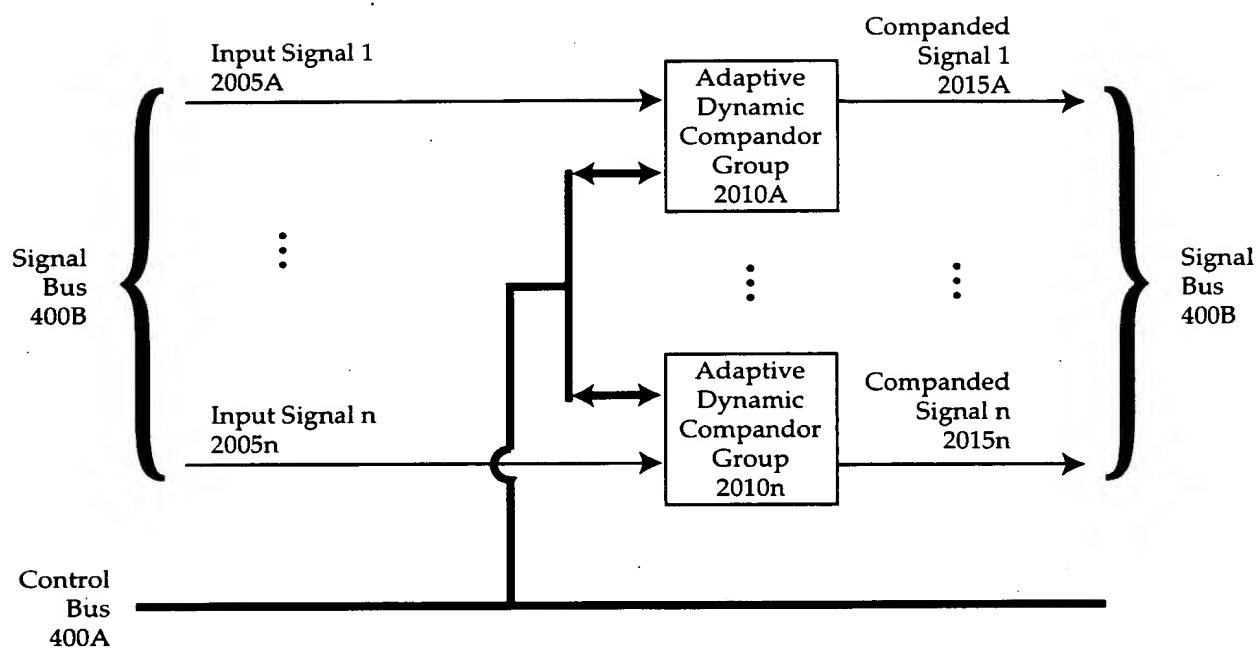


FIG. 20A

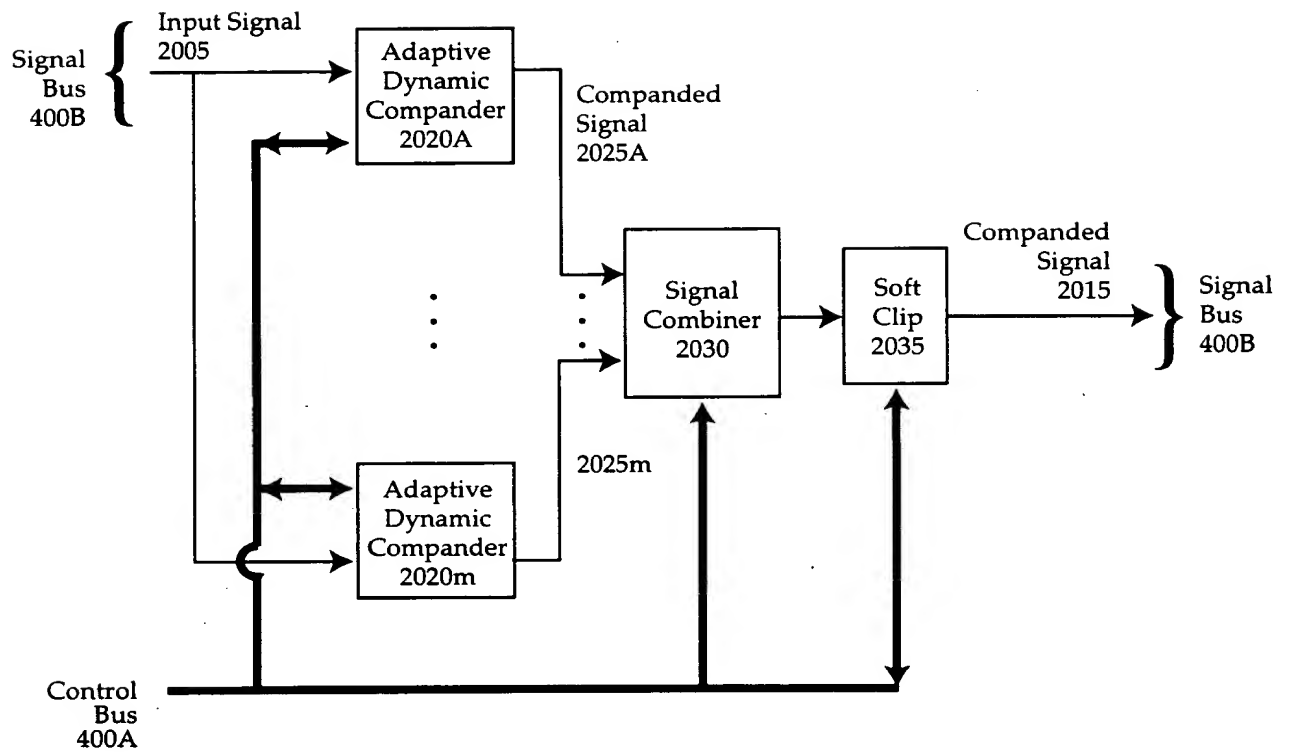


FIG. 20B

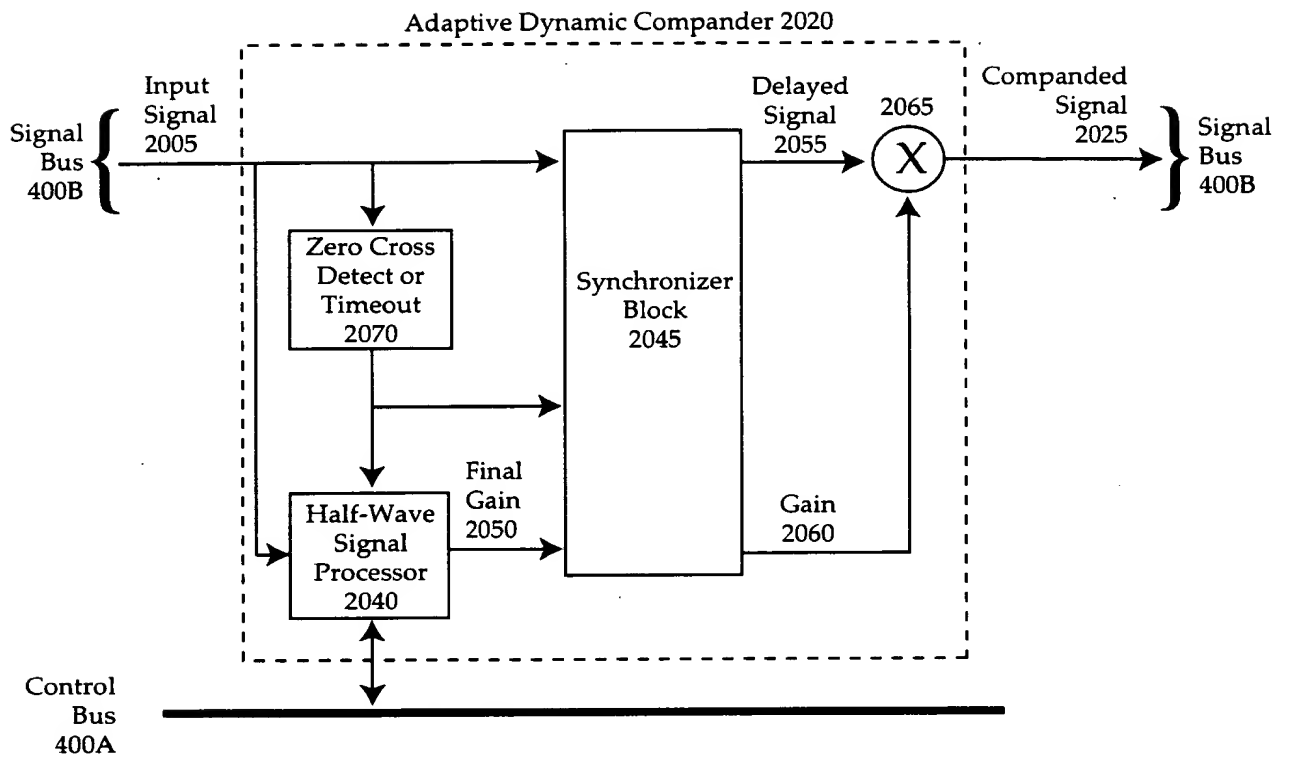


FIG. 20C

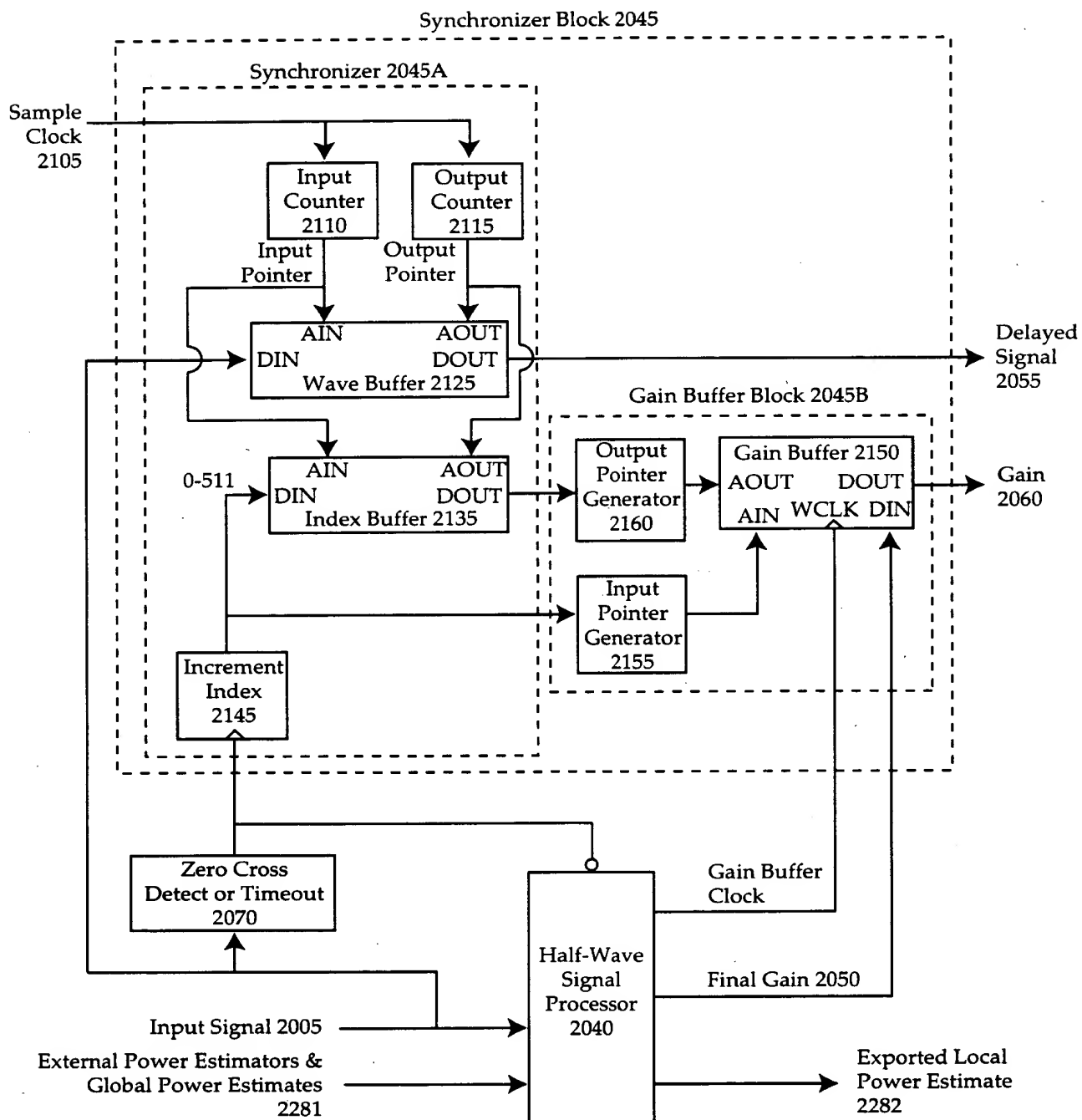
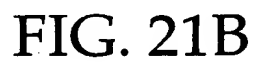
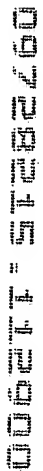


FIG. 21A





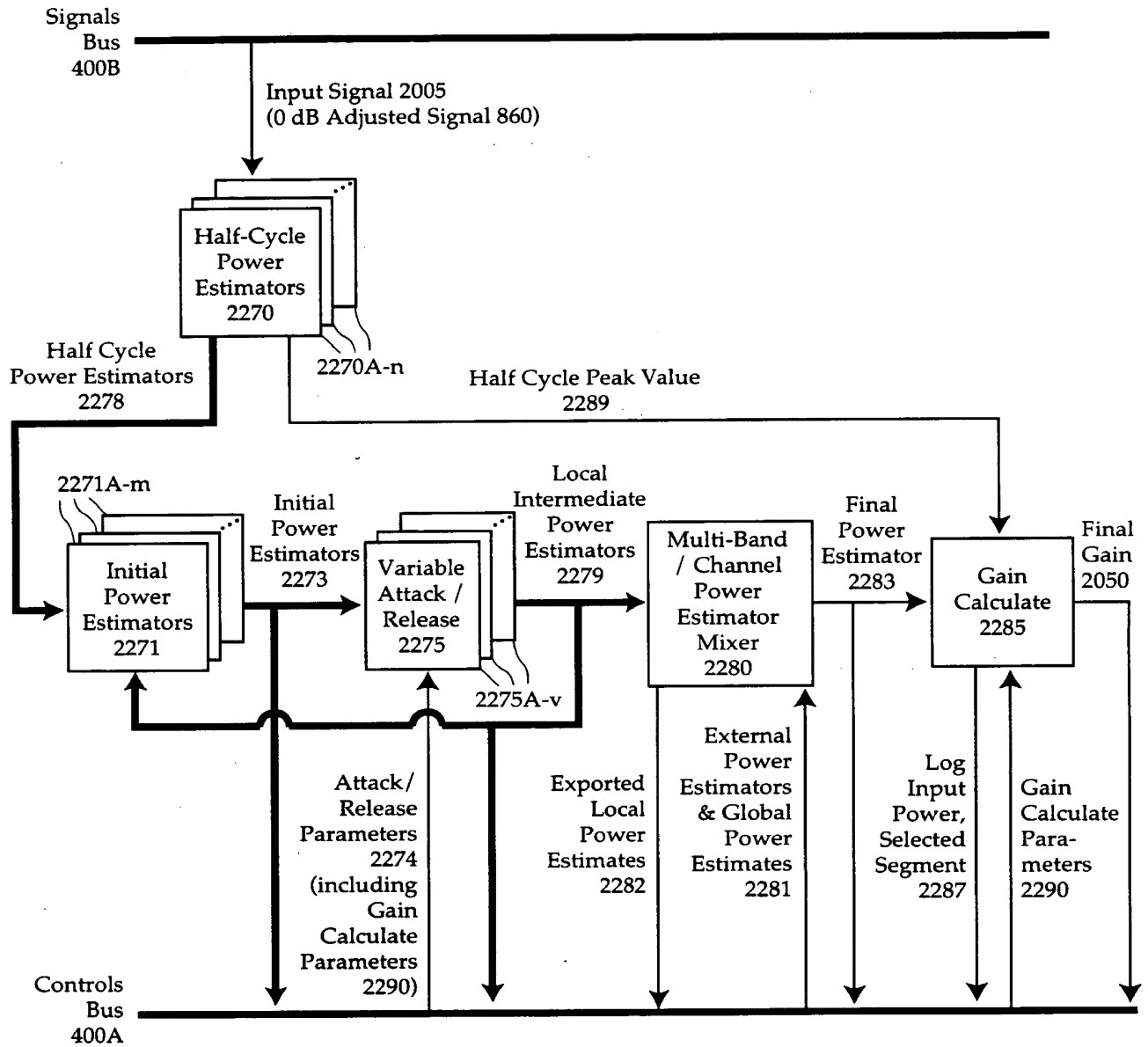


FIG. 22

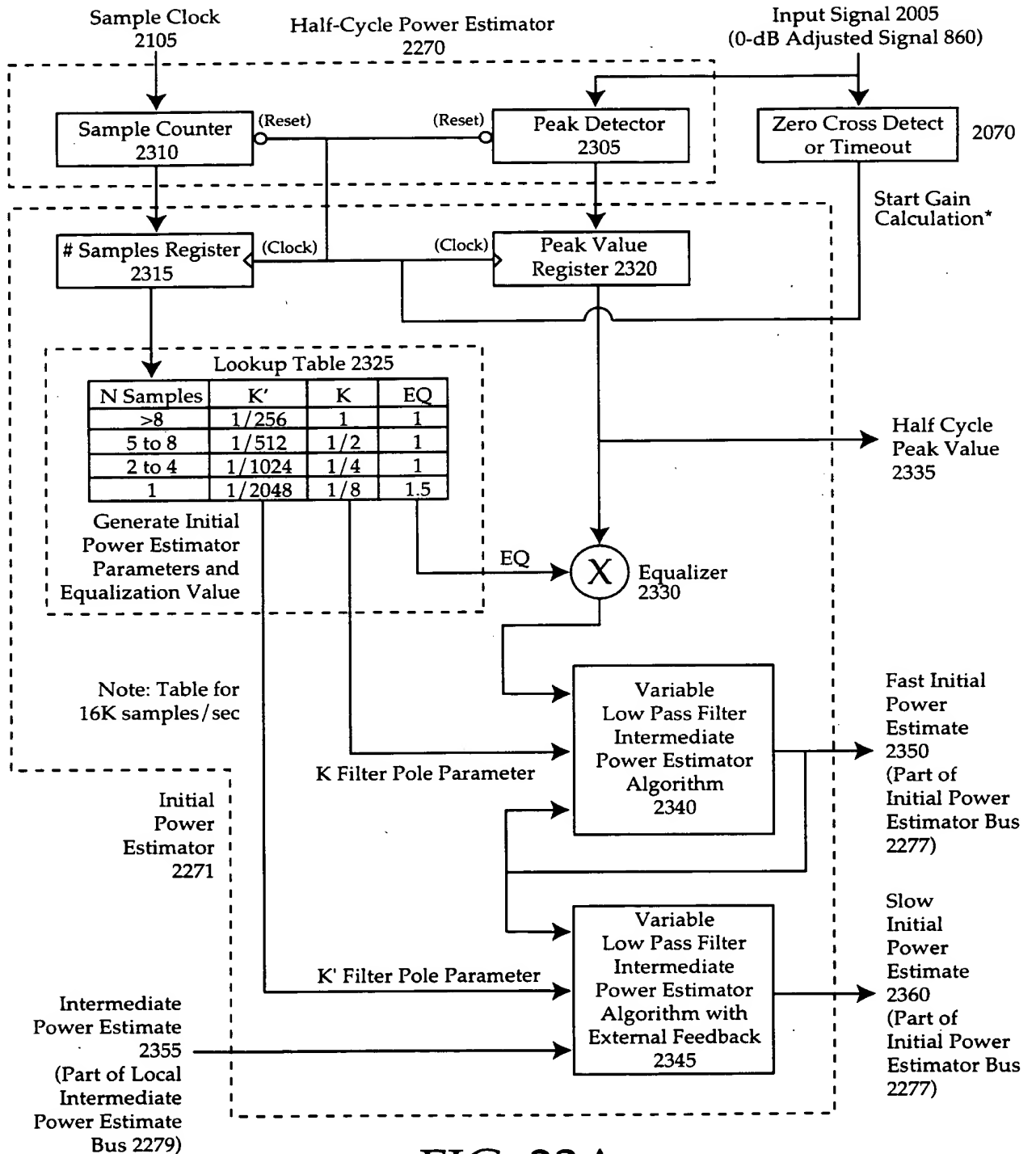


FIG. 23A



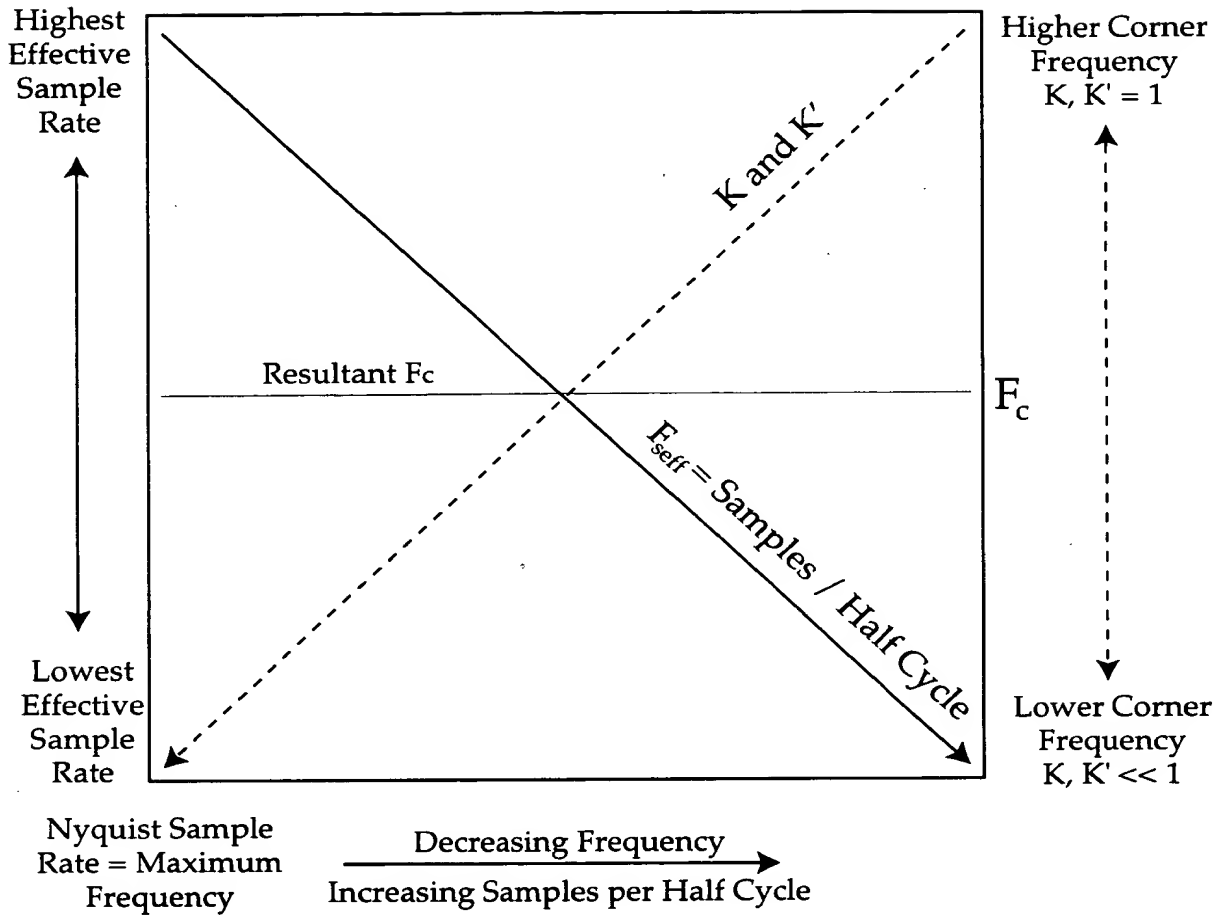


FIG. 23B

1

FIG. 23C

[illegible]

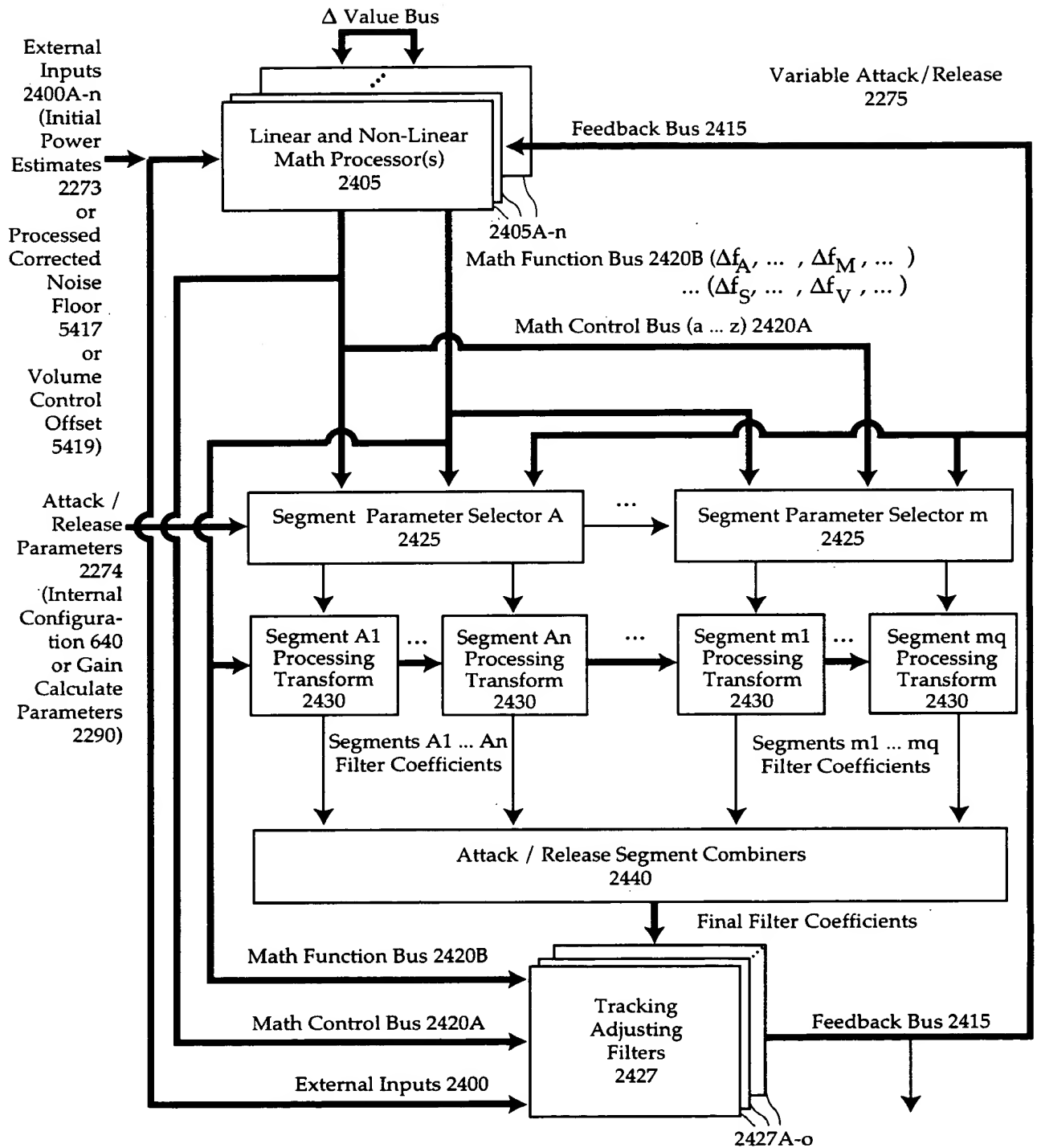


FIG. 24A

Local Intermediate Power Estimates 2279  
or Long Duration Noise Floor 5425  
or Long Duration Noise Offset 5427

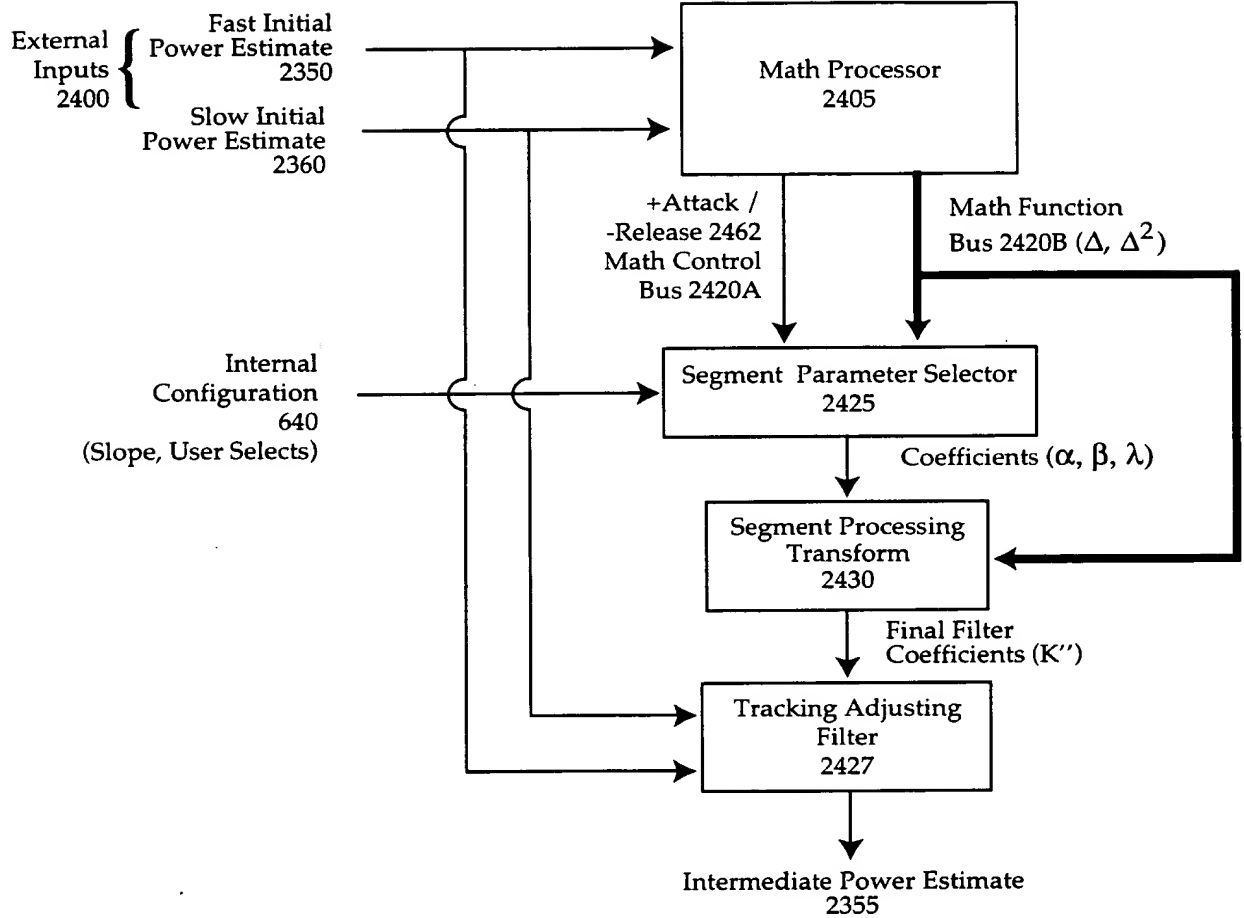
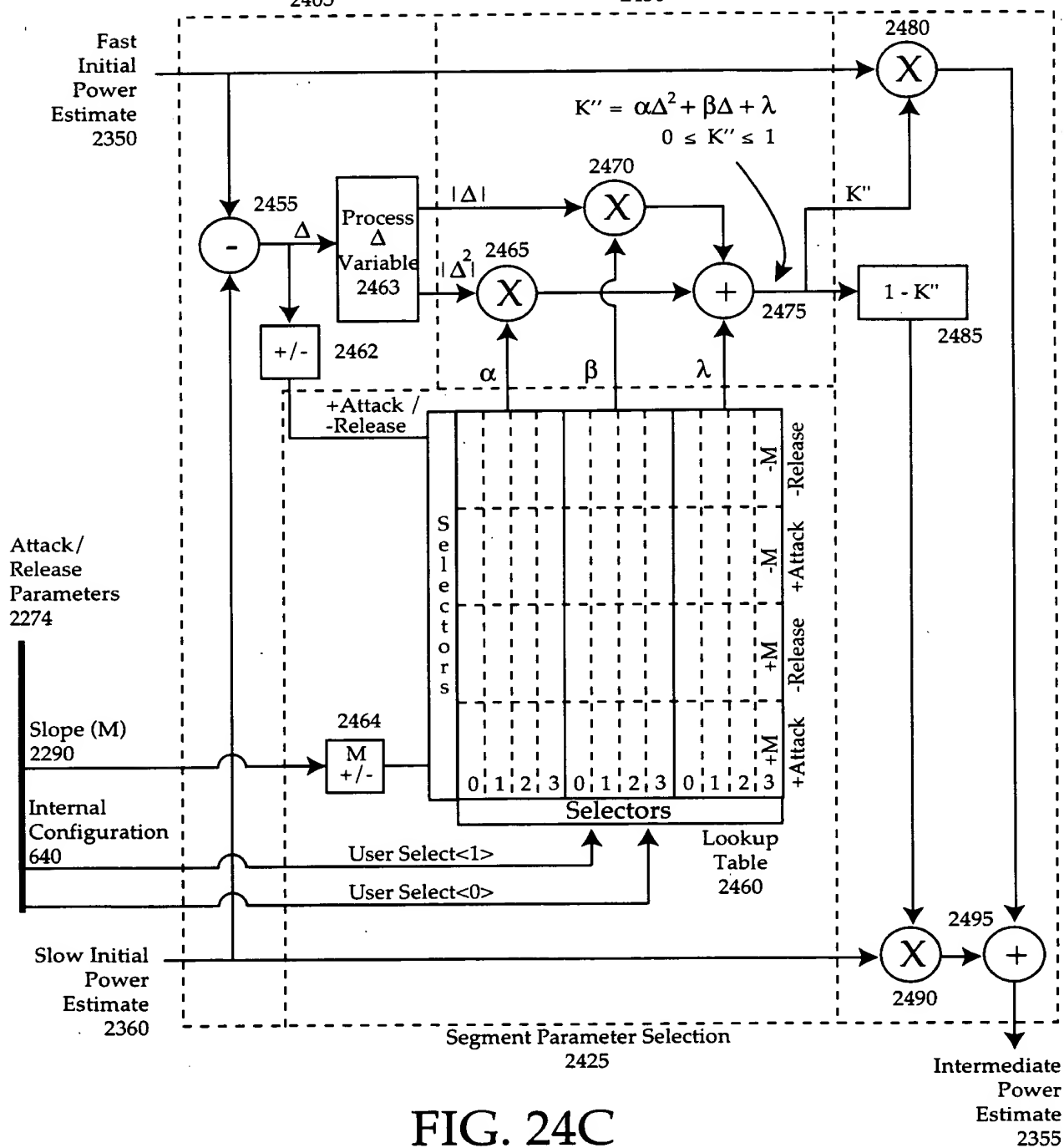


FIG. 24B



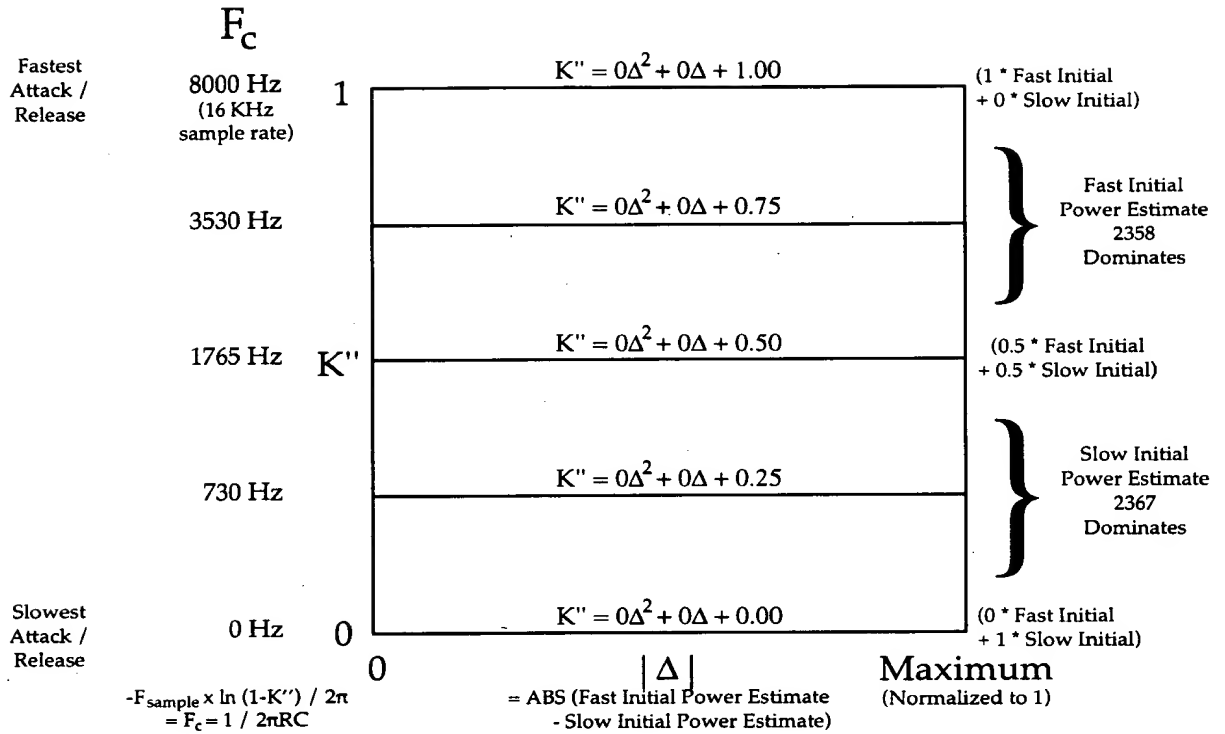


FIG. 24D

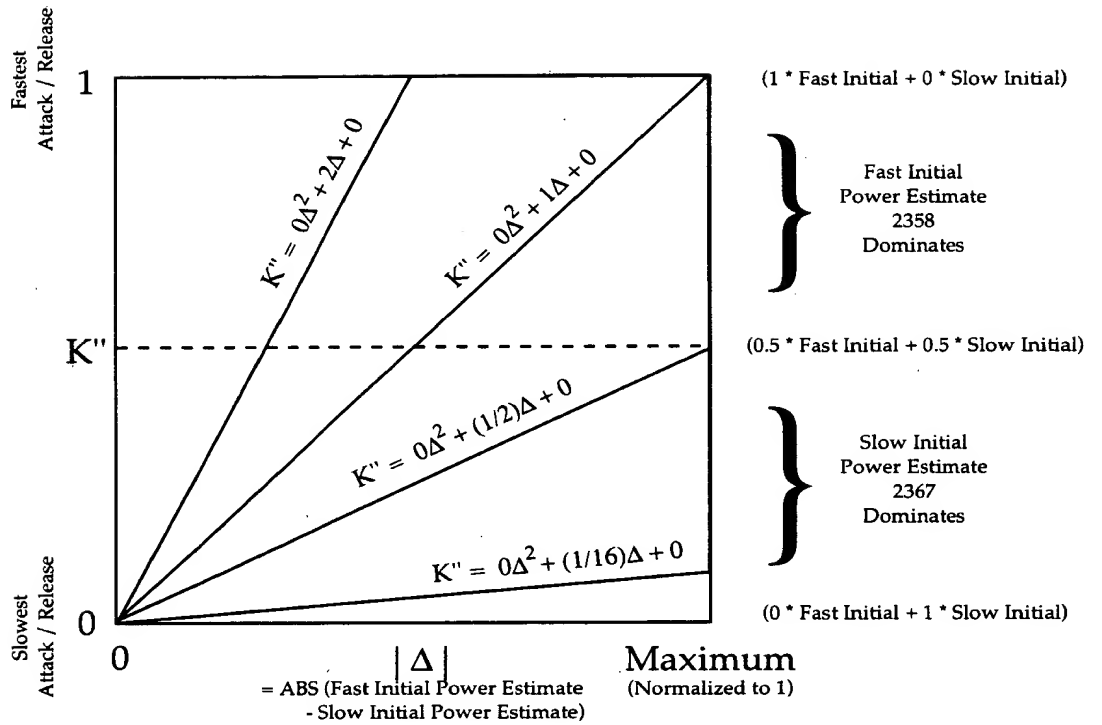


FIG. 24E

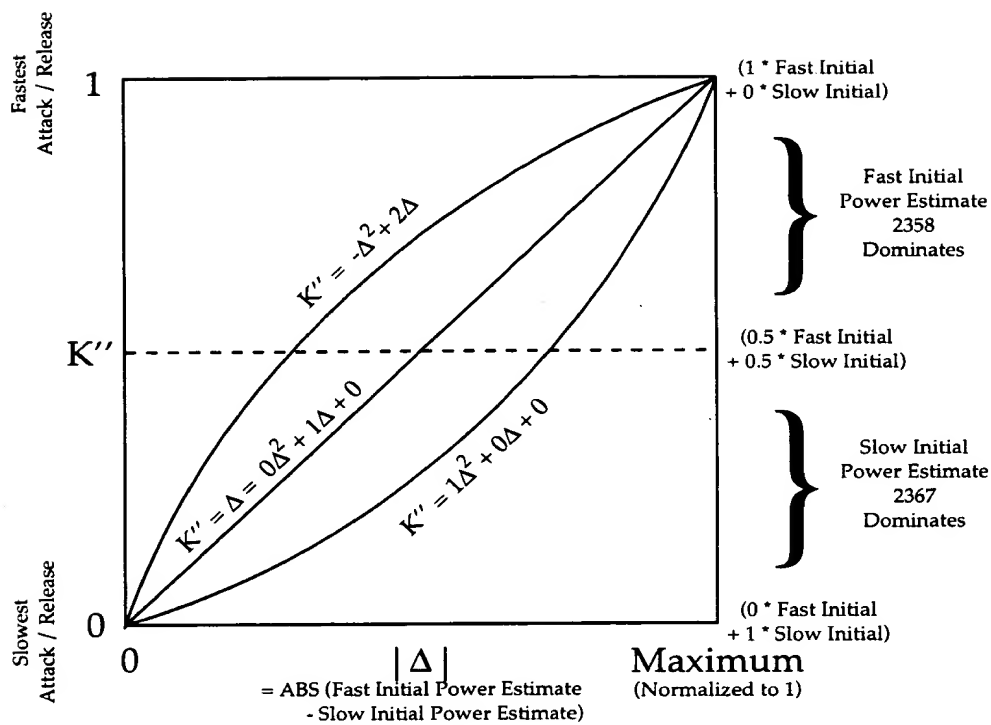


FIG. 24F

00621-51222200



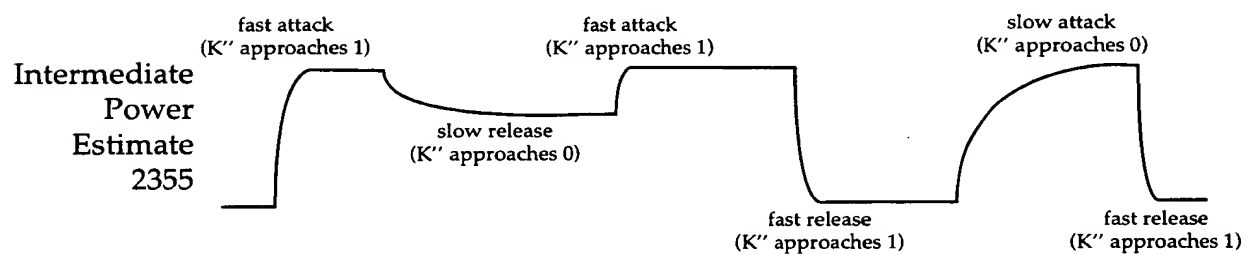
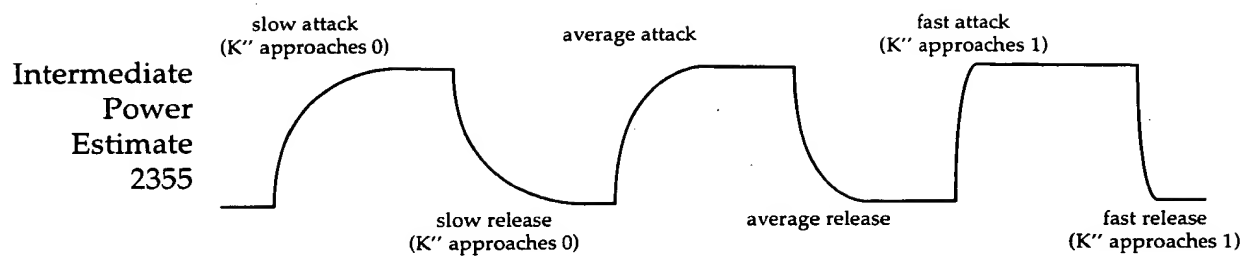
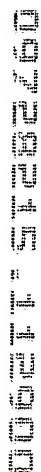


FIG. 24G

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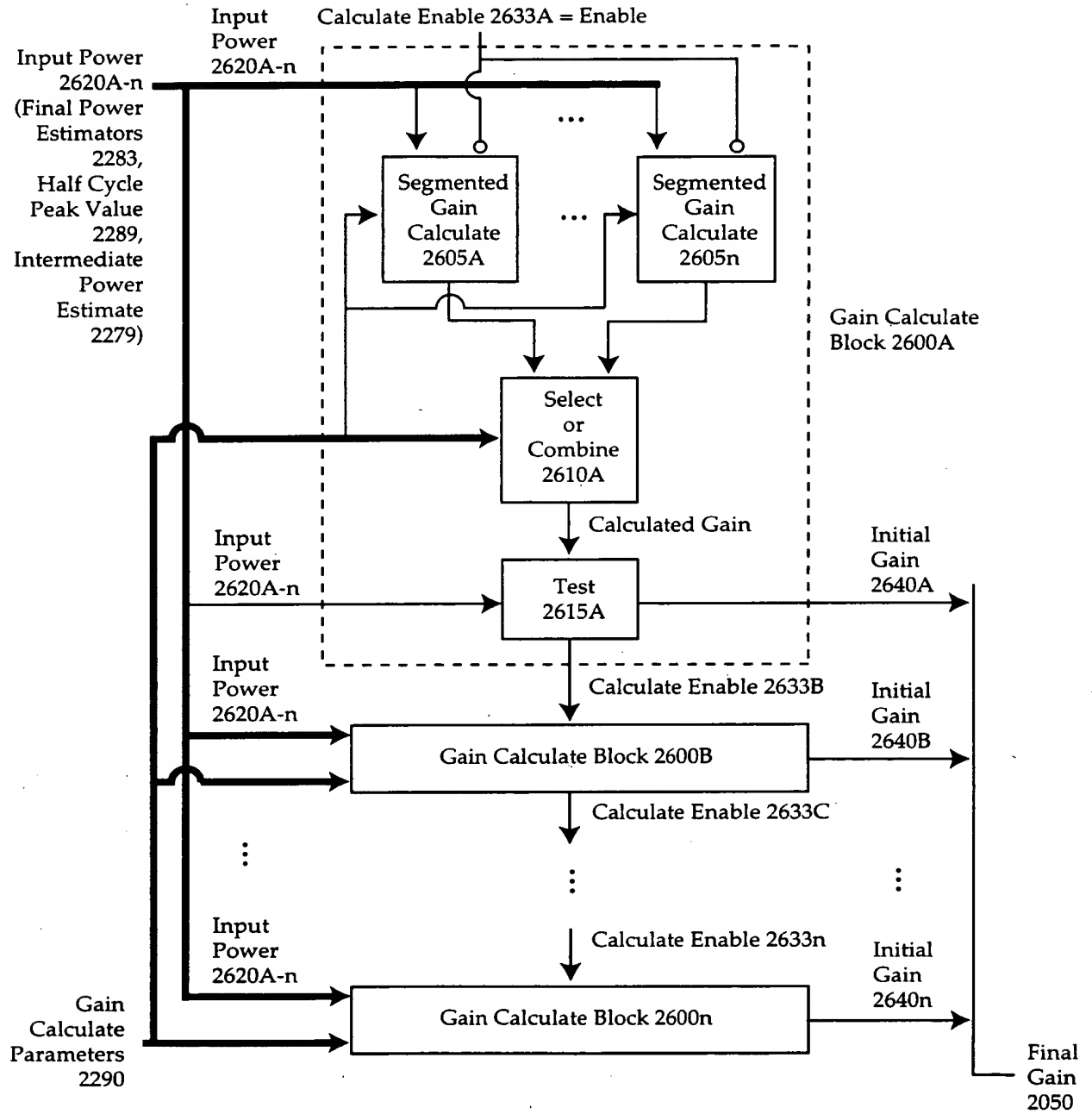


FIG. 26A

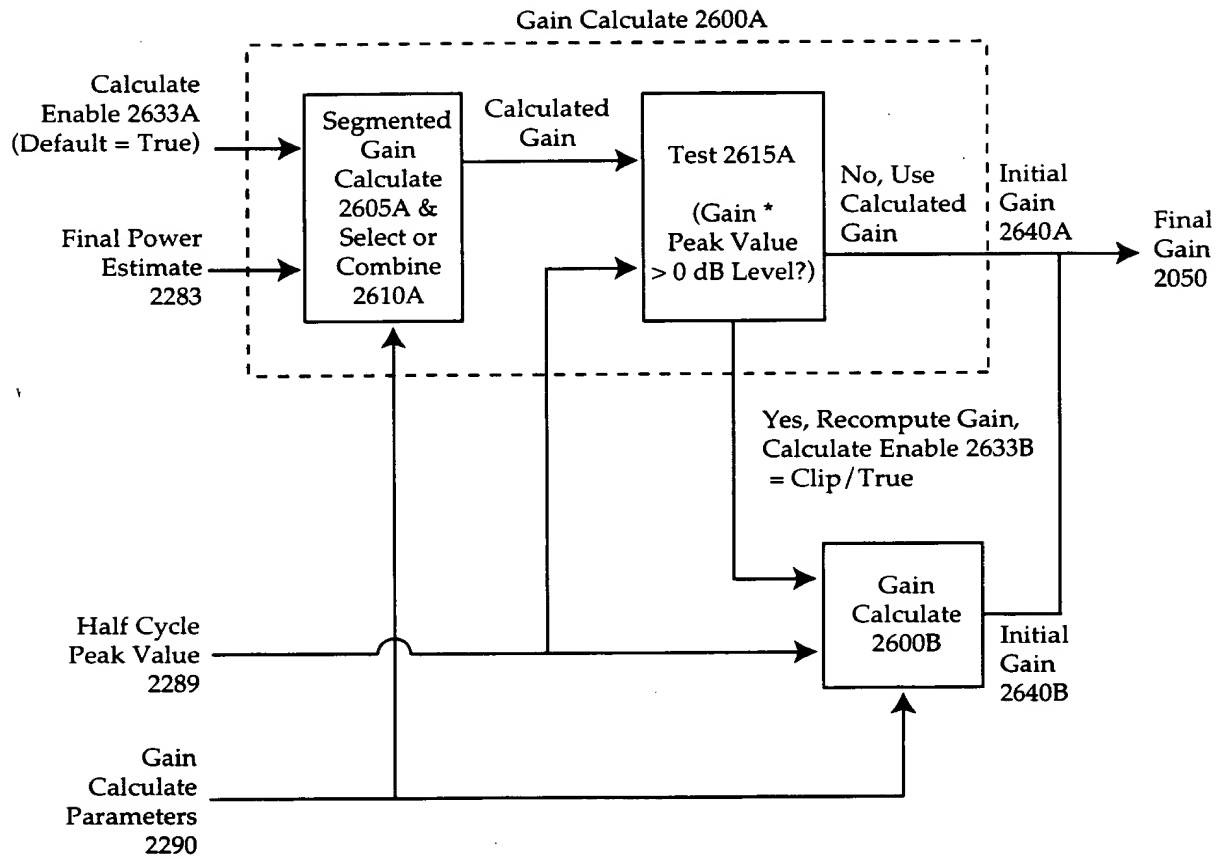


FIG. 26B

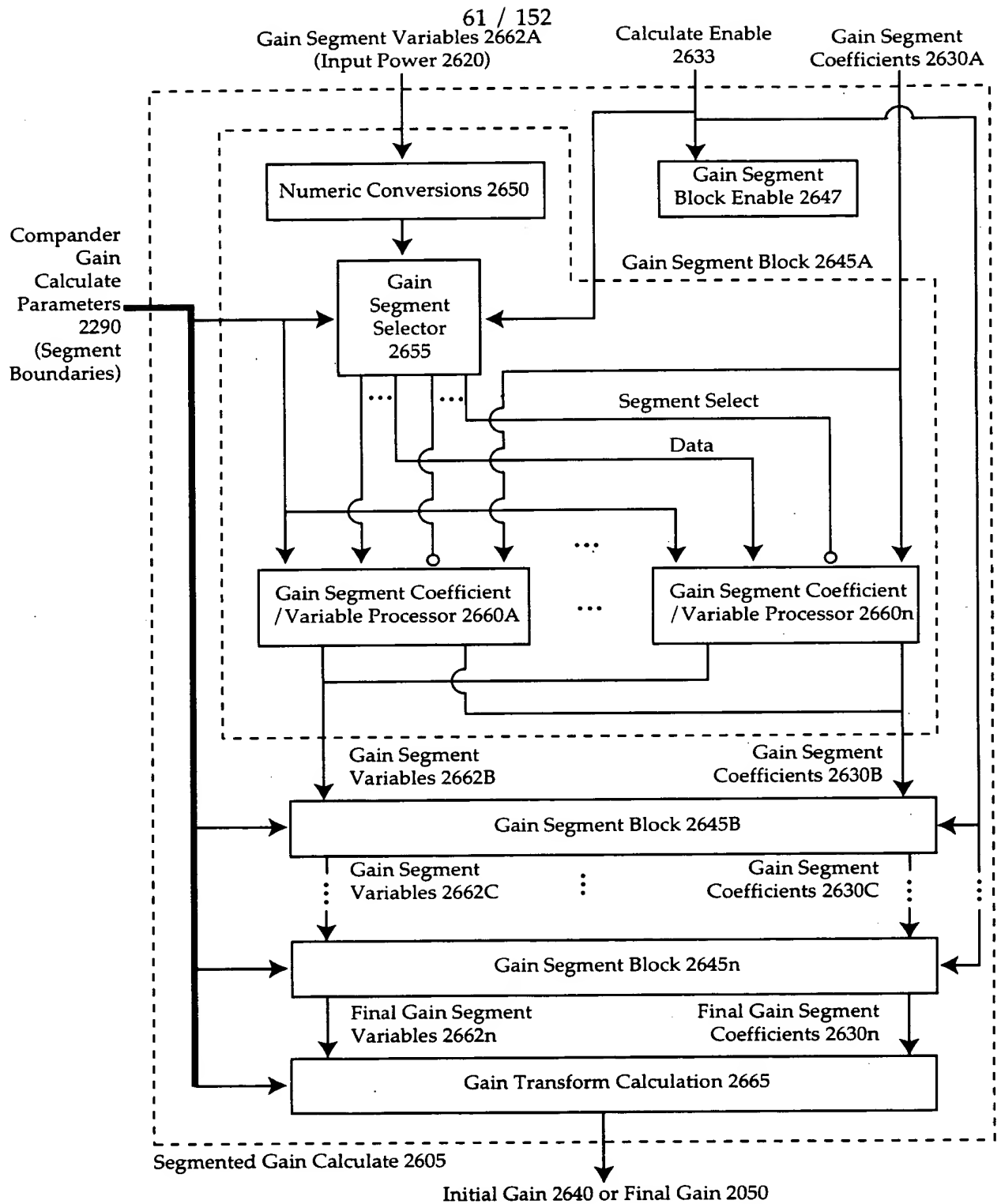


FIG. 26C

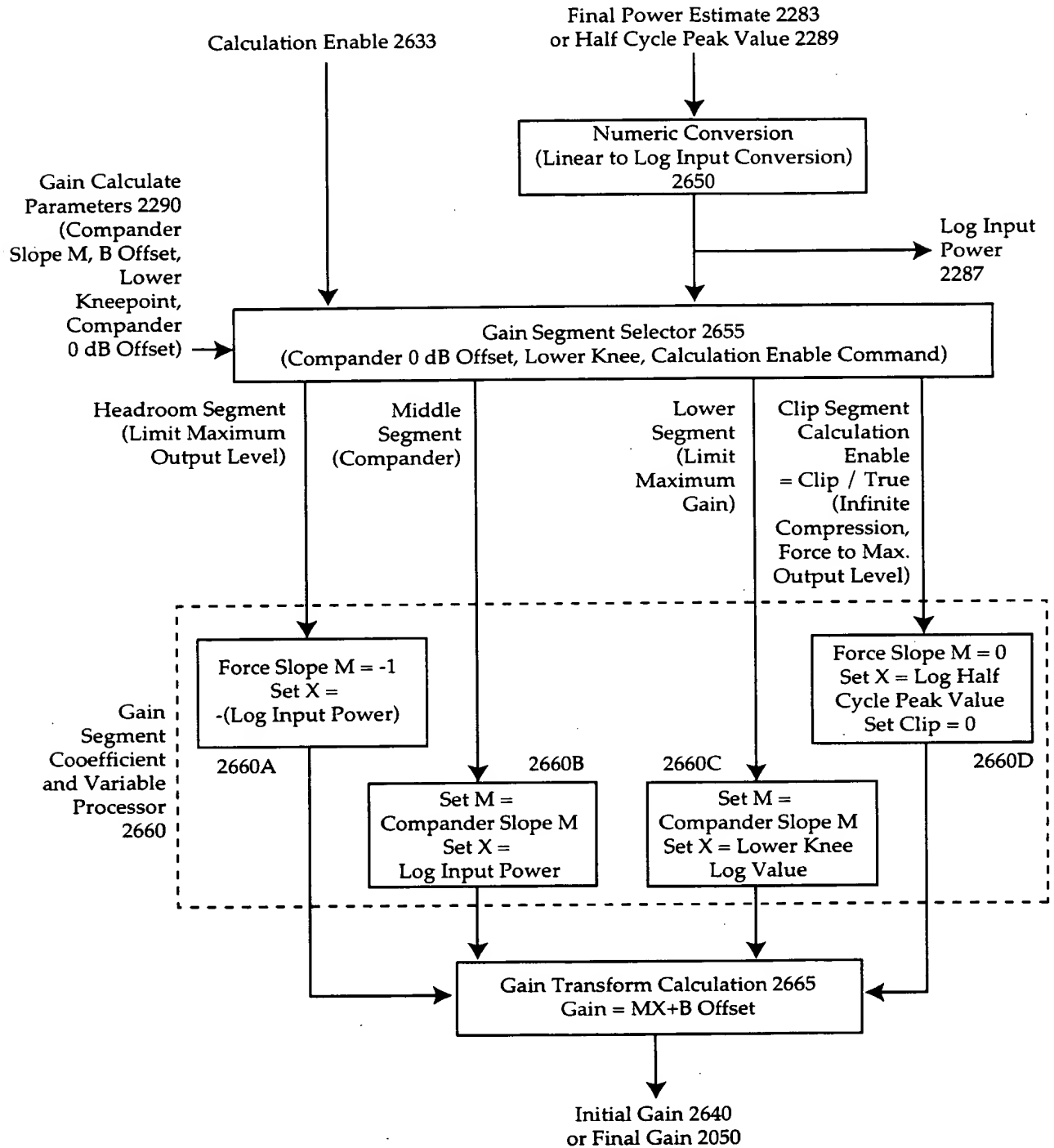
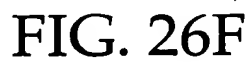


FIG. 26D



$$Y = MX + B$$

= Gain Table Address

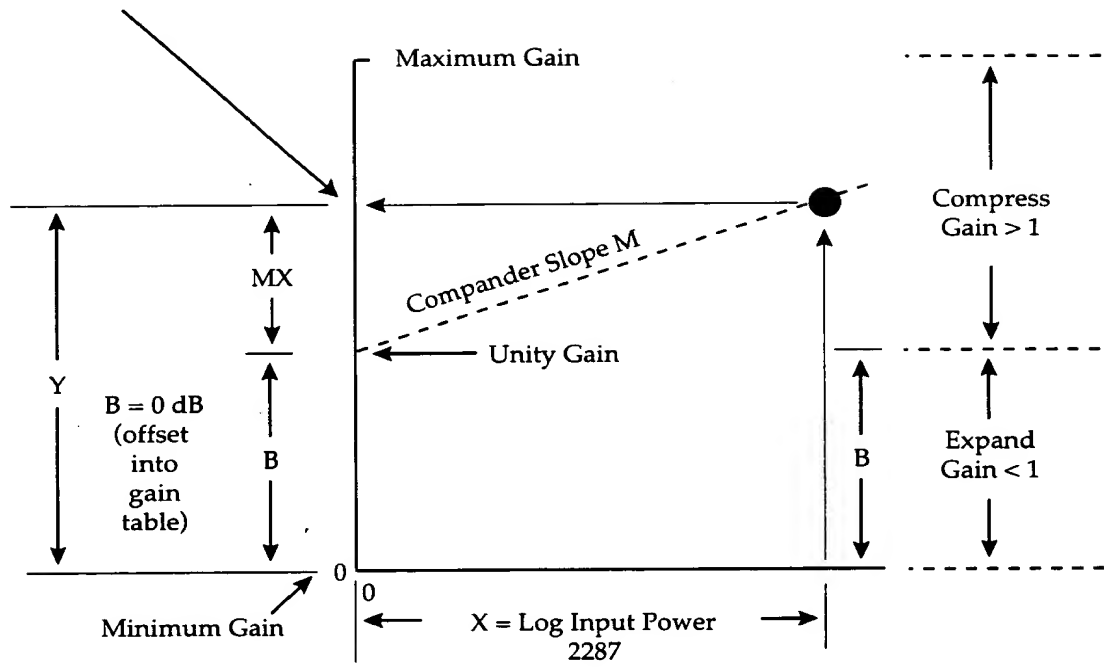


FIG. 26G



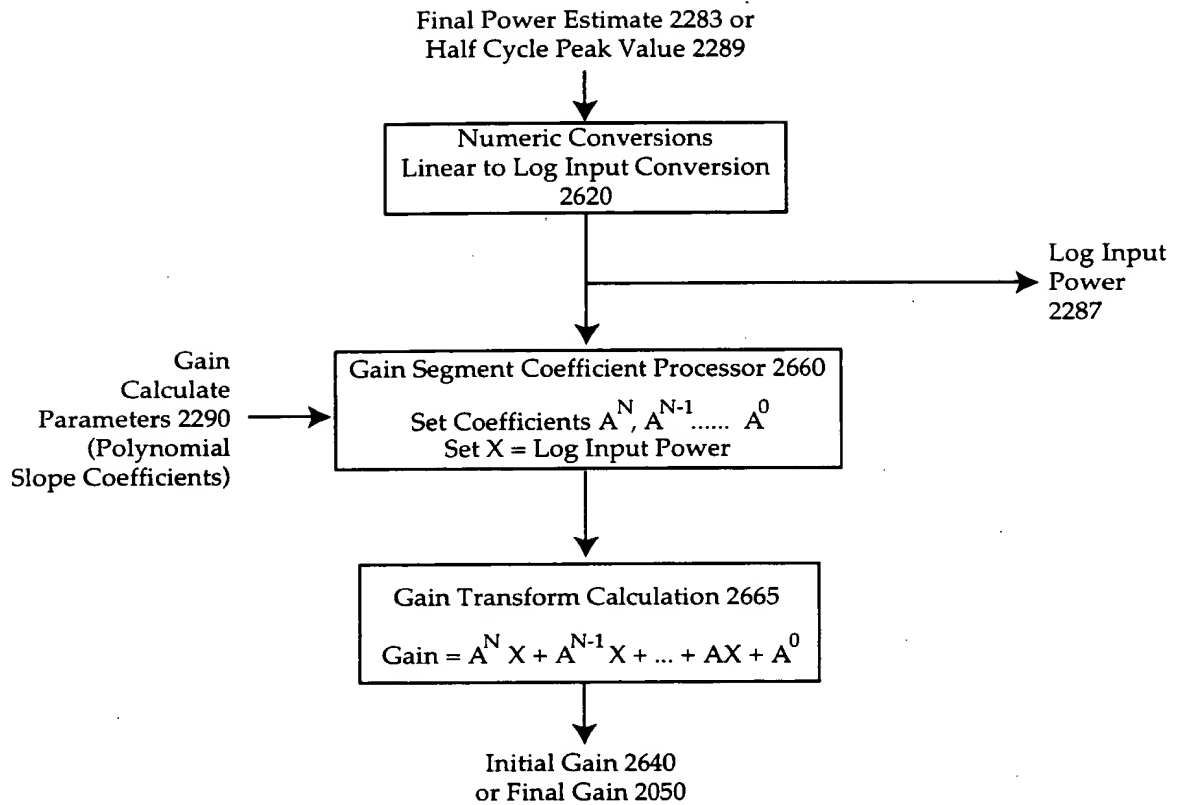


FIG. 26H

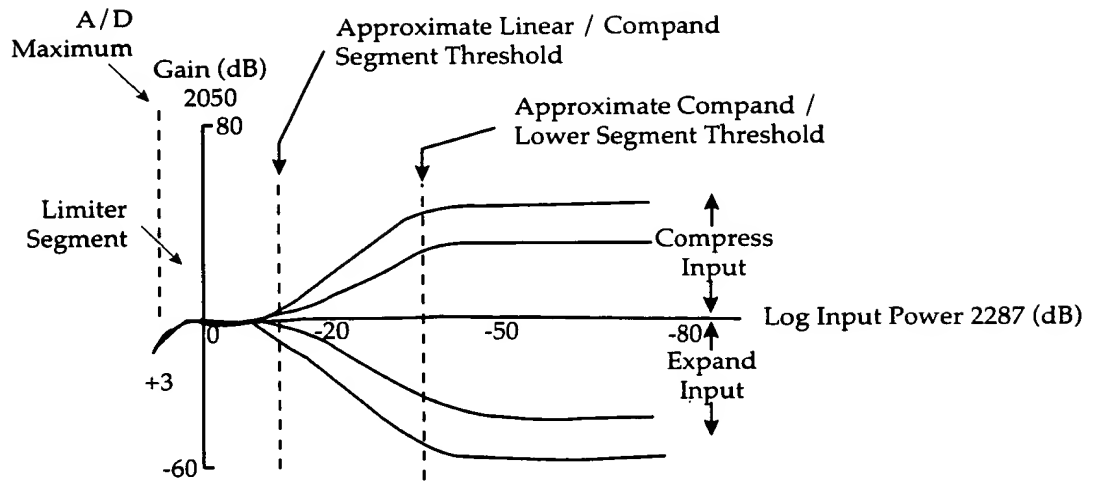


FIG. 26I

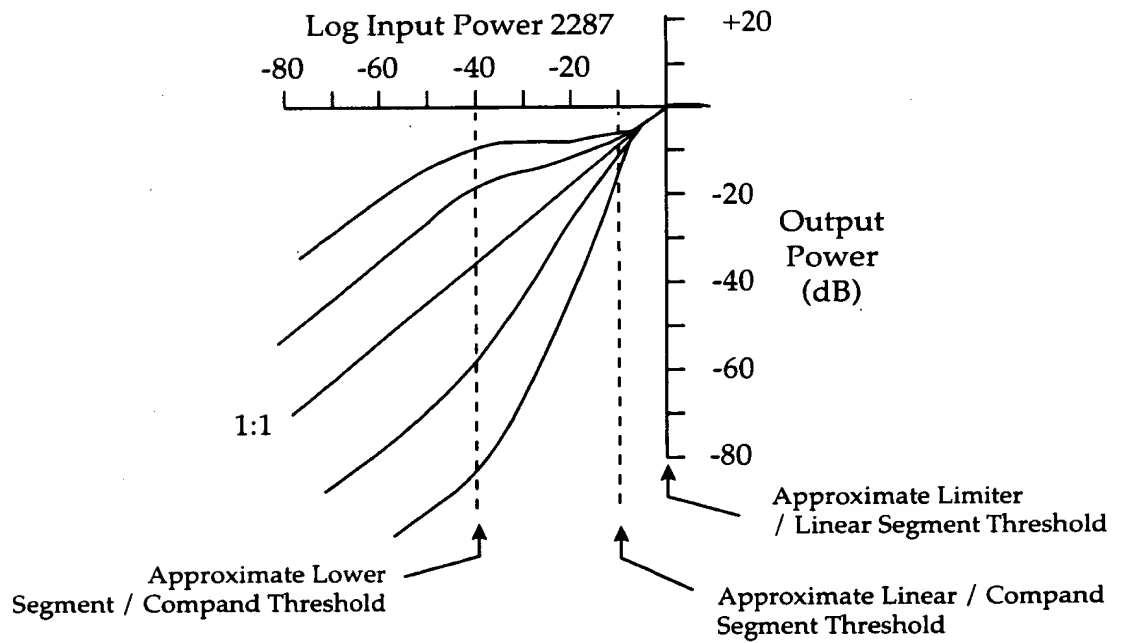


FIG. 26J

00728215-112900

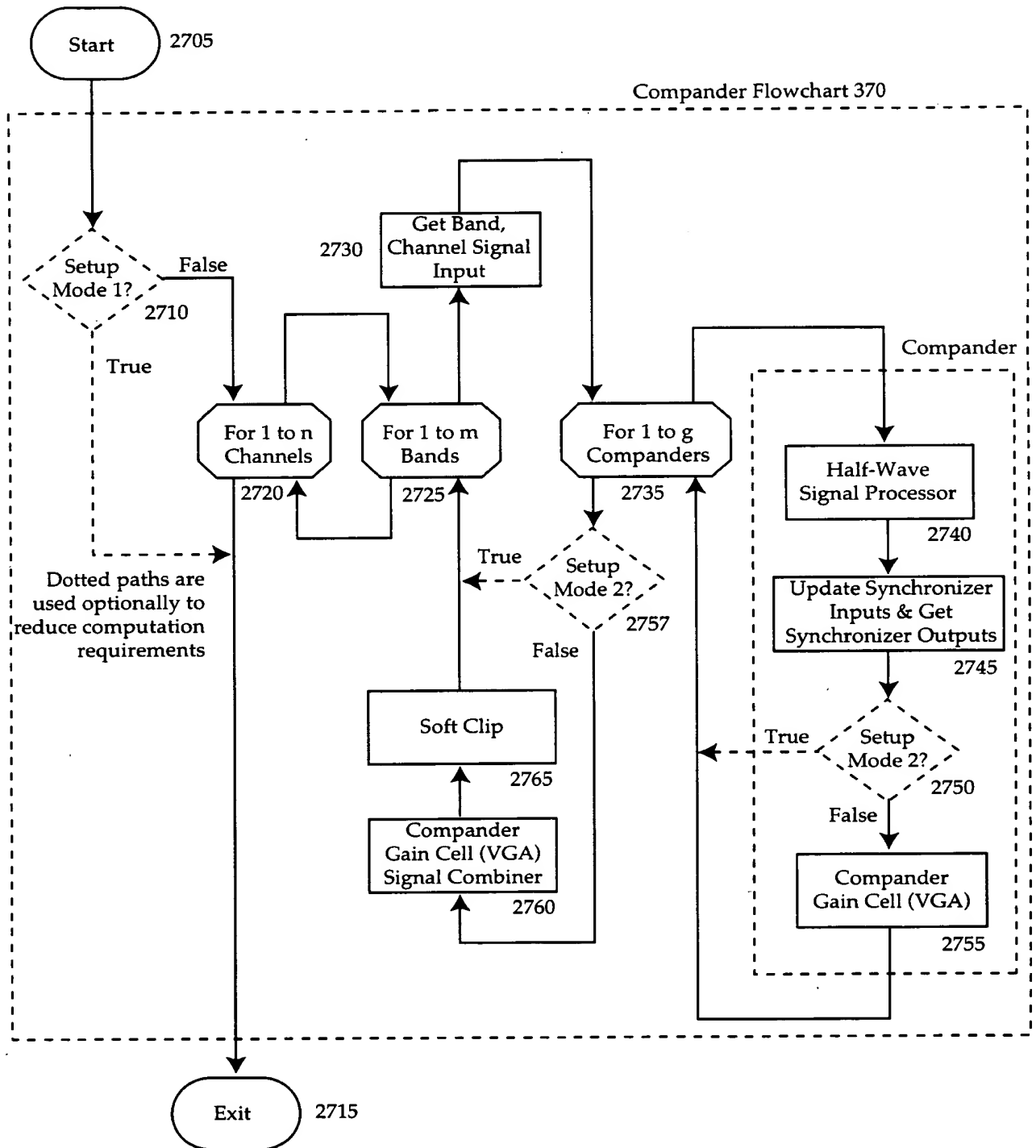


FIG. 27A

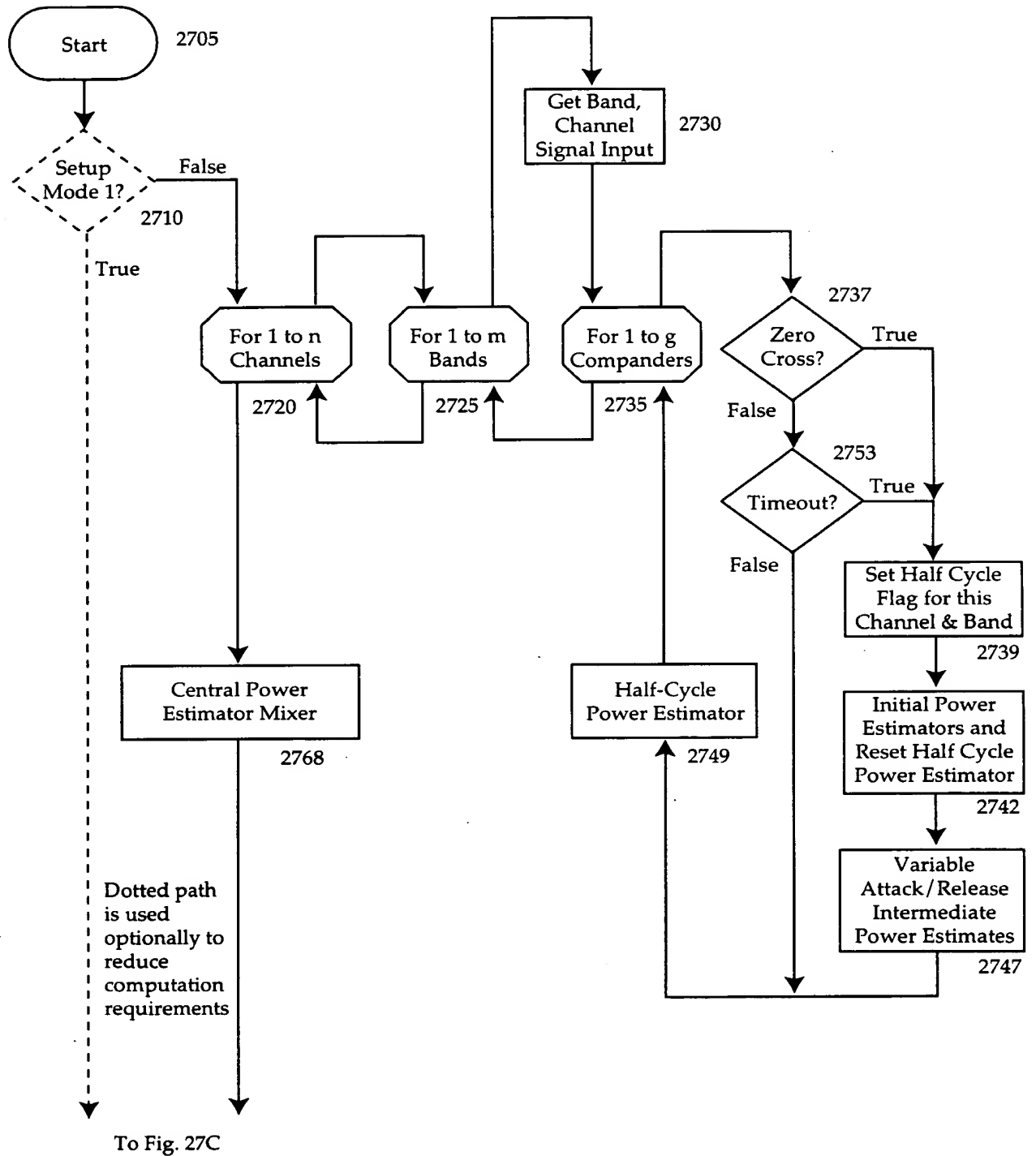


FIG. 27B

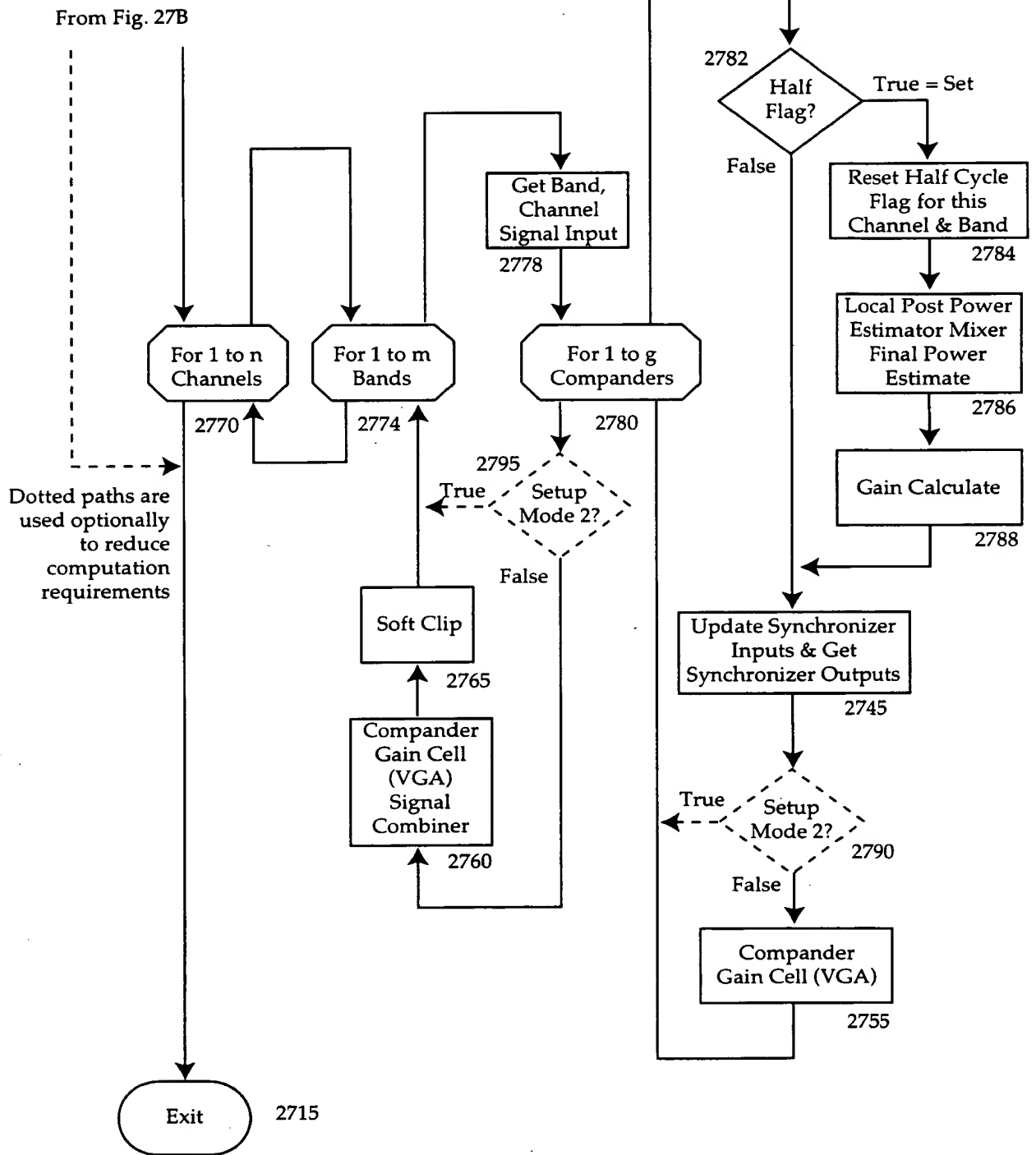


FIG. 27C

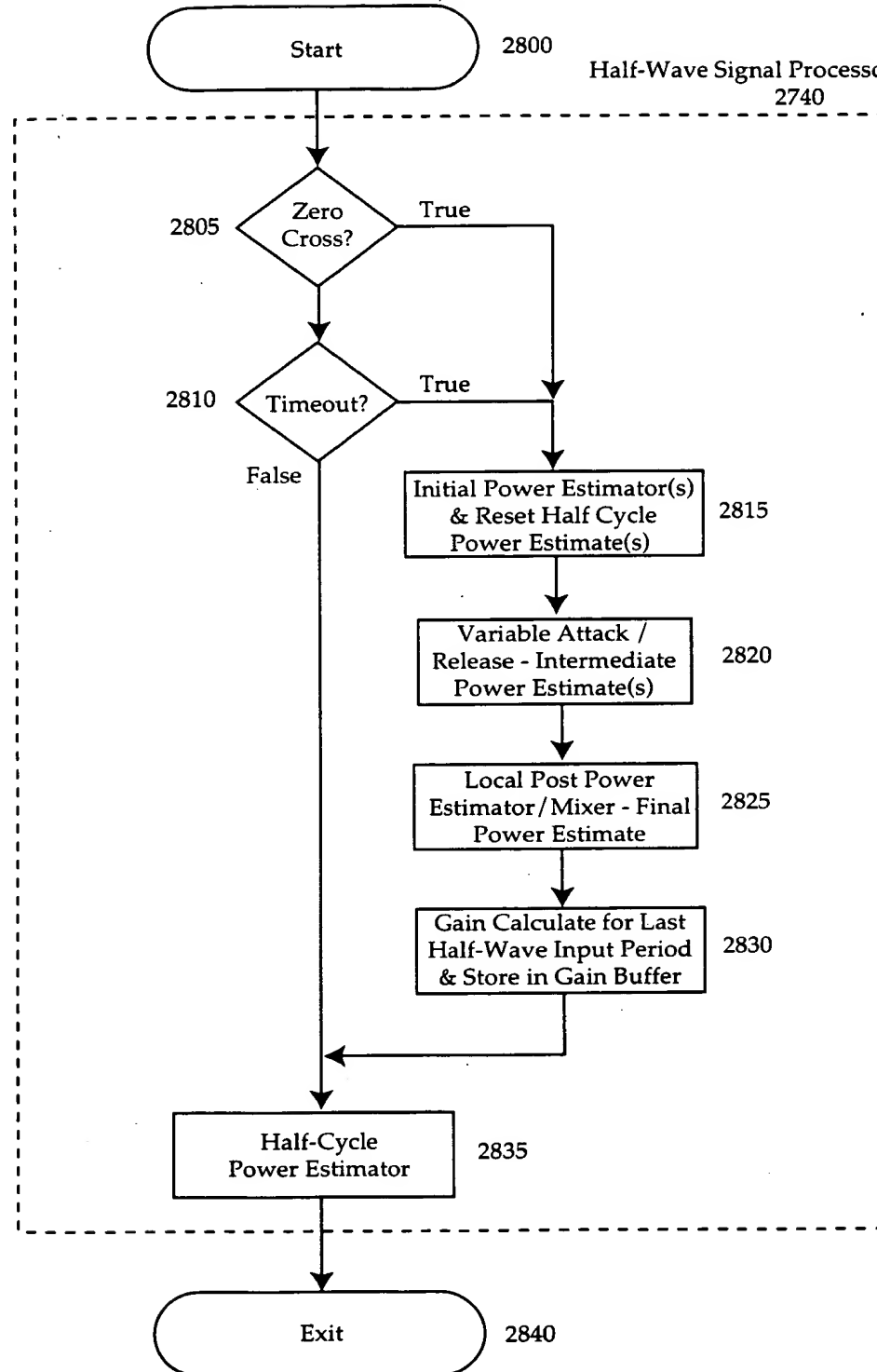


FIG. 28

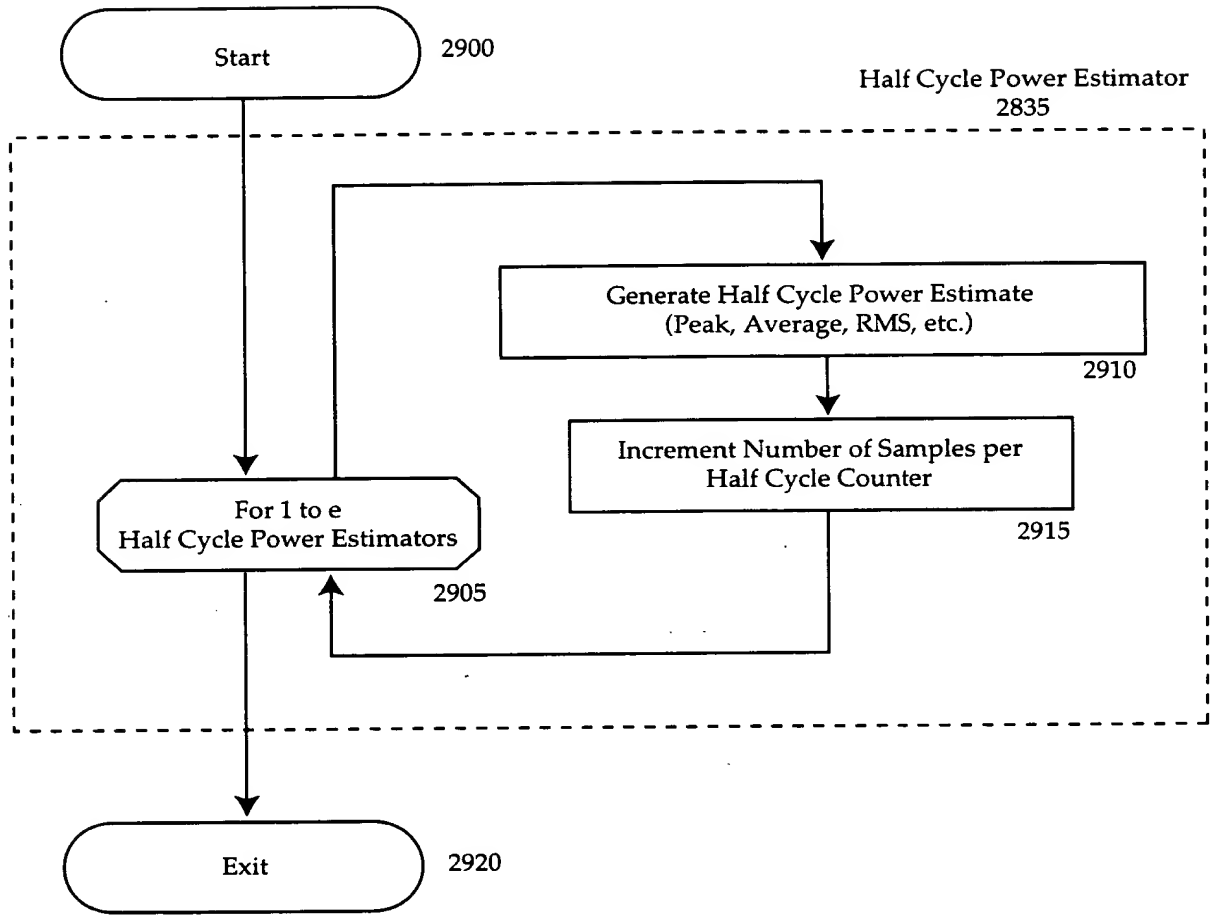


FIG. 29

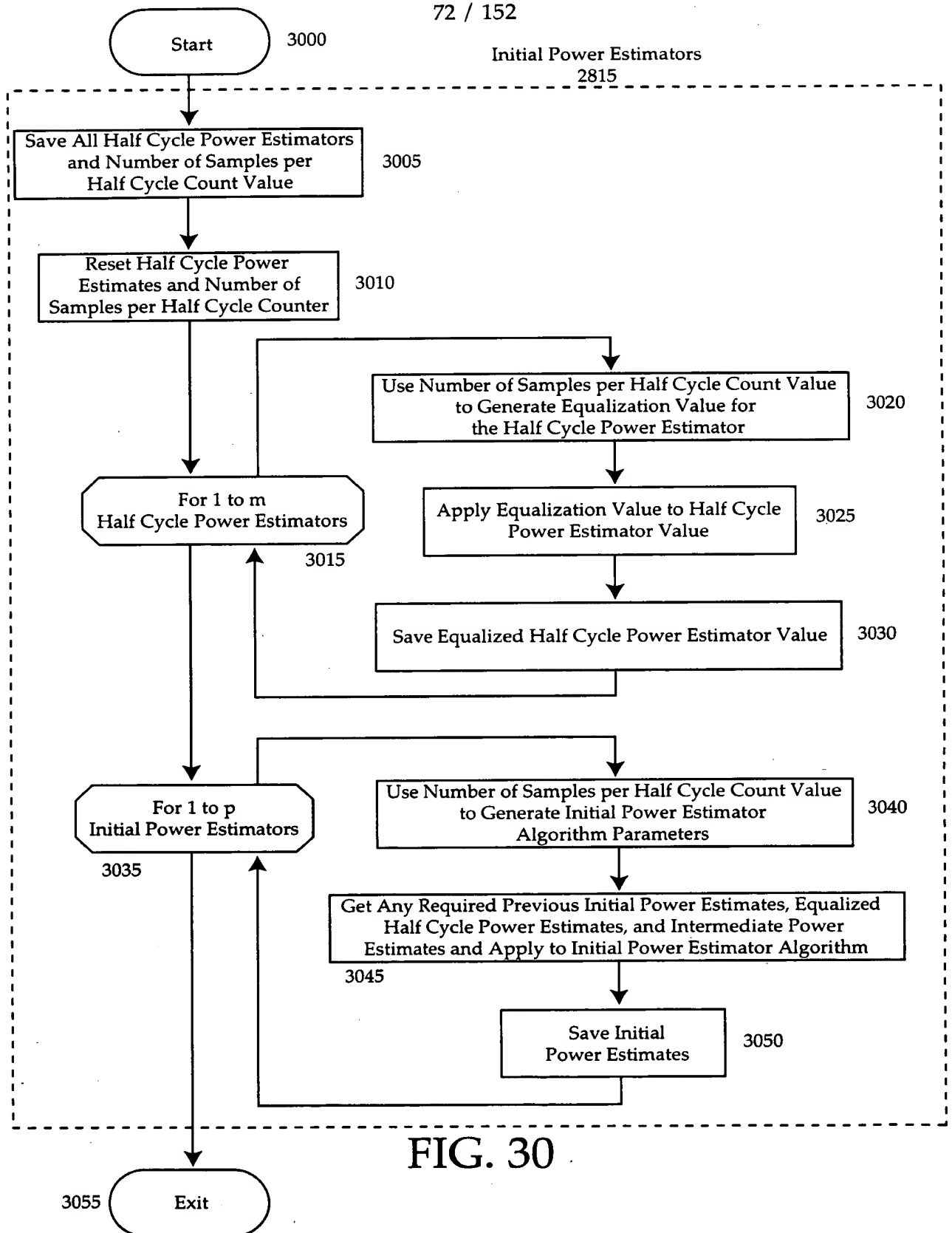
Initial Power Estimators  
2815

FIG. 30



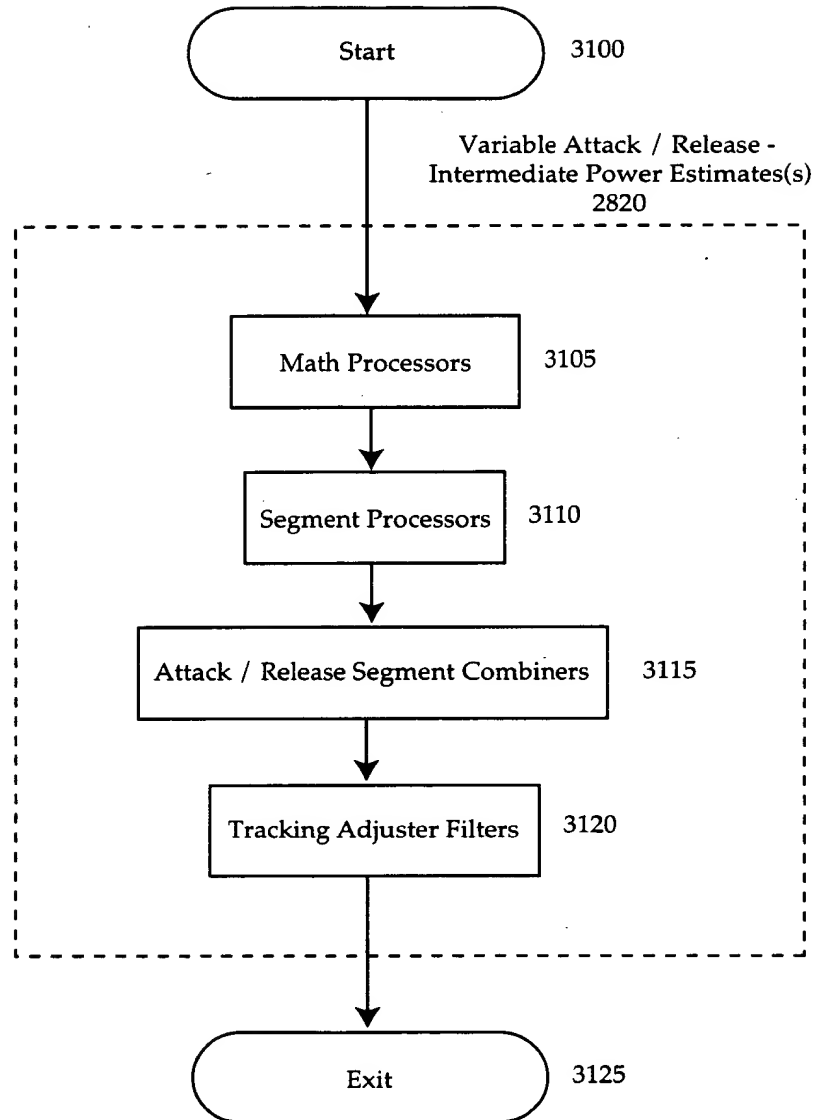
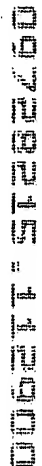


FIG. 31



—

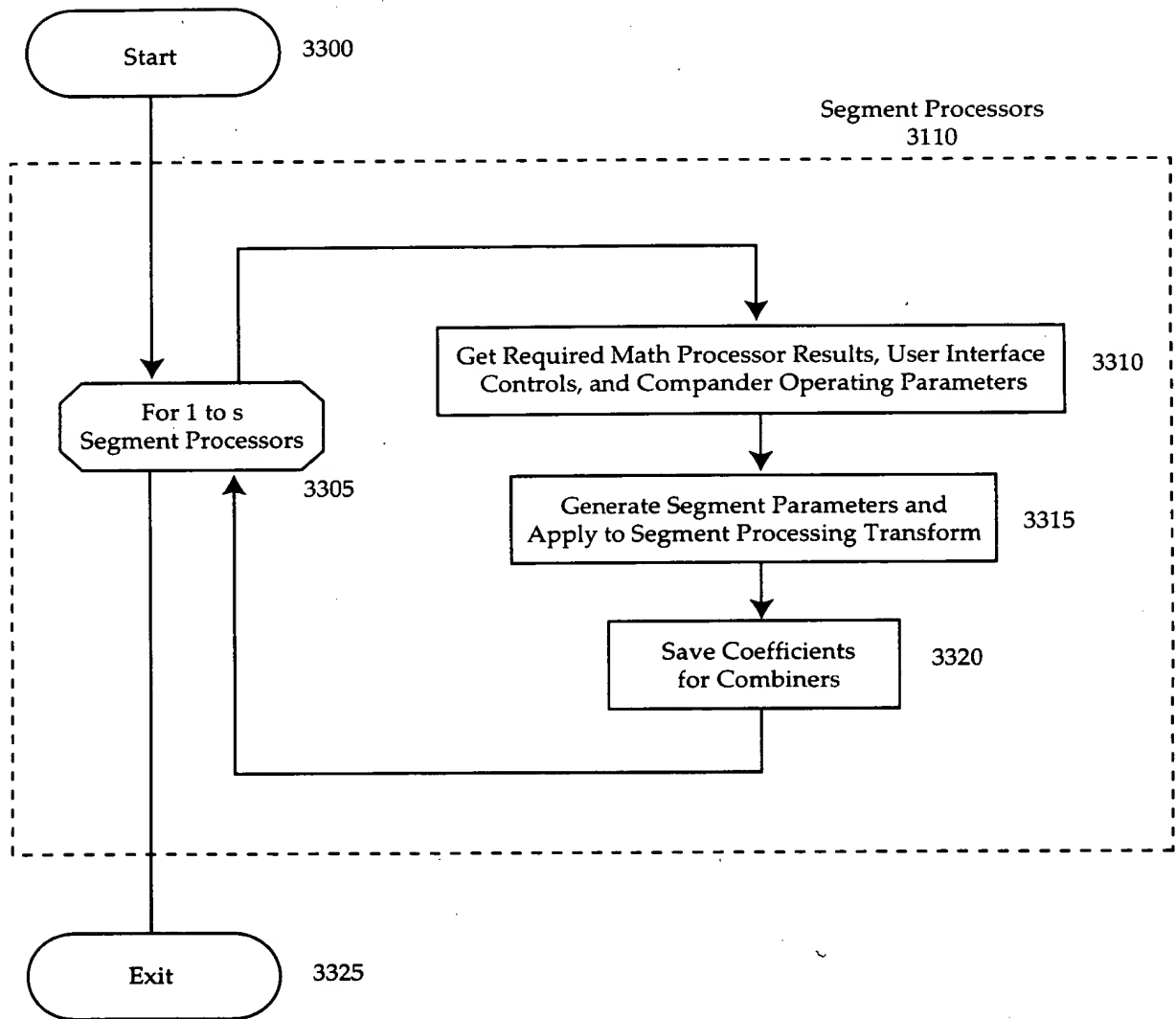


FIG. 33

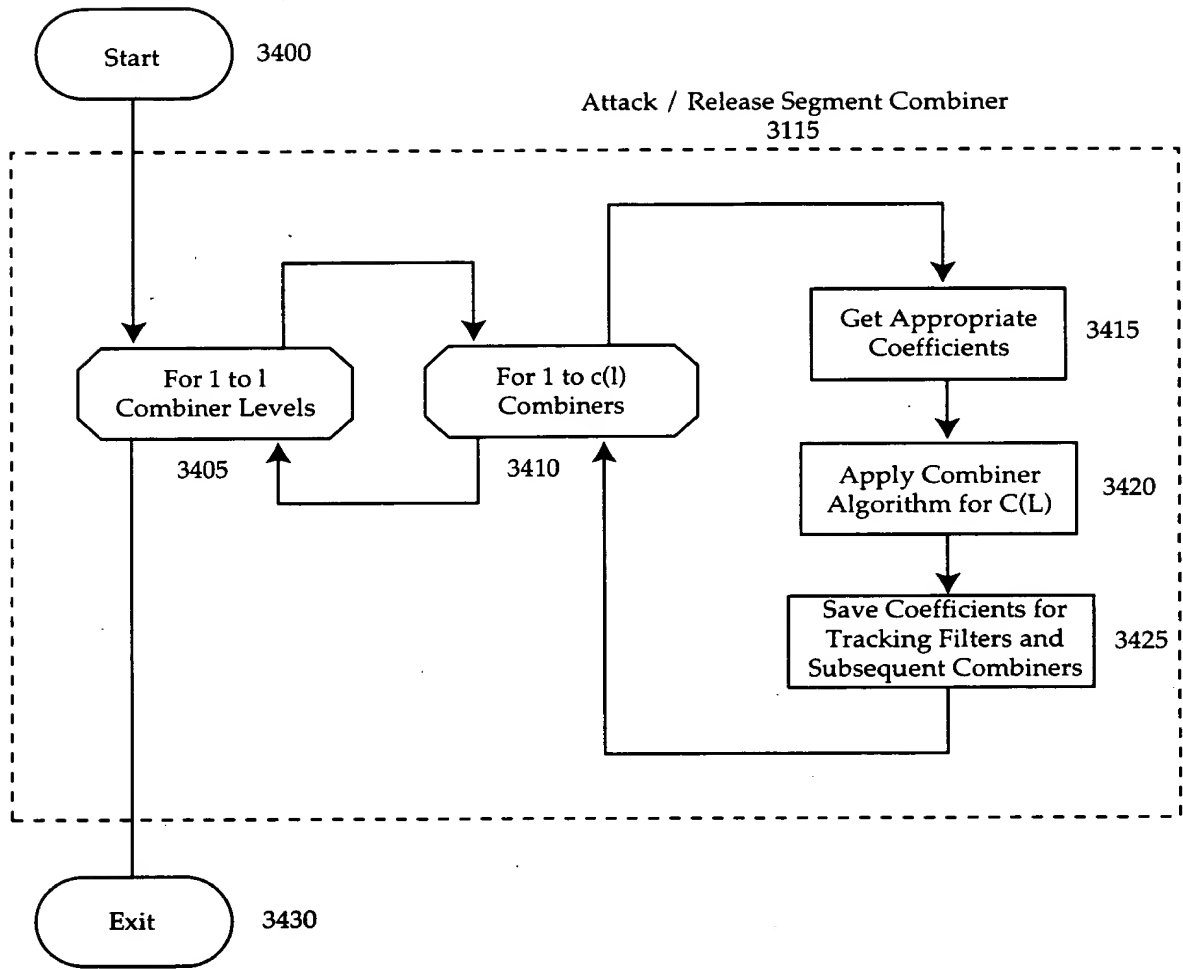
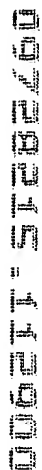


FIG. 34



—

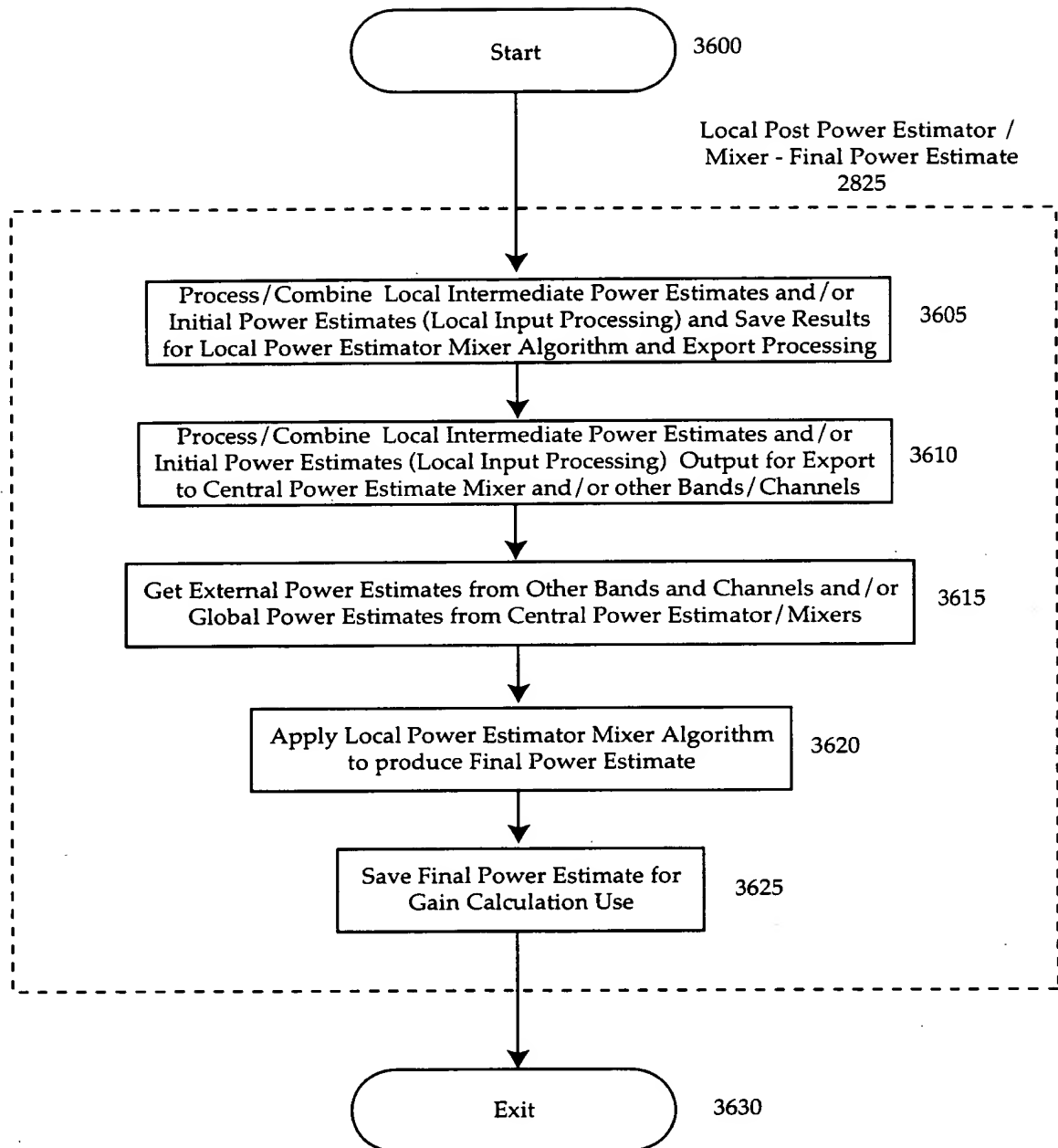


FIG. 36

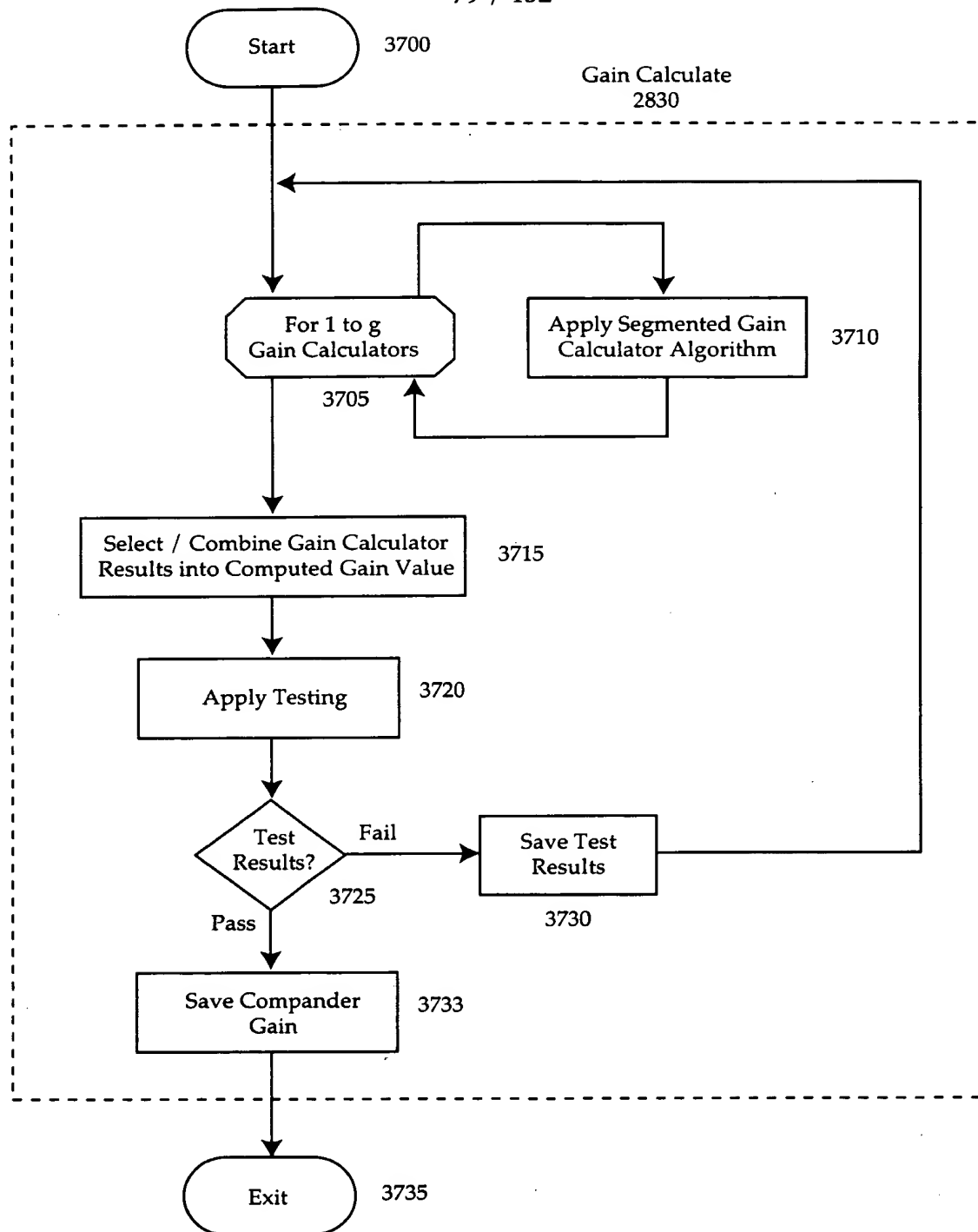


FIG. 37A

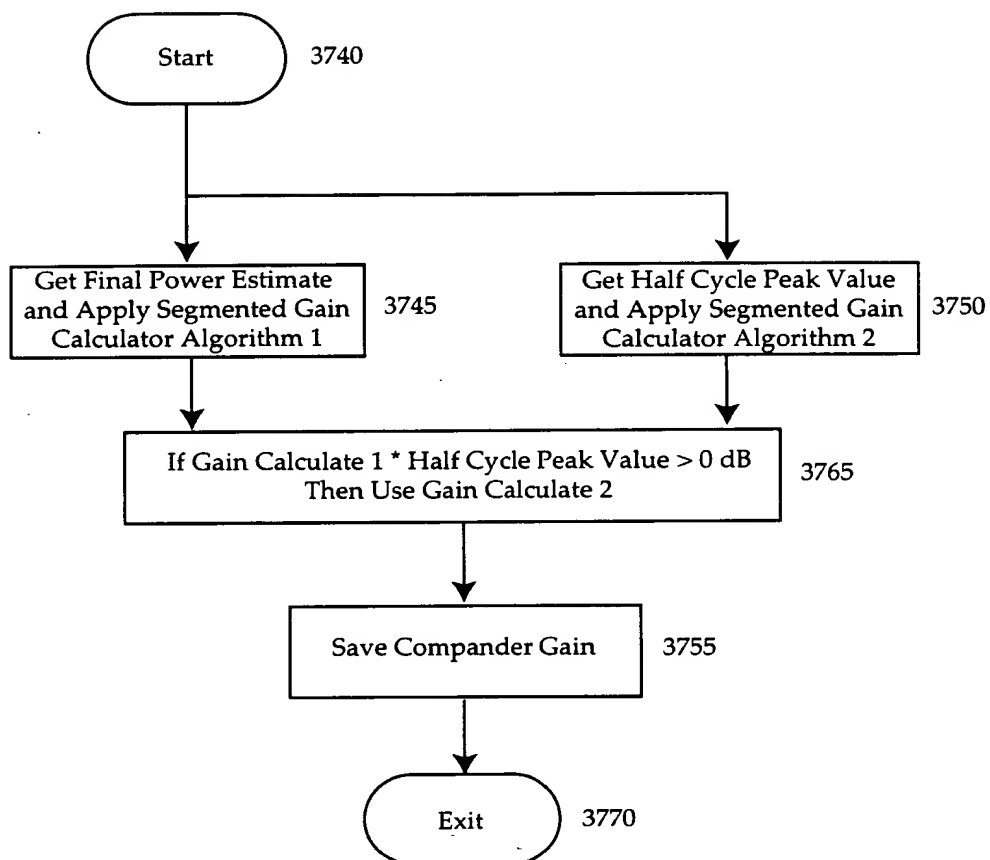


FIG. 37B



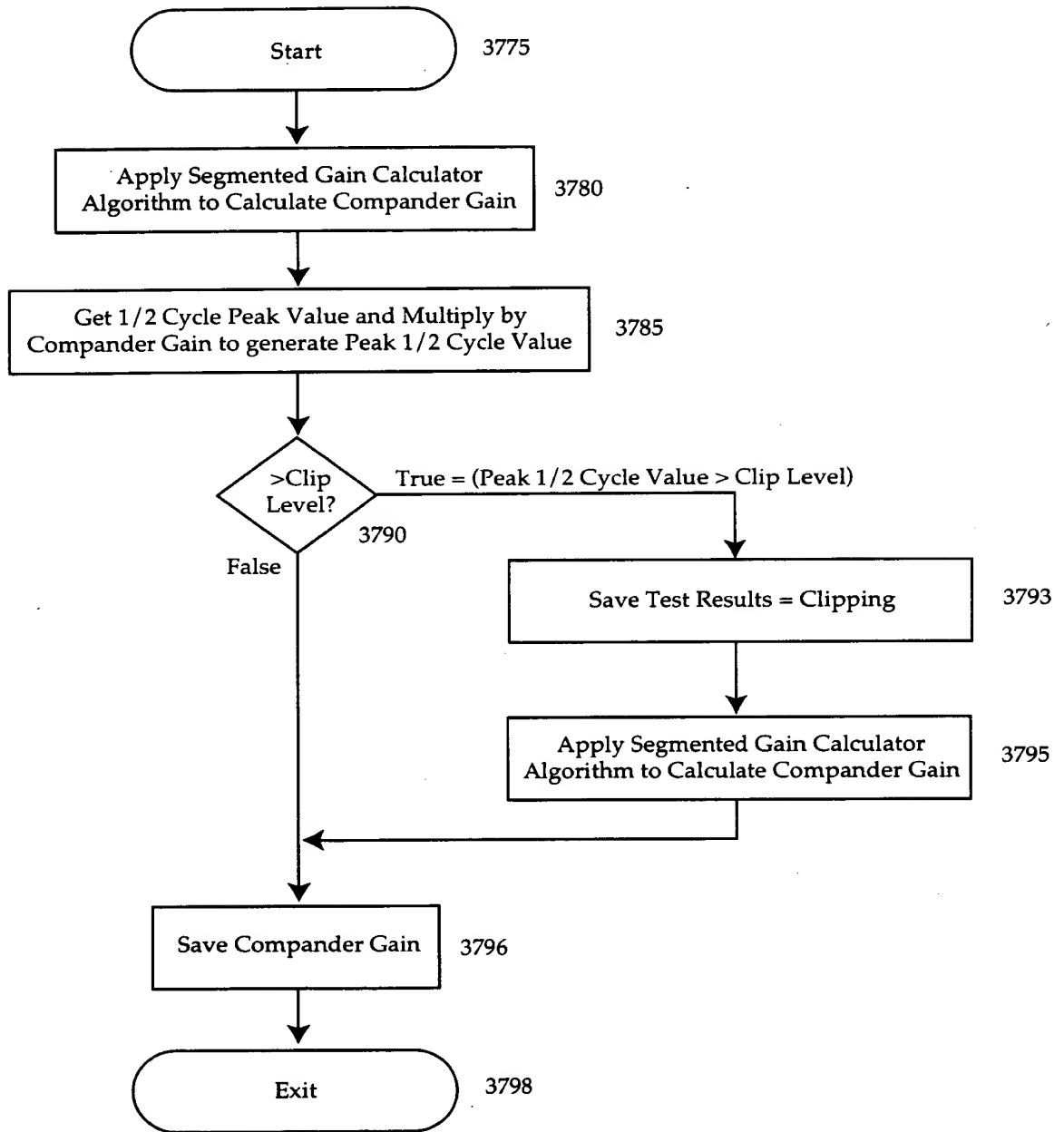


FIG. 37C



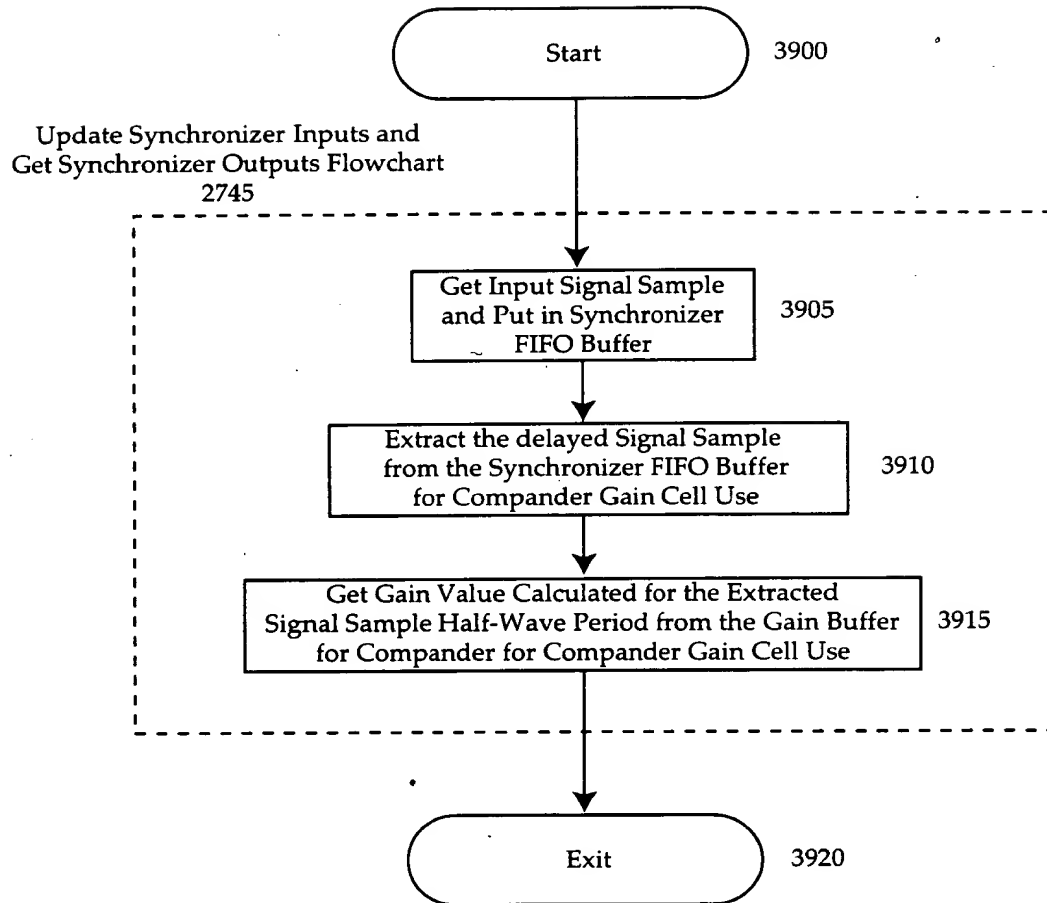


FIG. 39

Softclip 2765

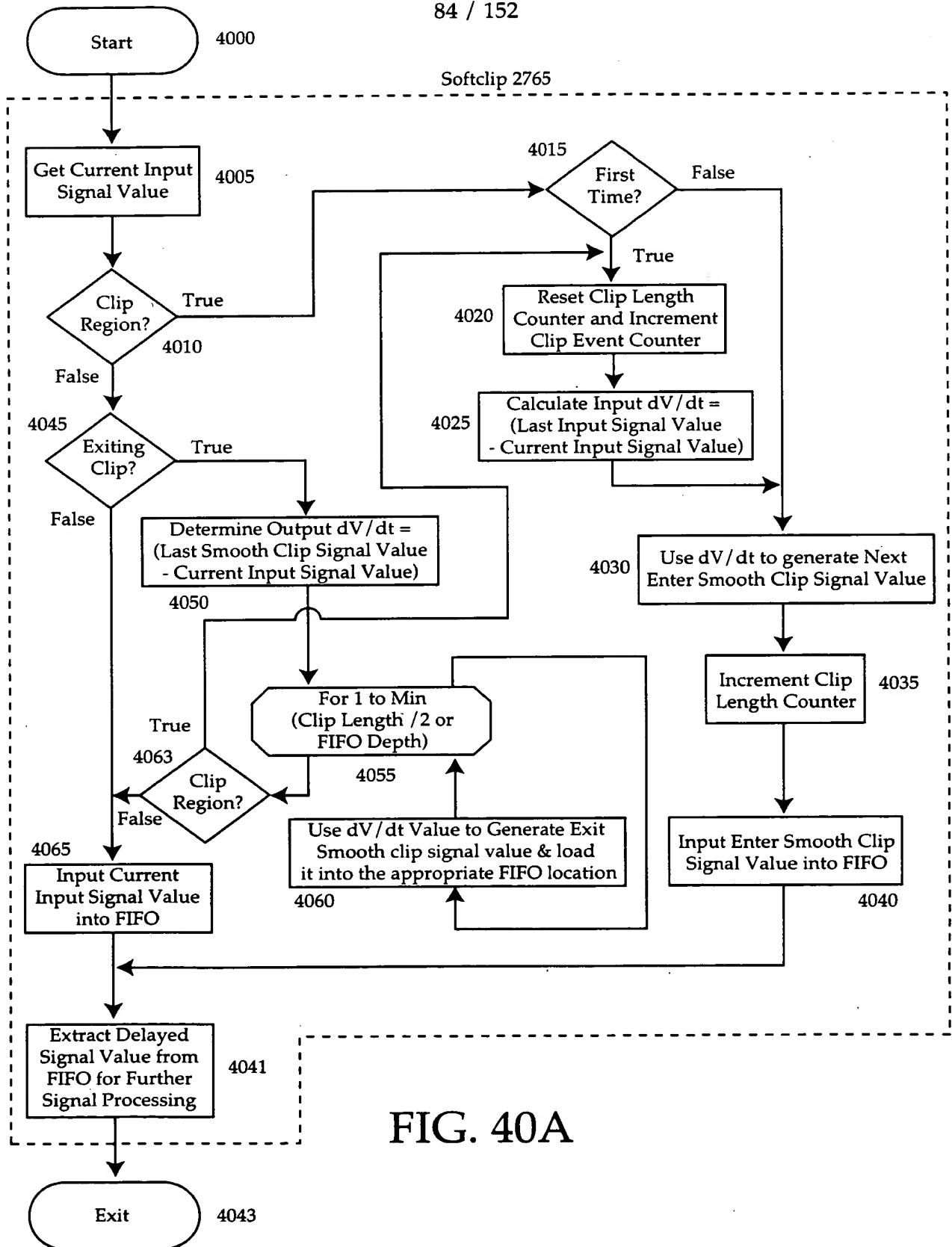


FIG. 40A

00728215-112900

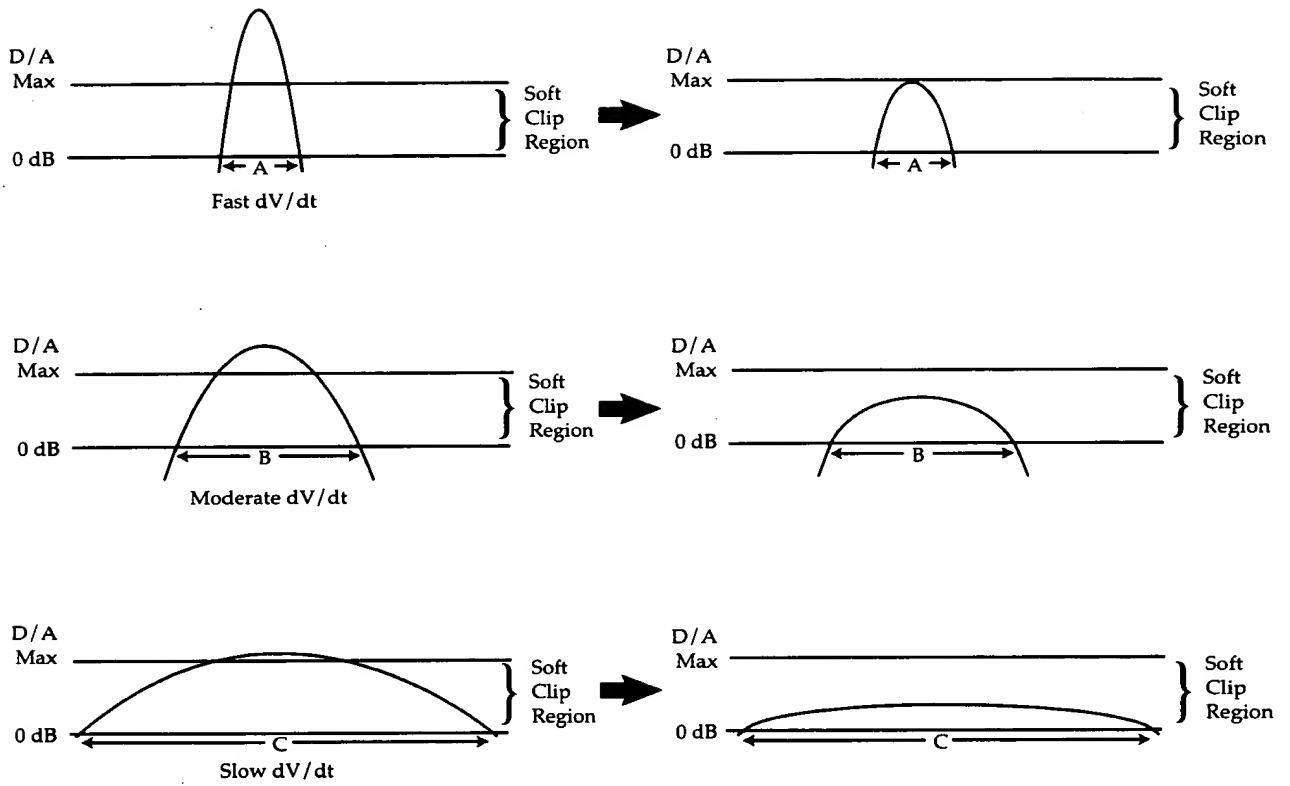


FIG. 40B

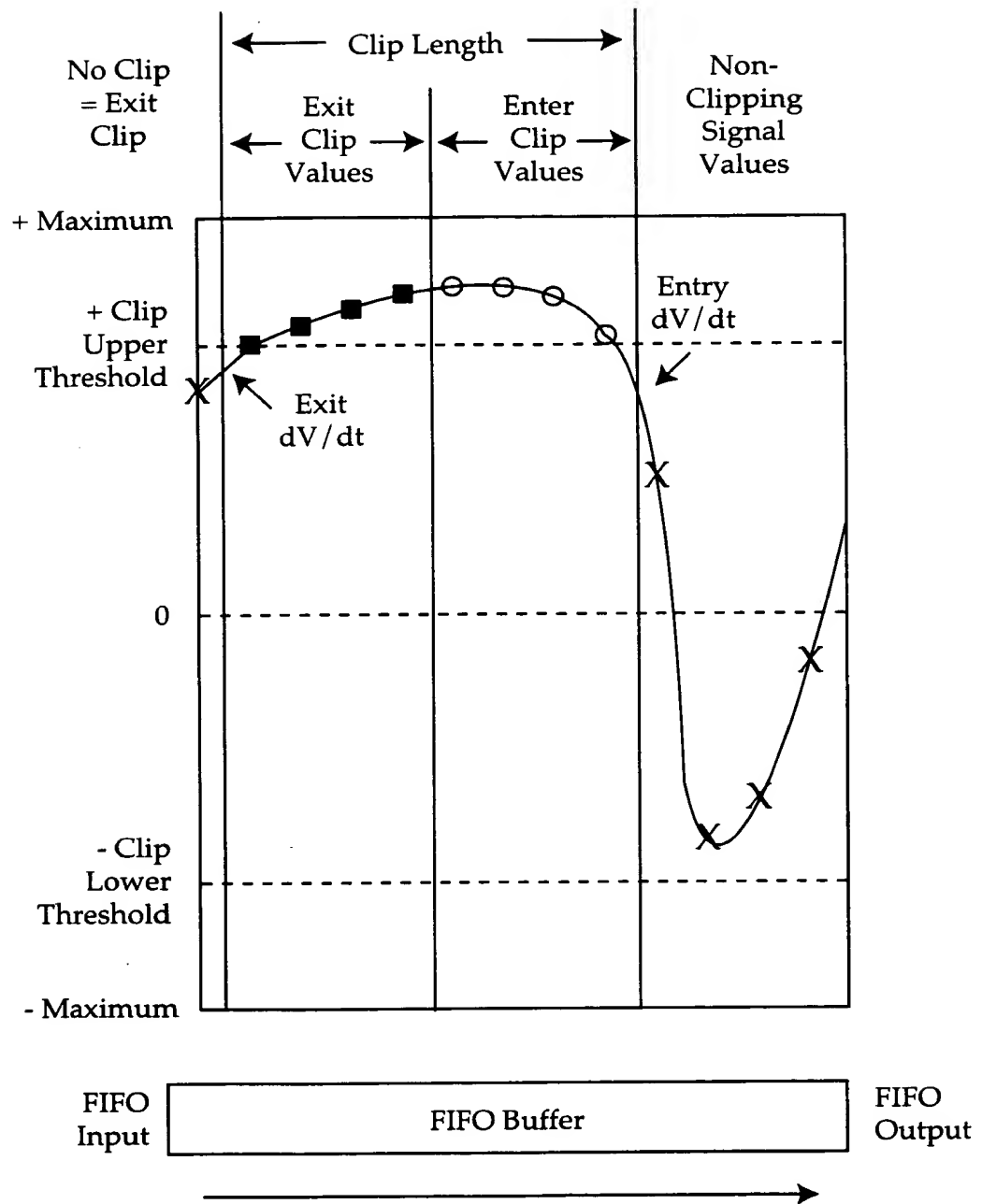


FIG. 40C

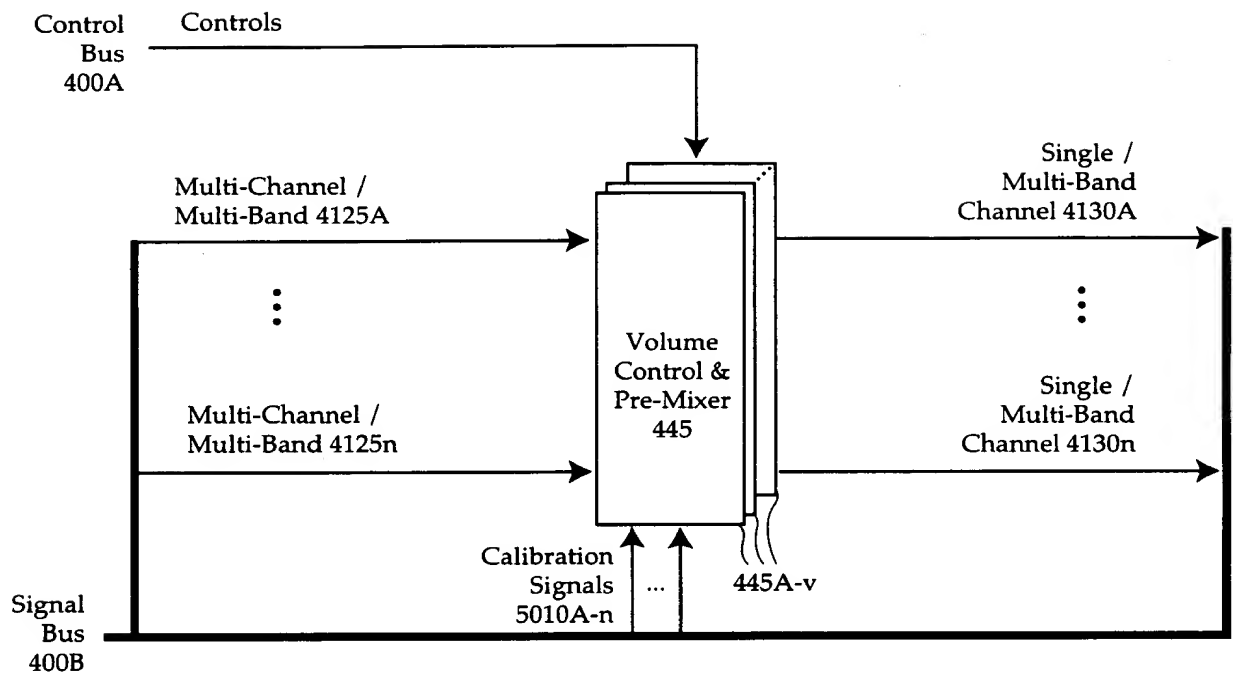


FIG. 41A

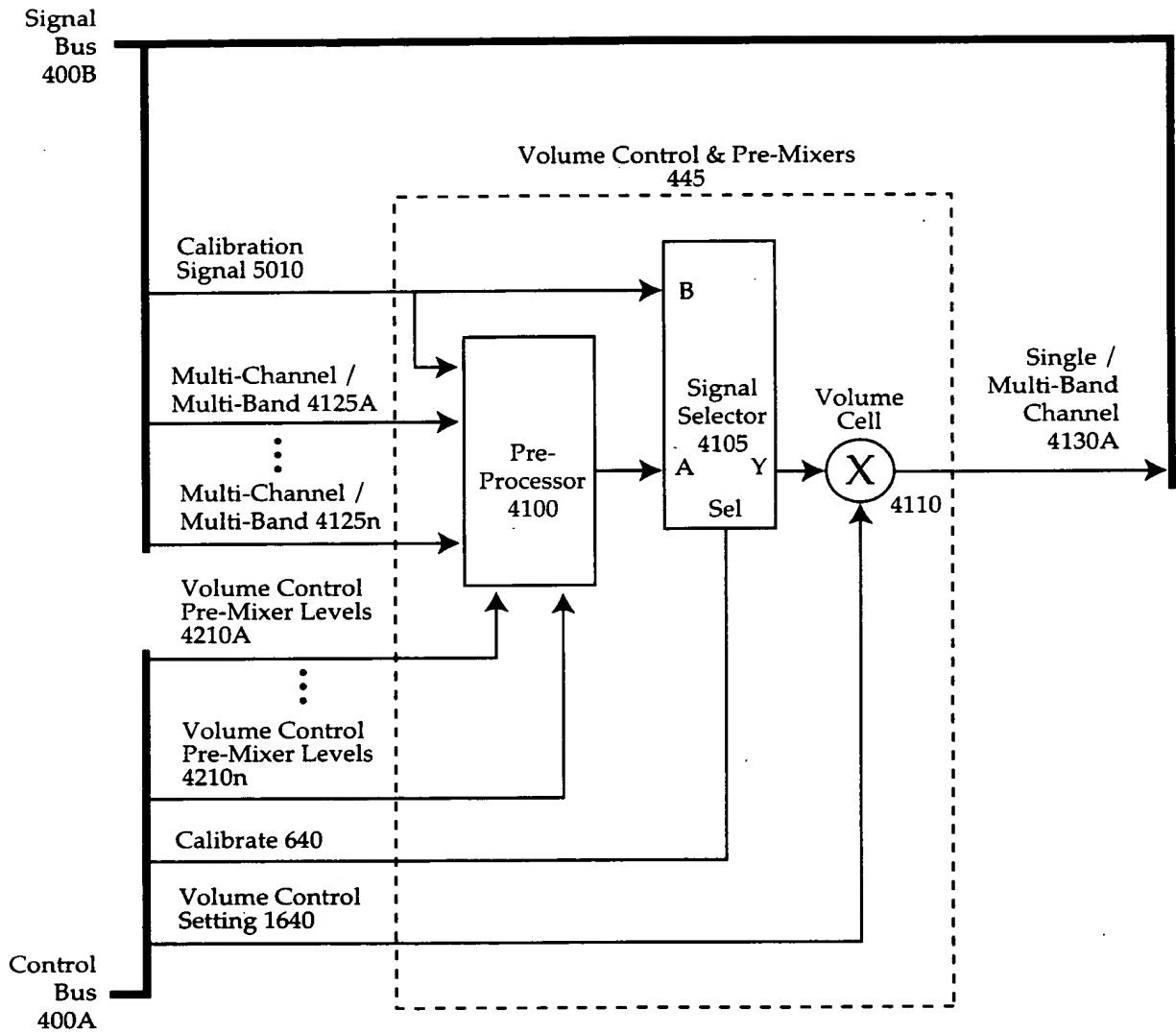


FIG. 41B



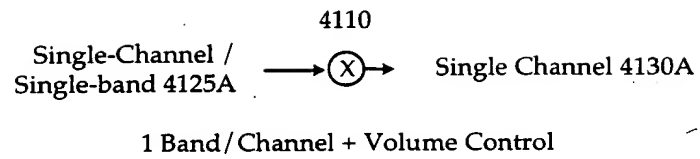


FIG. 42A

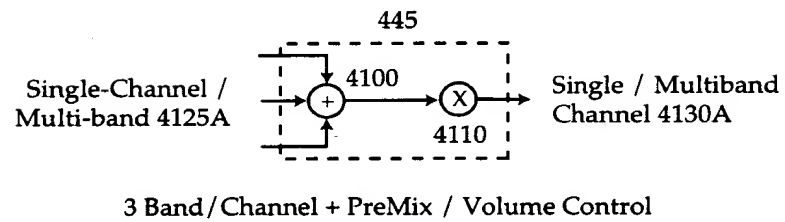
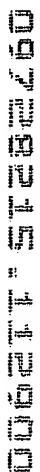


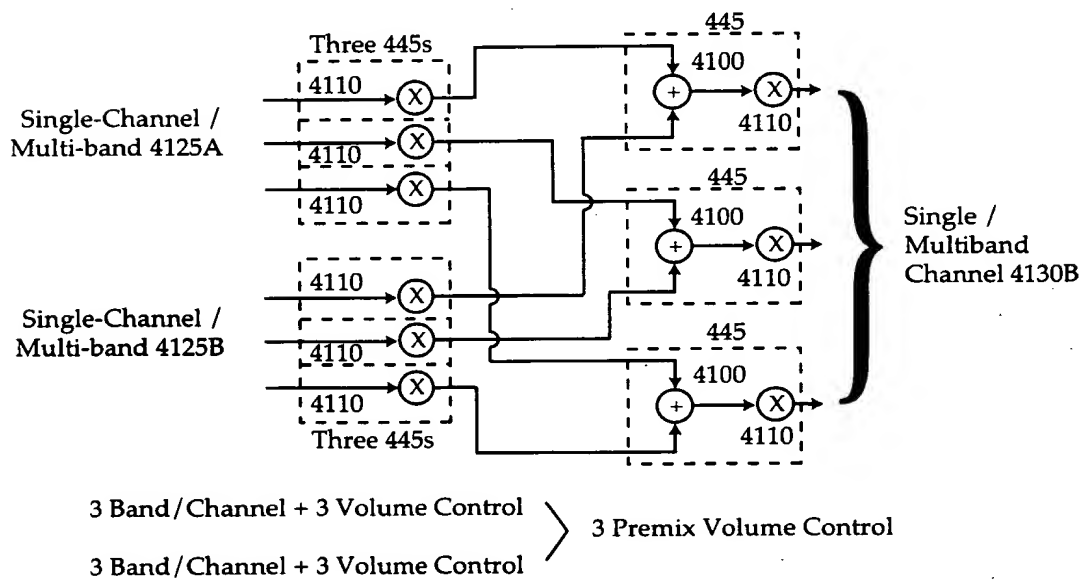
FIG. 42B

006211 51232/60



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FIG. 42D



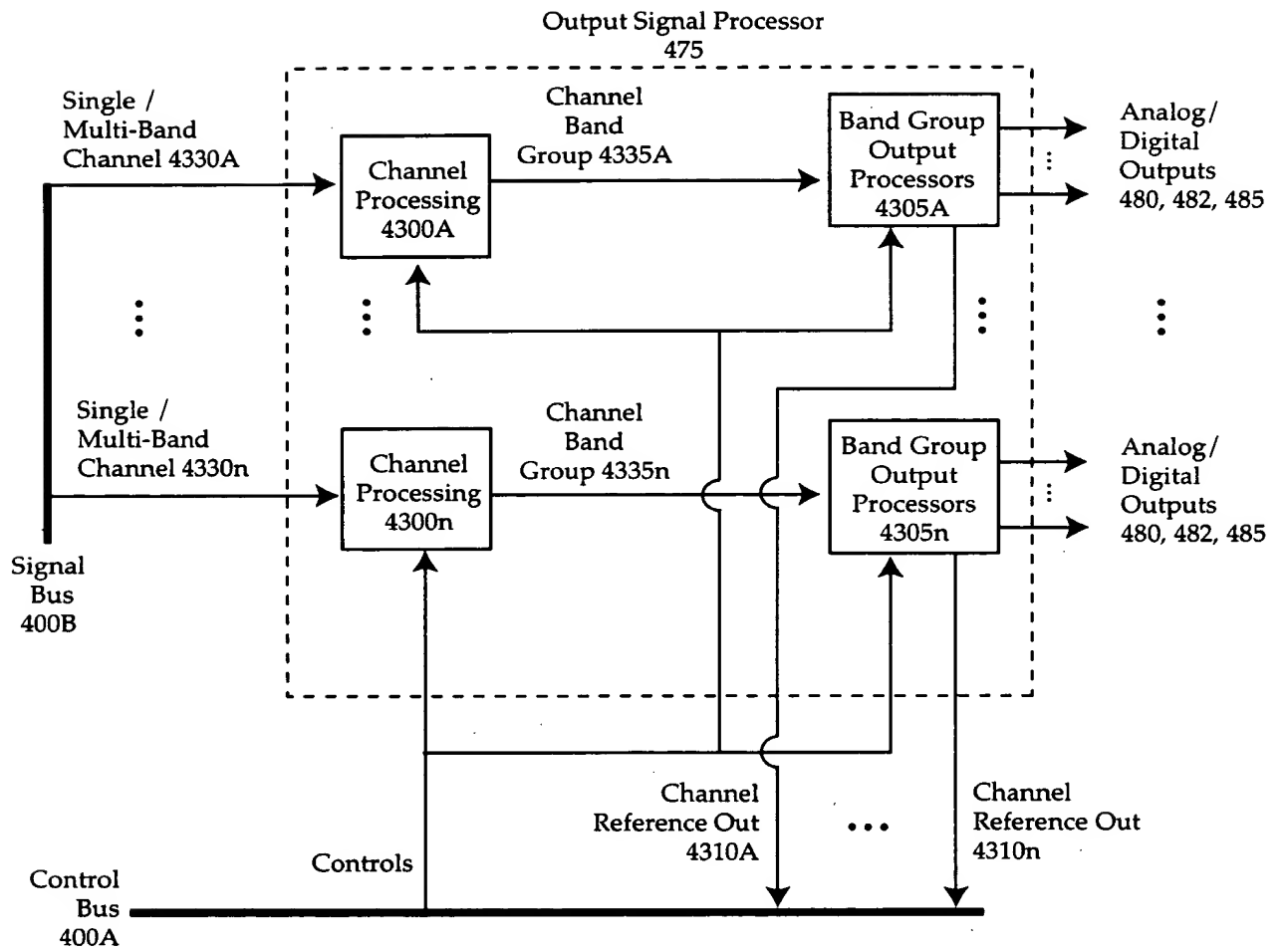


FIG. 43A

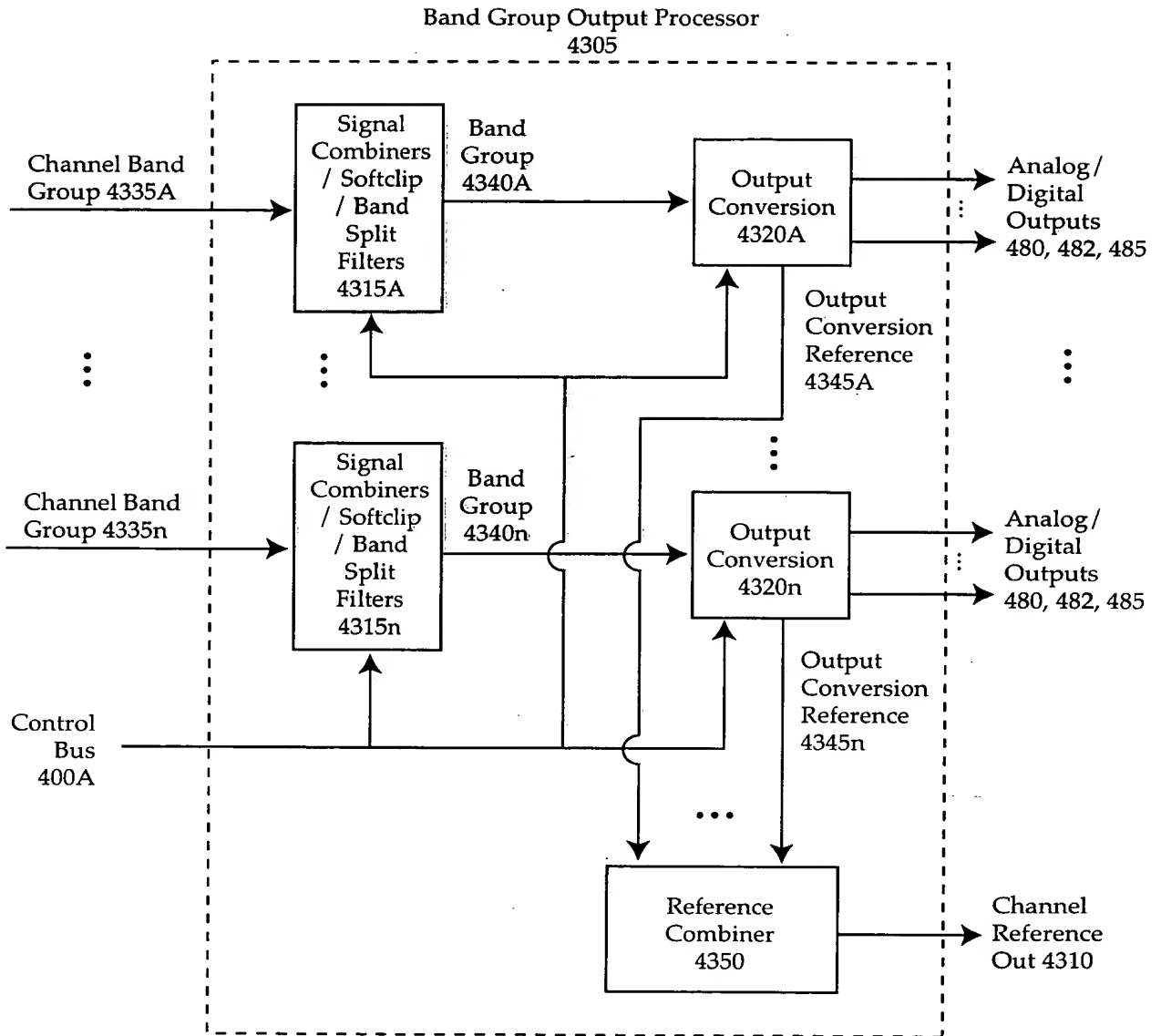


FIG. 43B

7

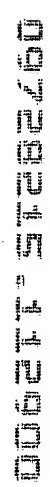


FIG. 45A

FIG. 45B

FIG. 45C

[illegible]



—

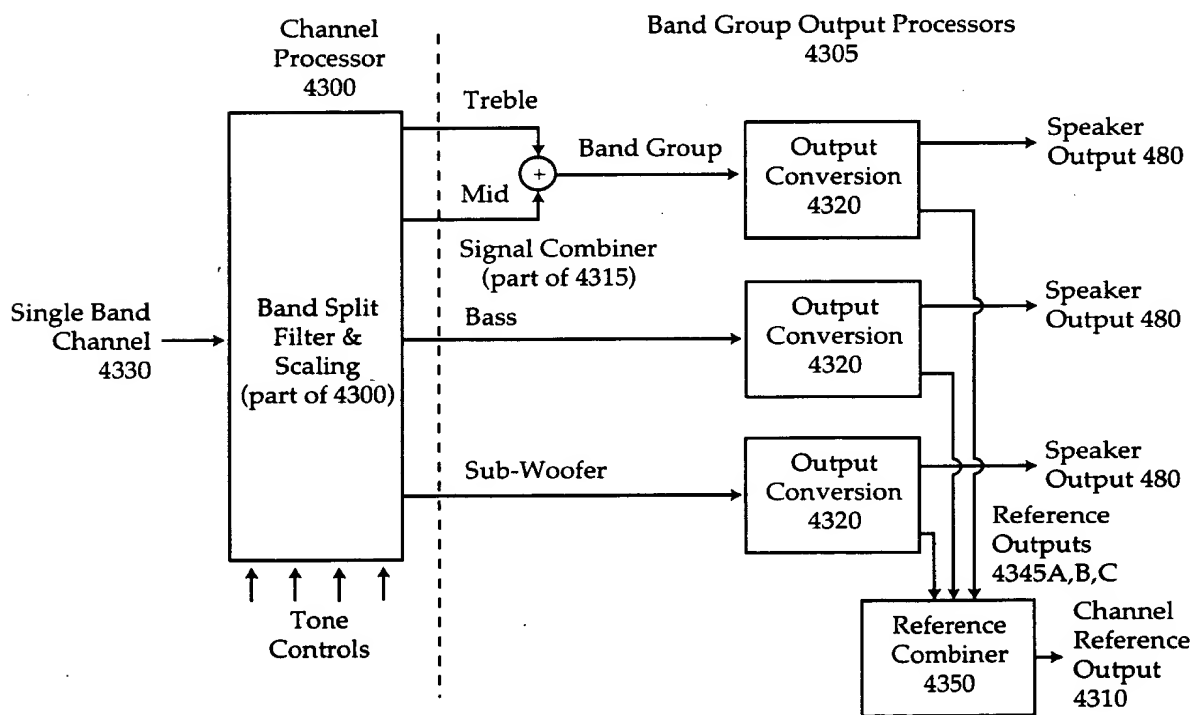


FIG. 45E

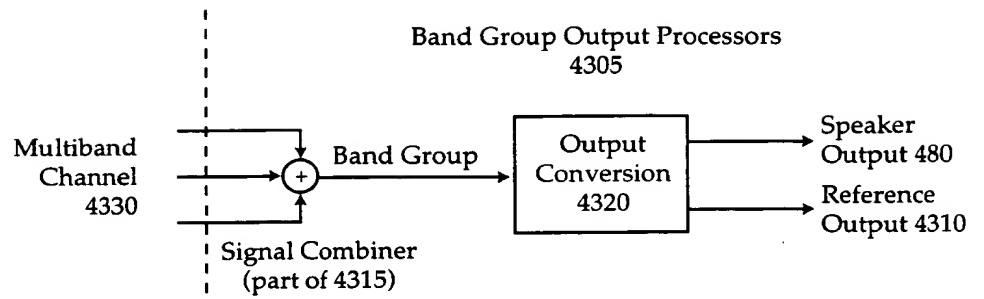


FIG. 45F

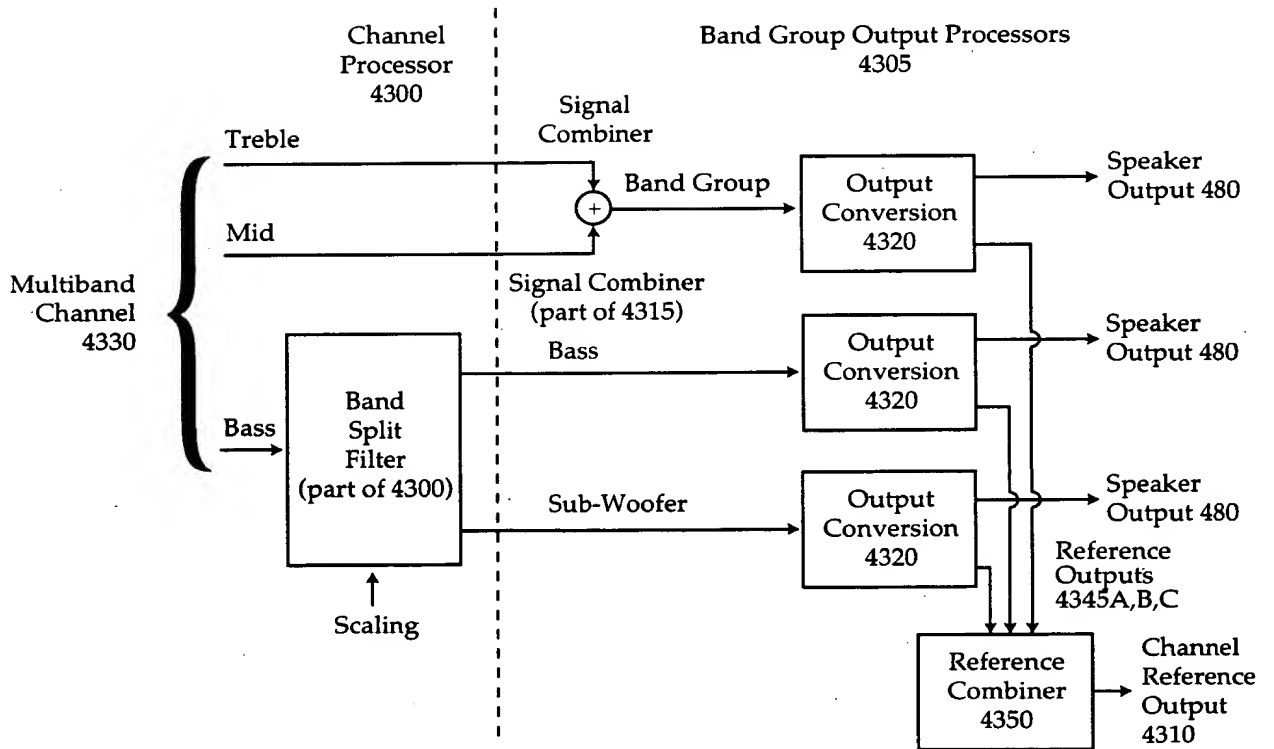


FIG. 45G



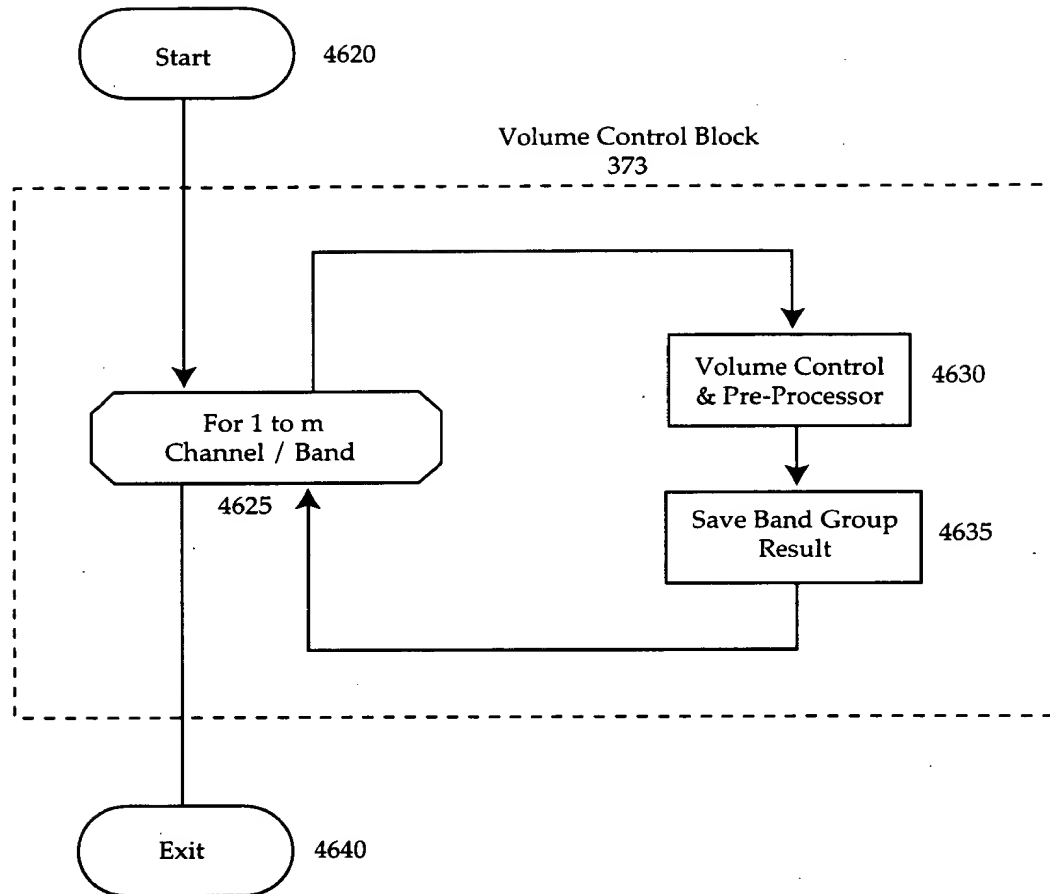


FIG. 46

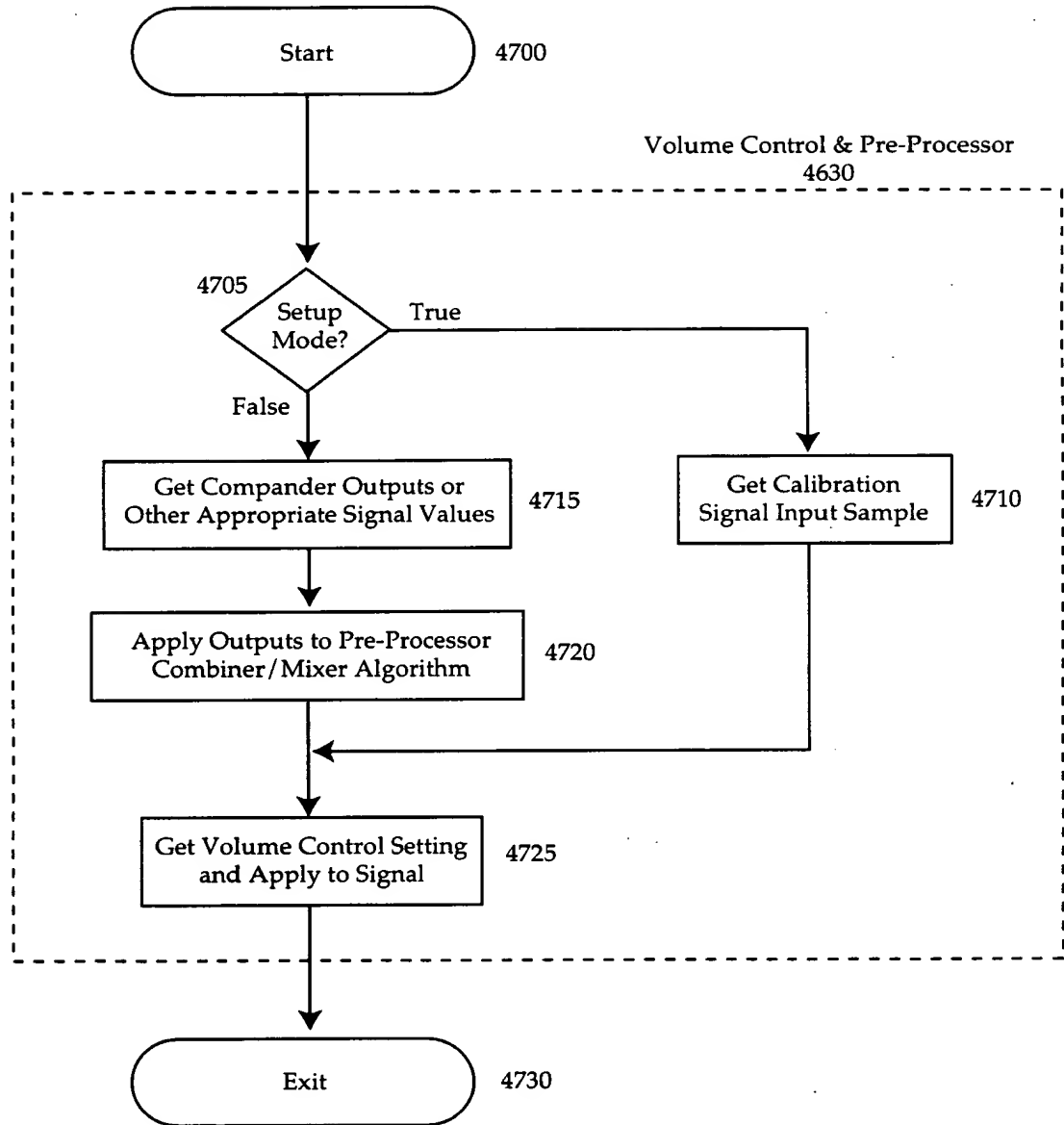


FIG. 47

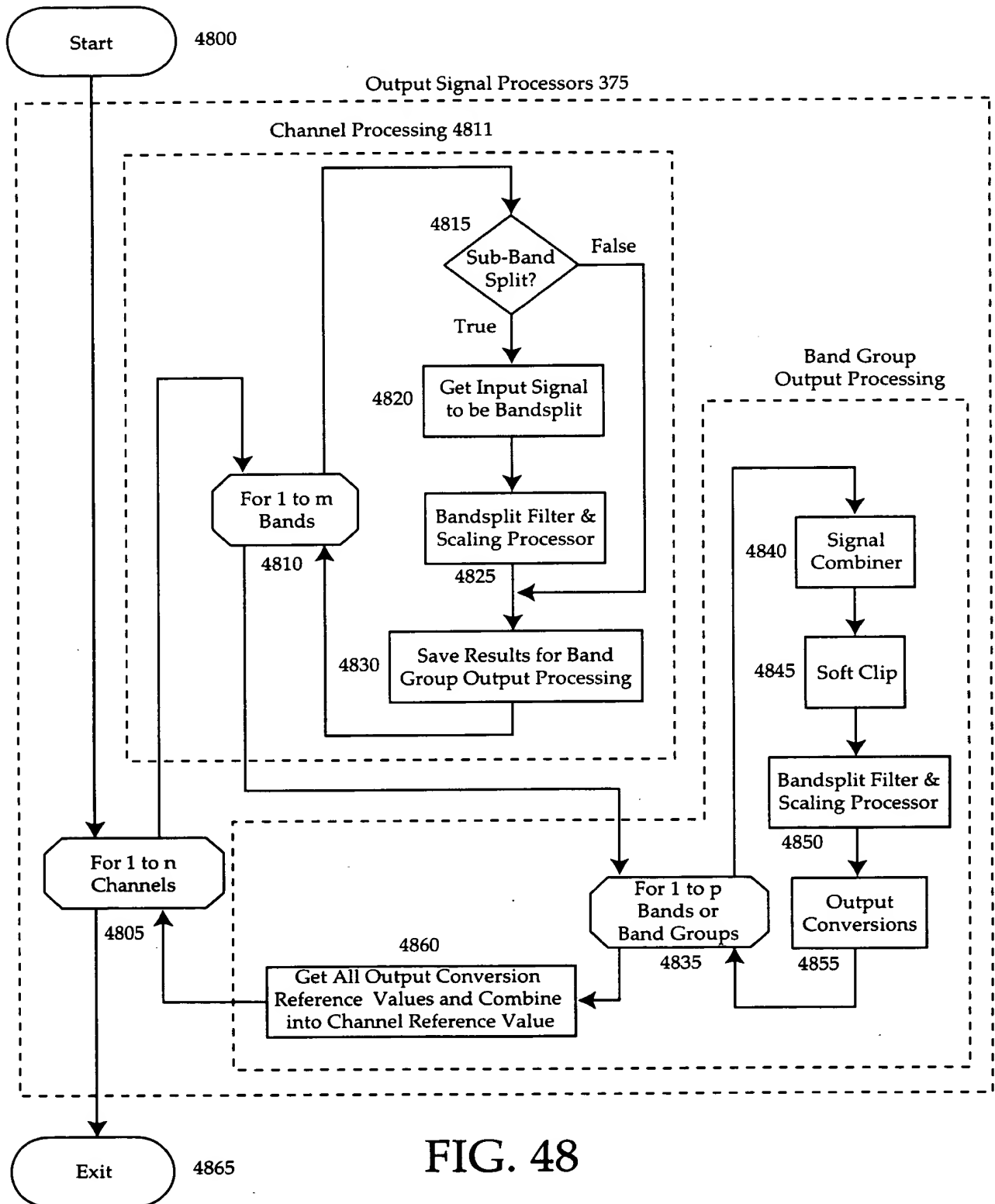


FIG. 48

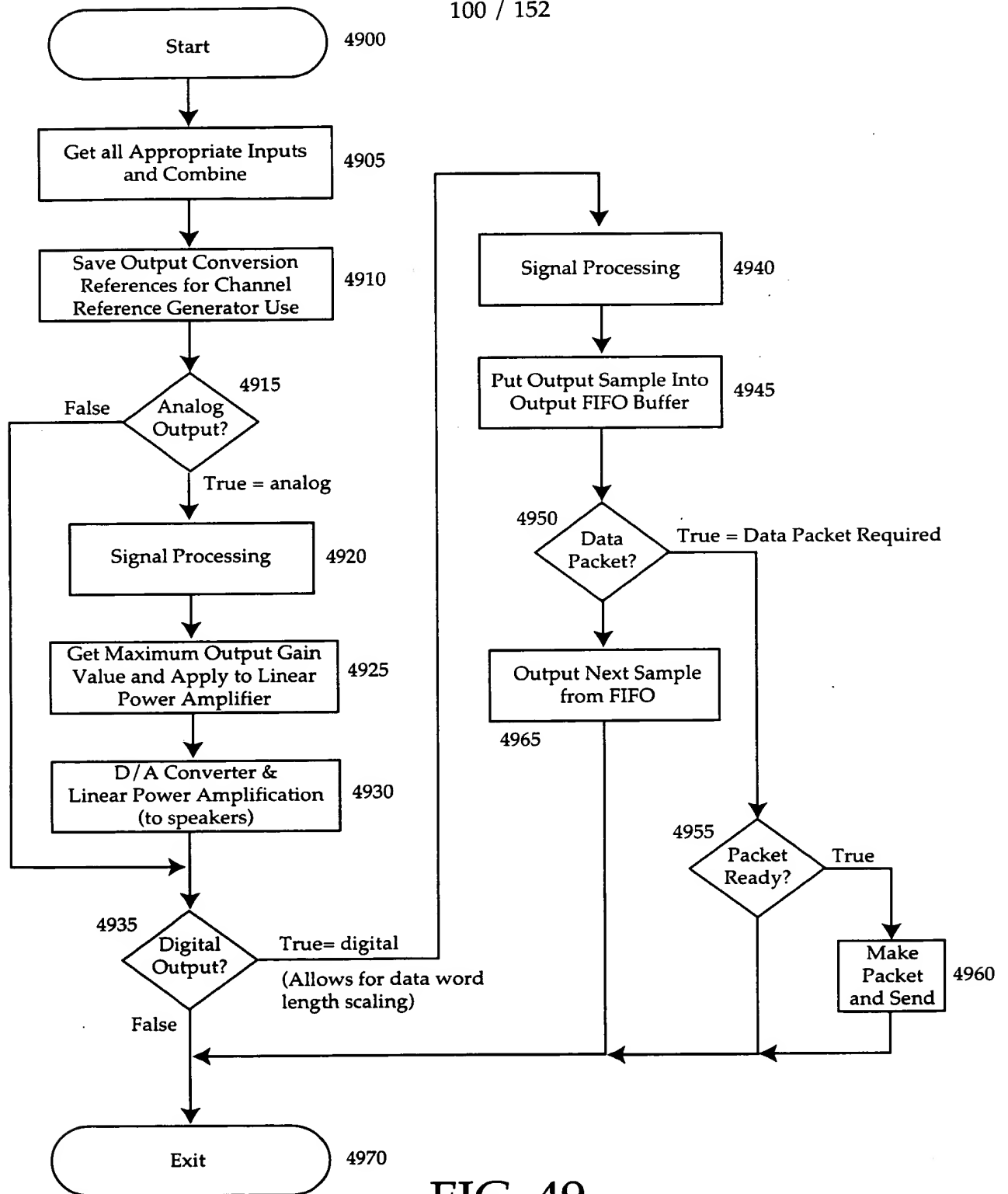


FIG. 49

## Calibrator / Annunciator 420

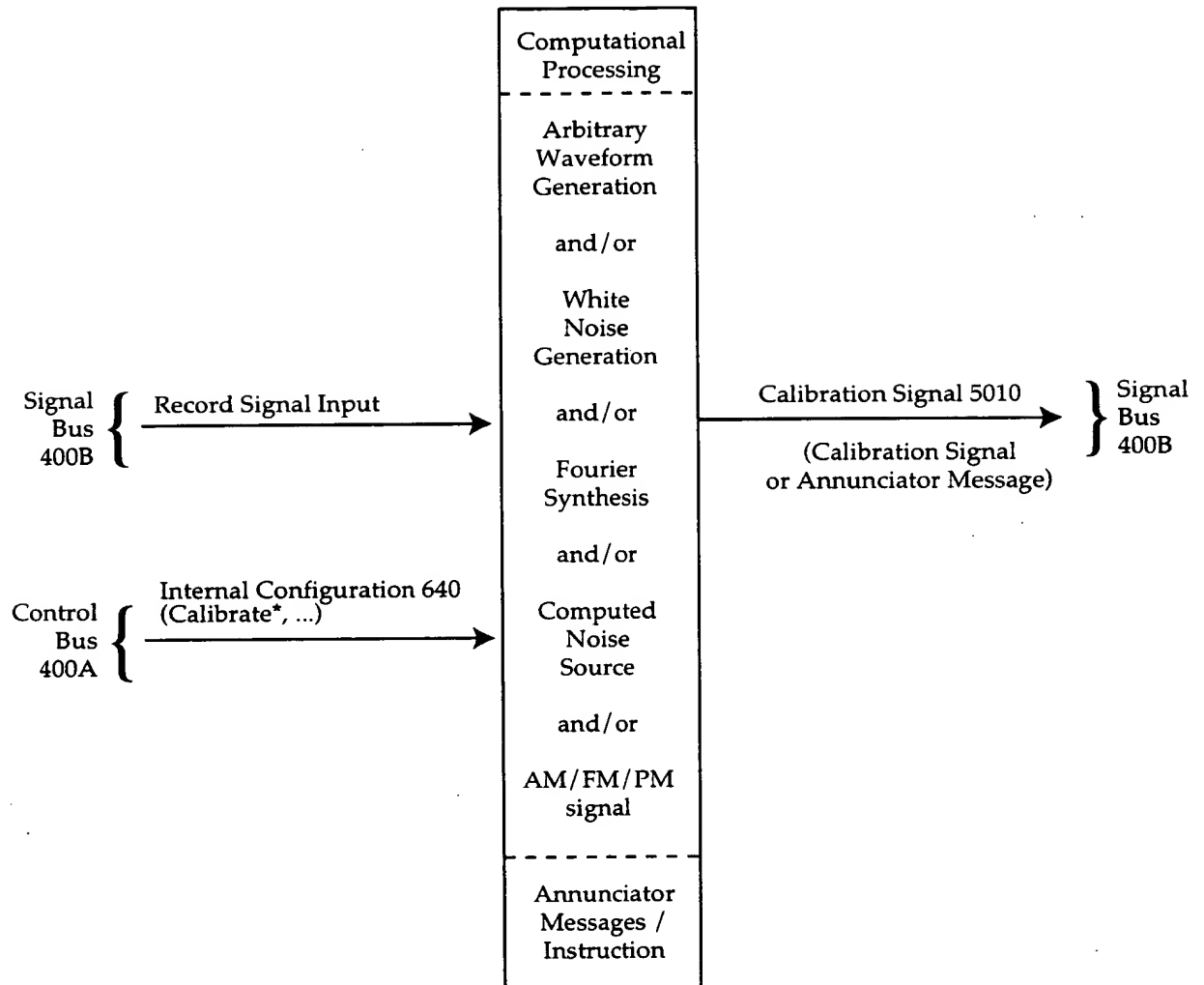


FIG. 50

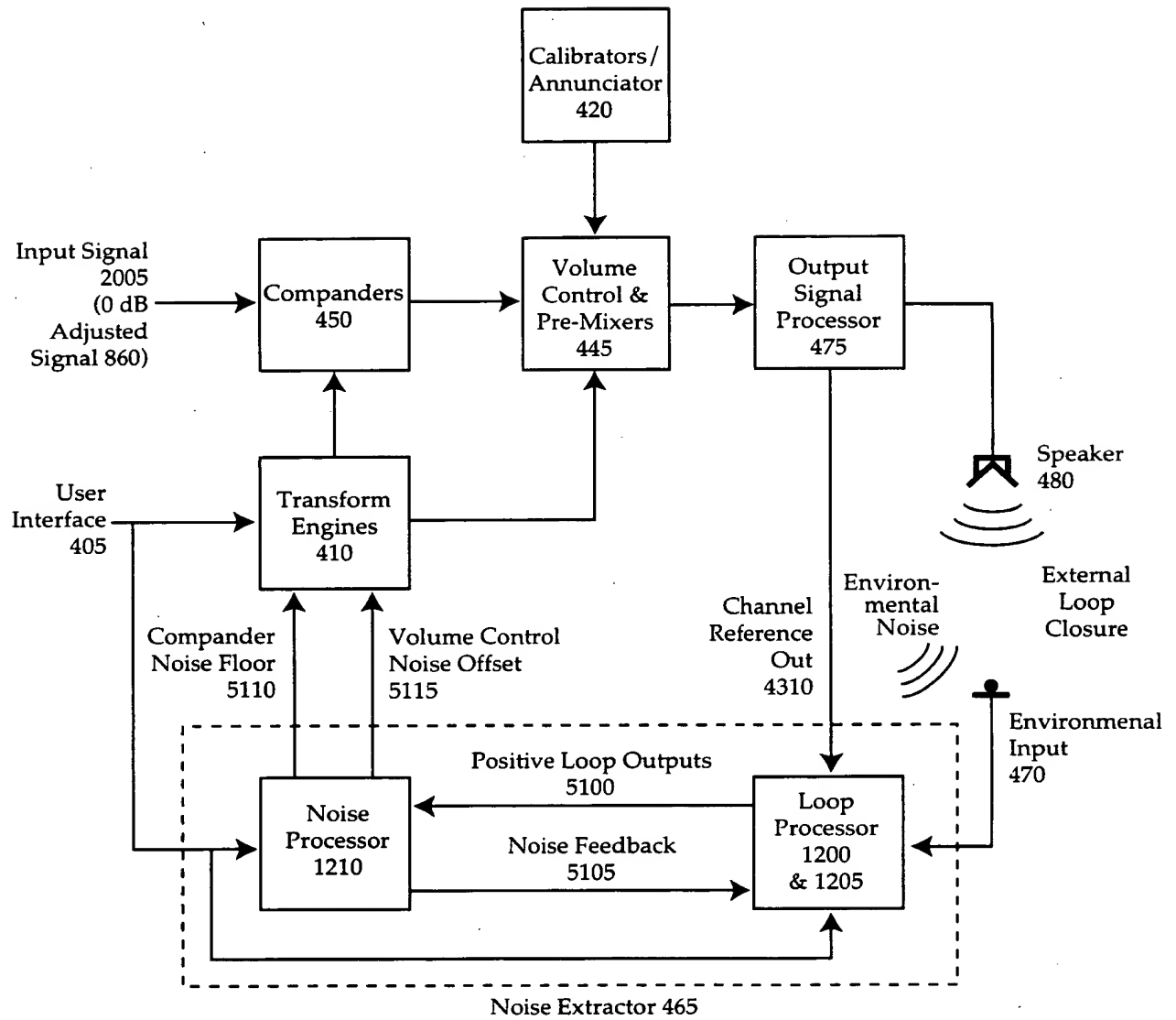


FIG. 51

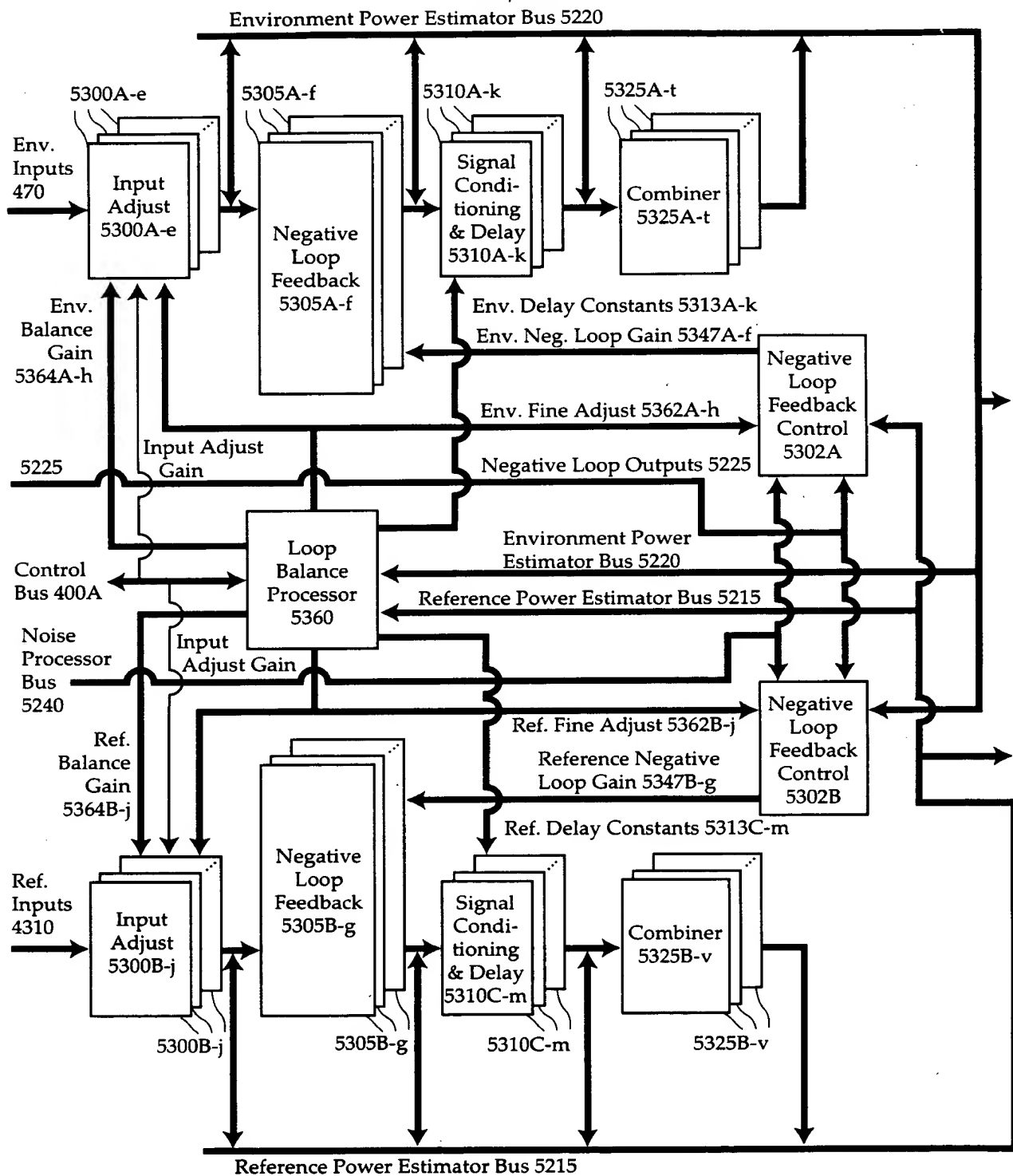


FIG. 52A

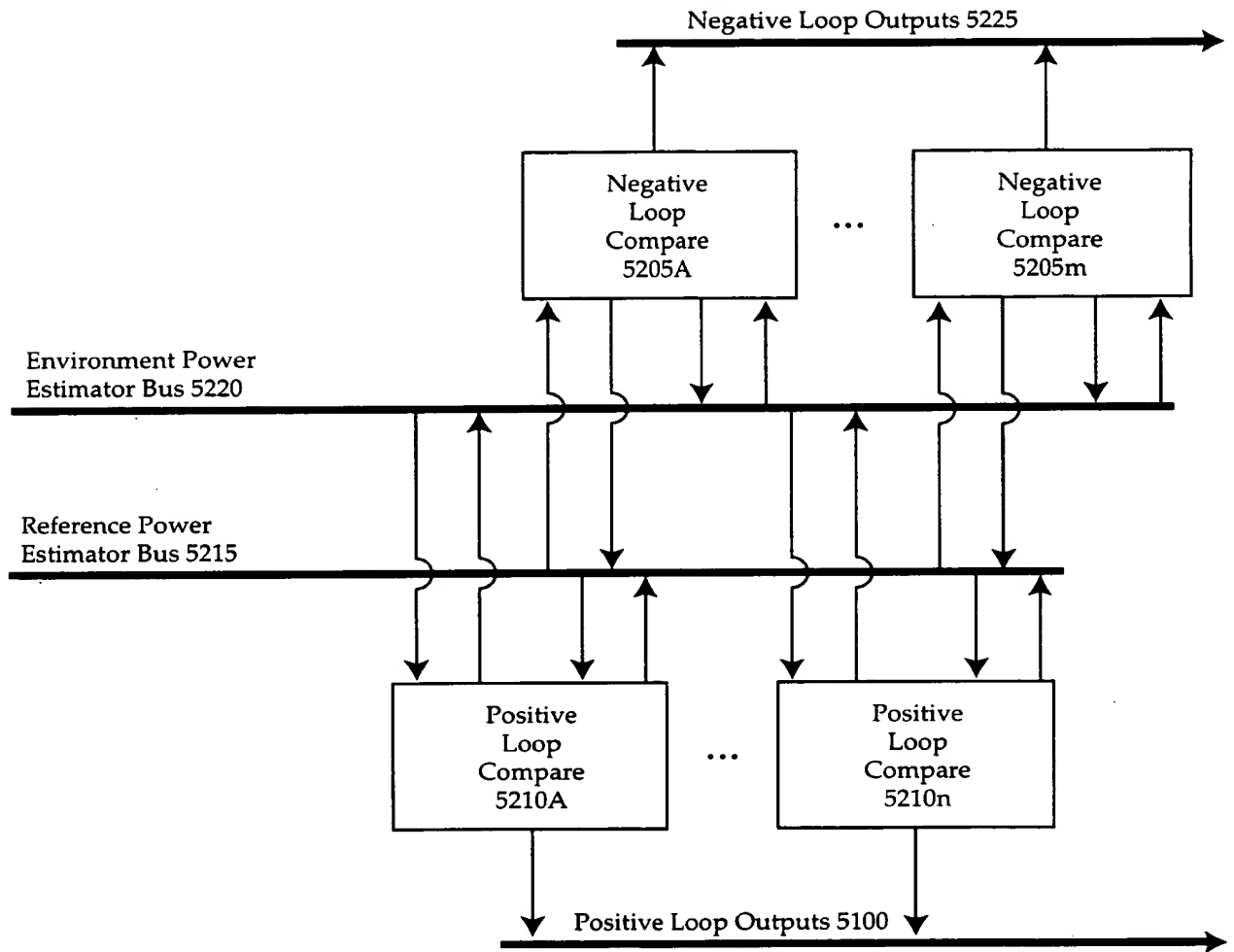


FIG. 52B



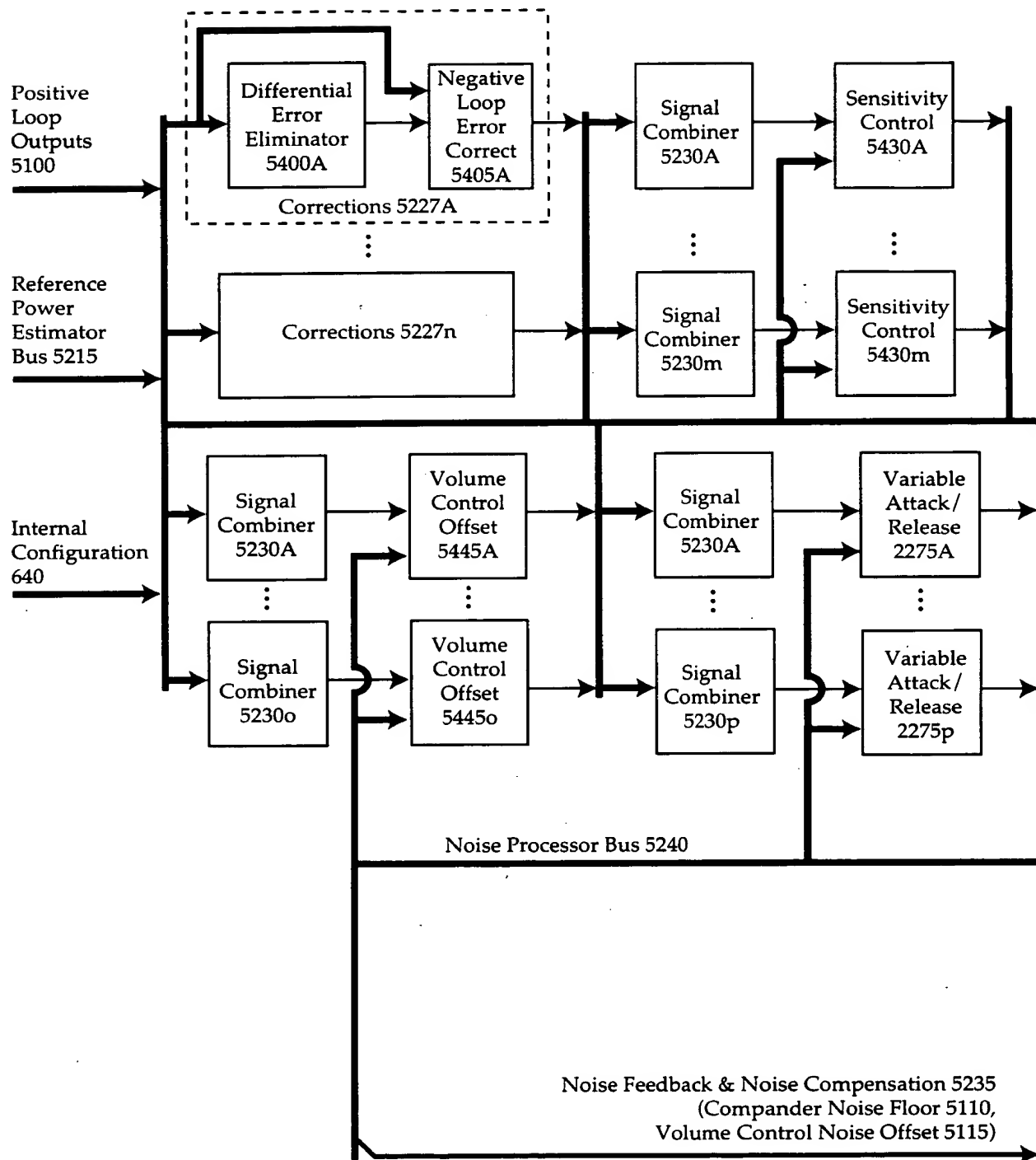


FIG. 52C

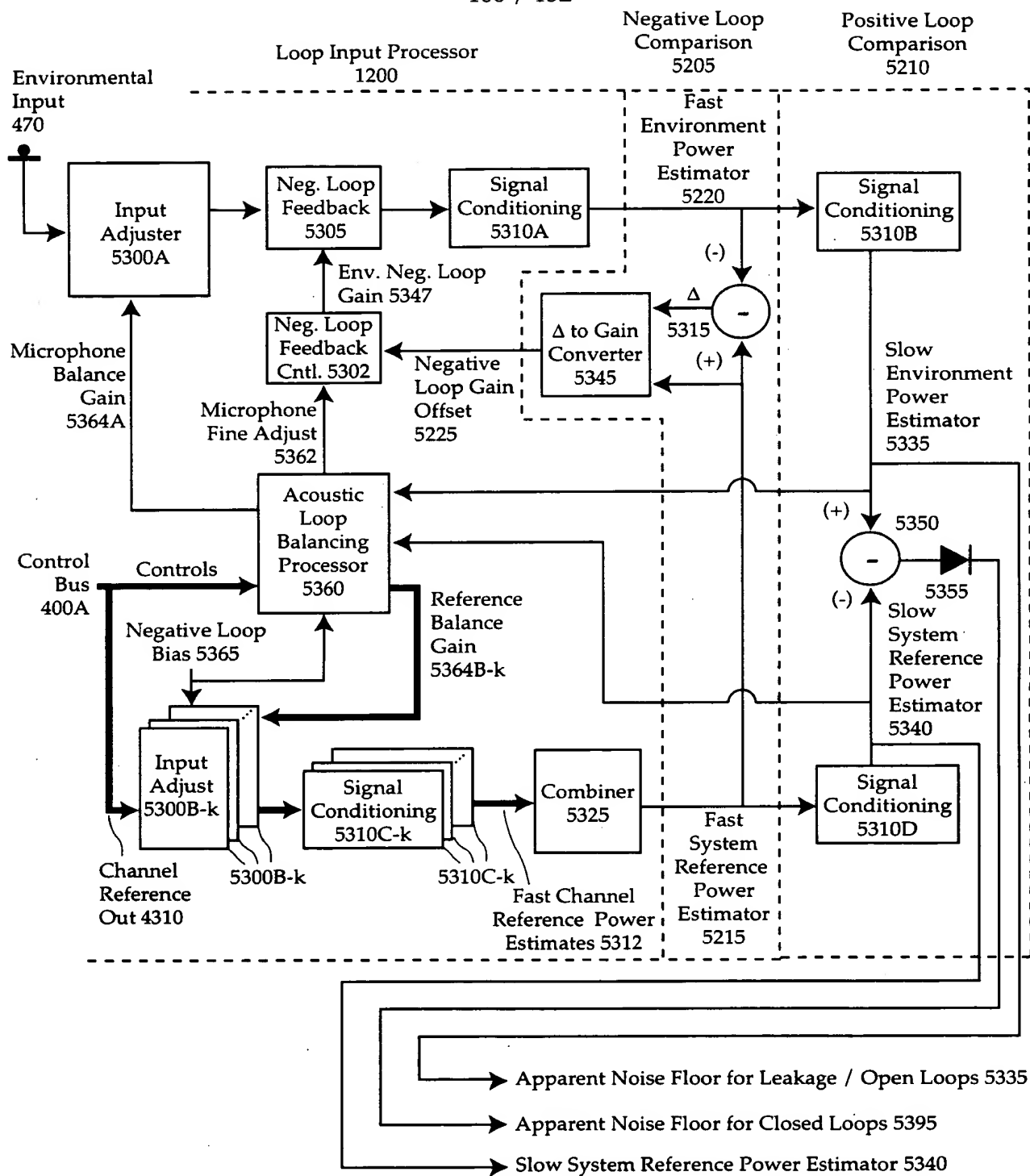


FIG. 53A

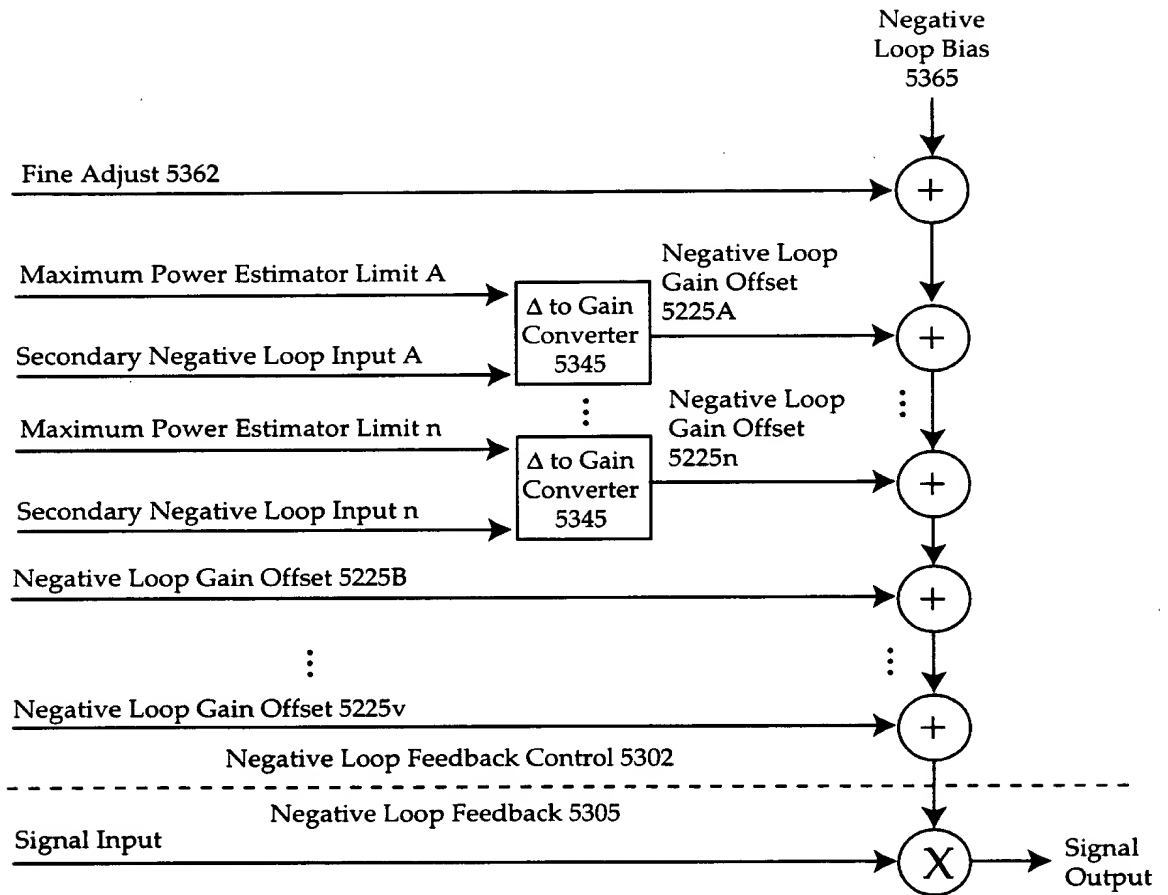


FIG. 53B

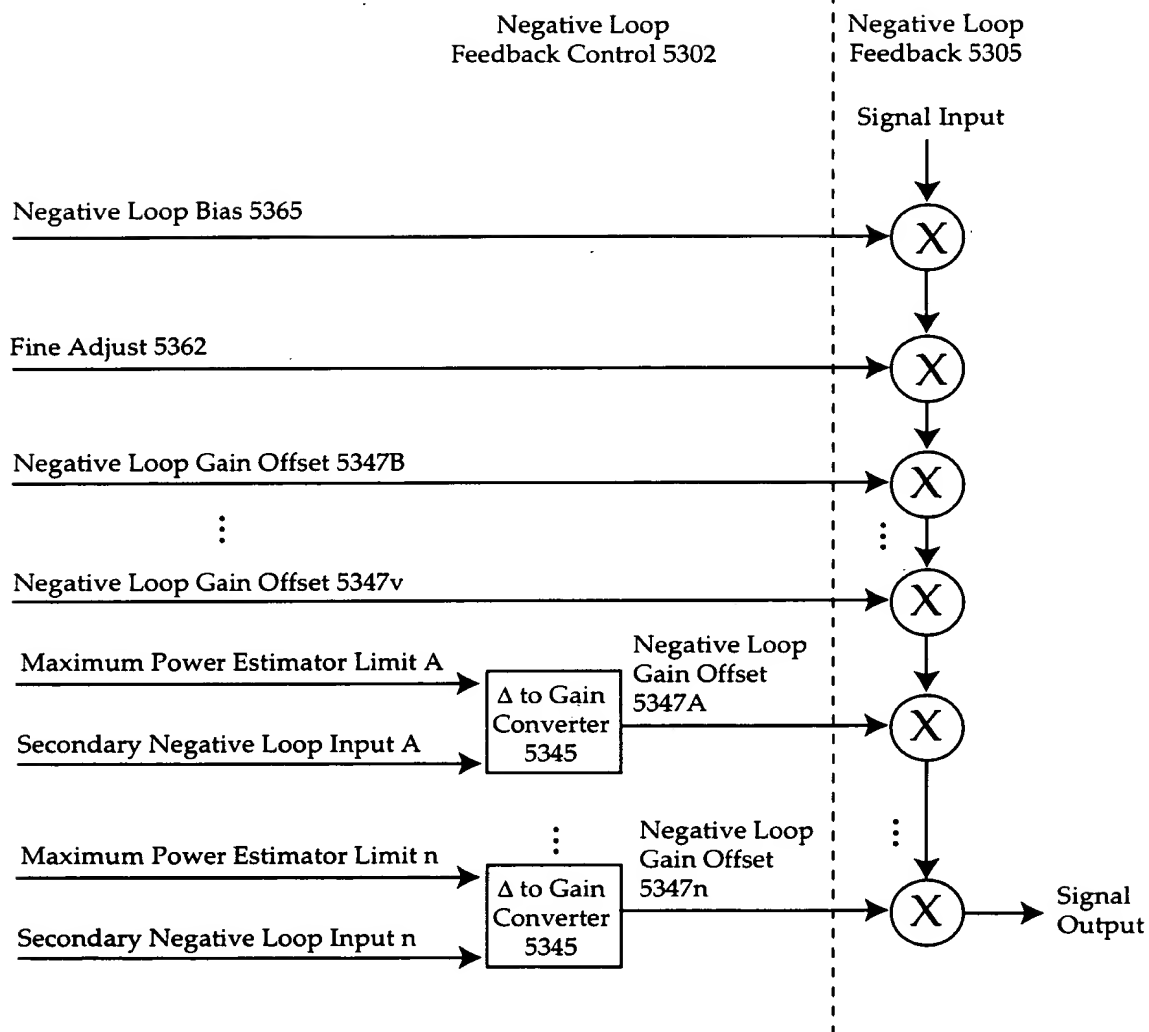


FIG. 53C

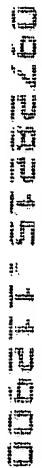


FIG. 53D

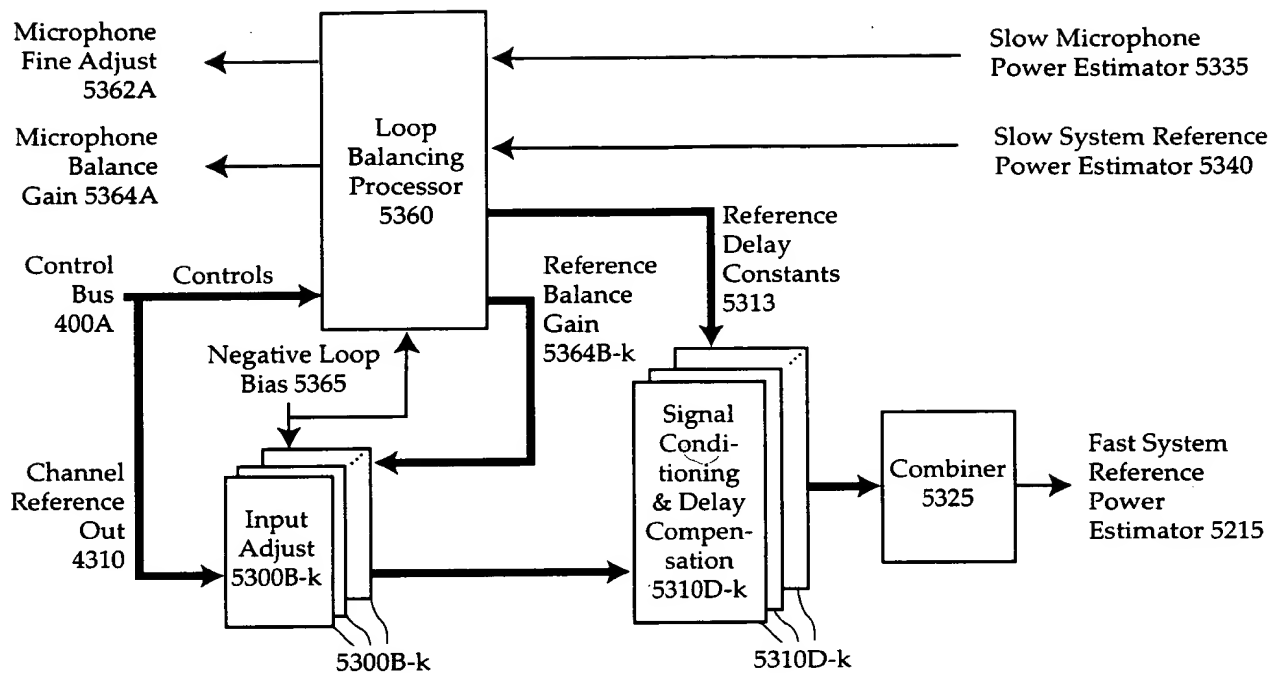


FIG. 53E

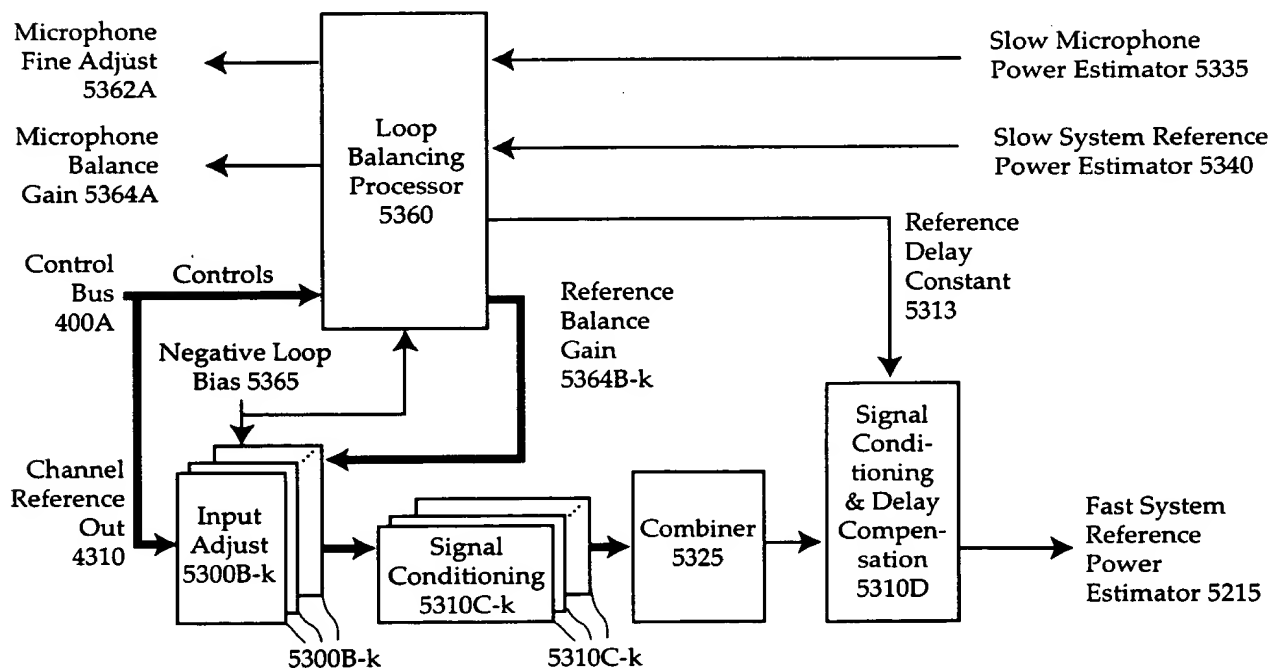


FIG. 53F

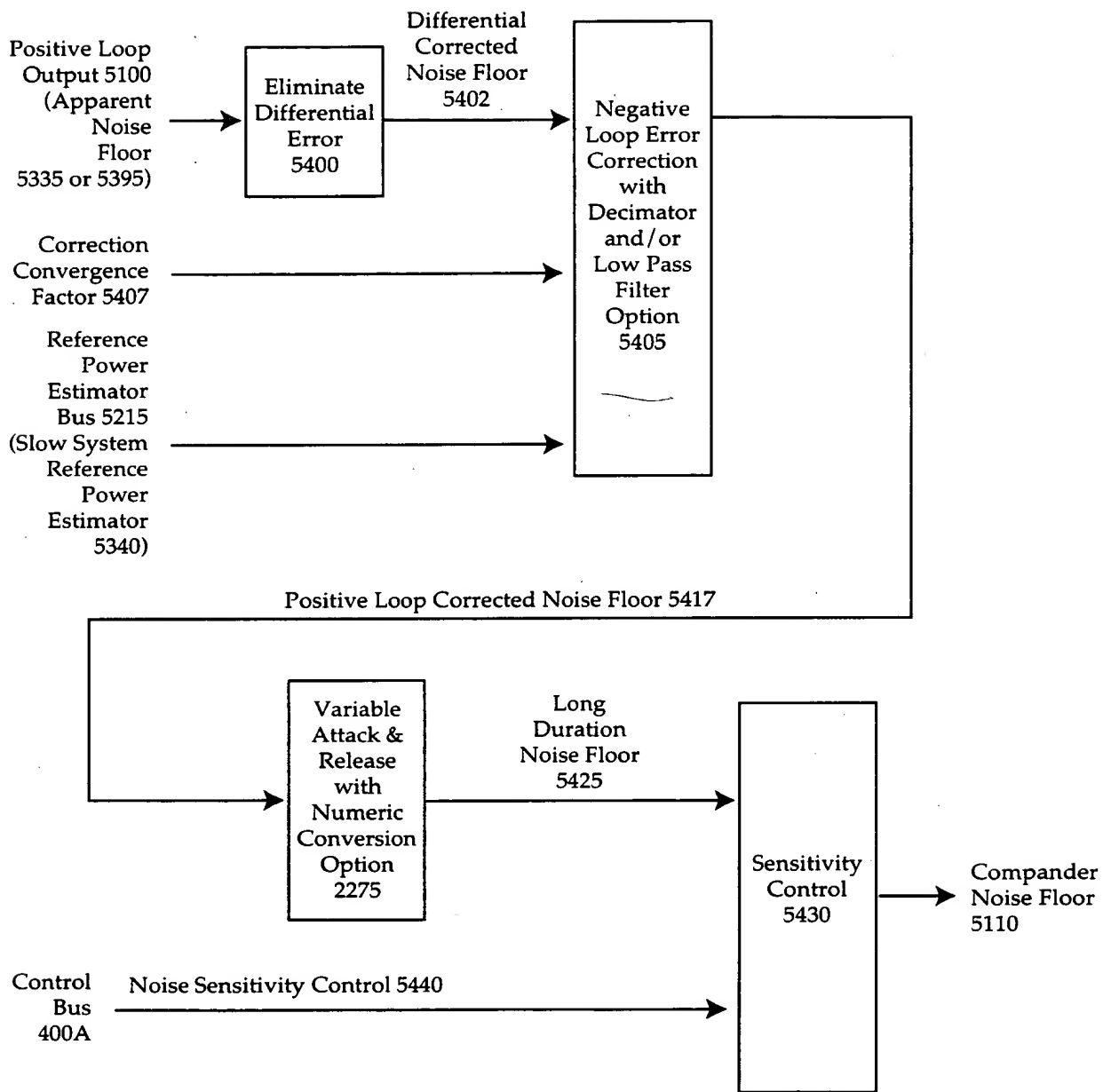


FIG. 54A

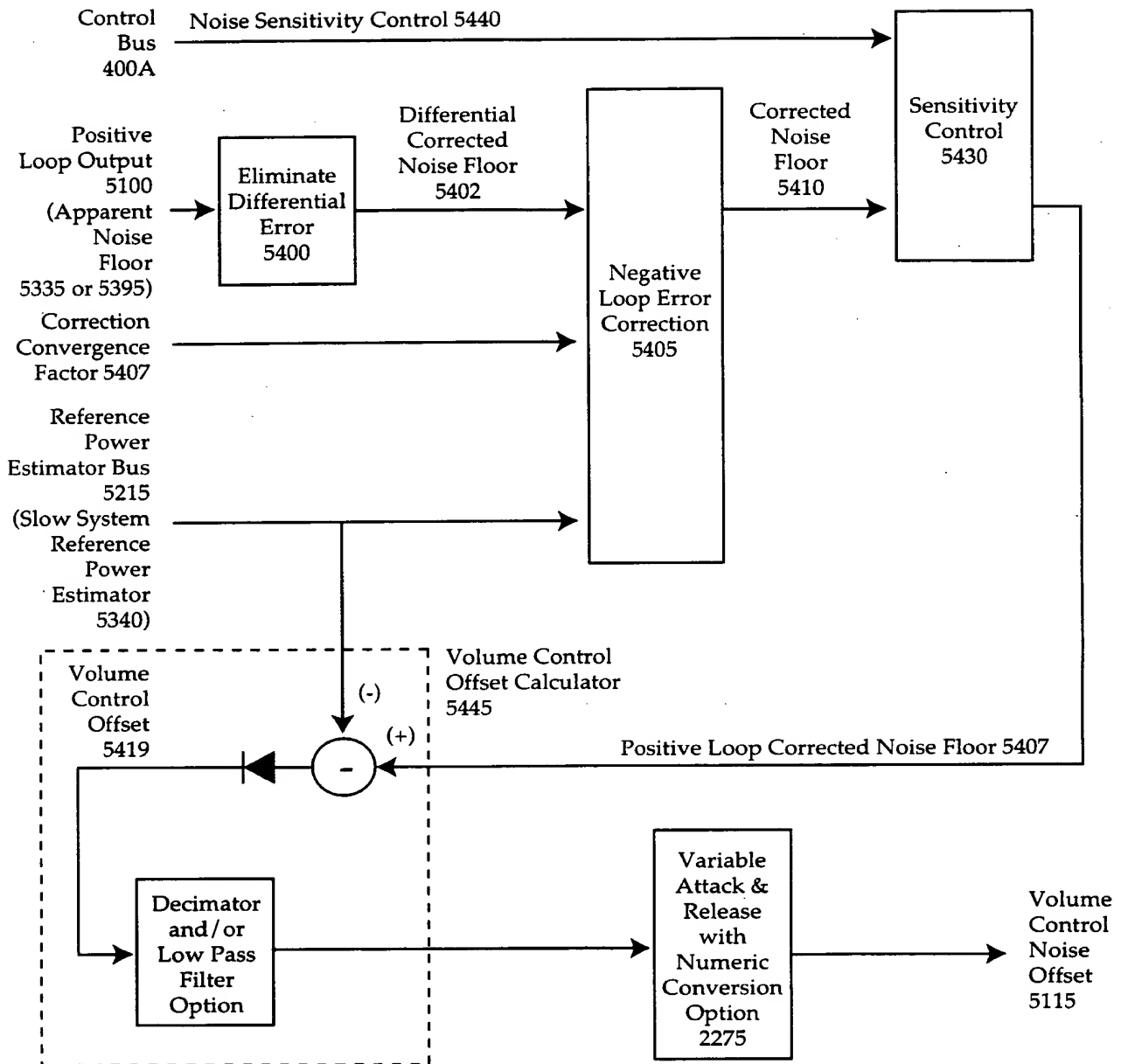


FIG. 54B



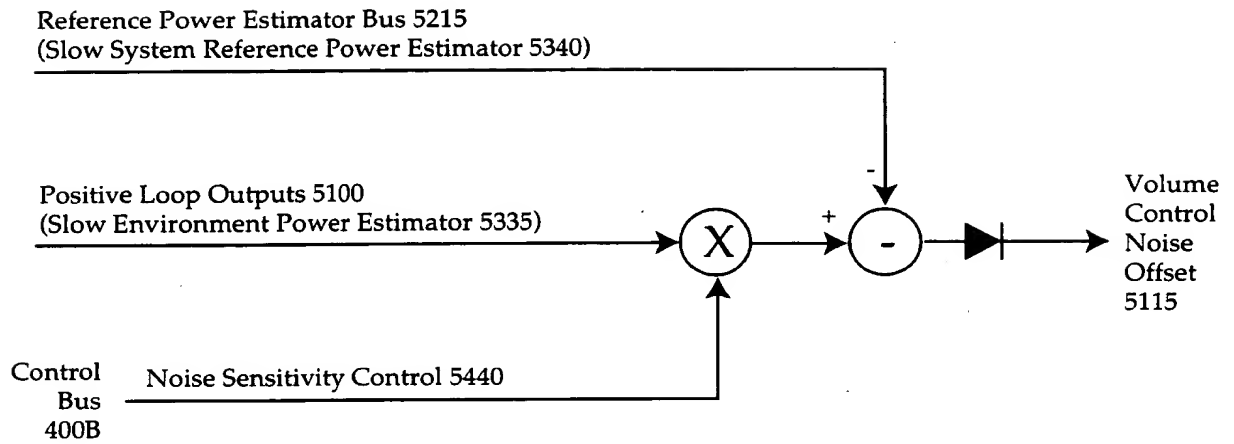


FIG. 54C

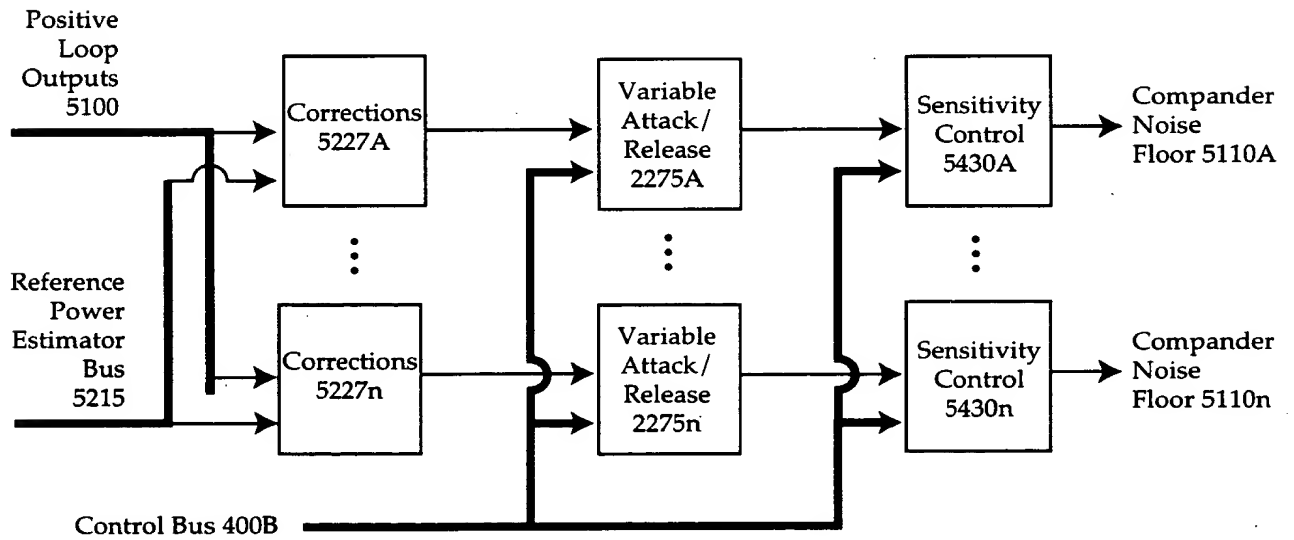


FIG. 54D

000001511000

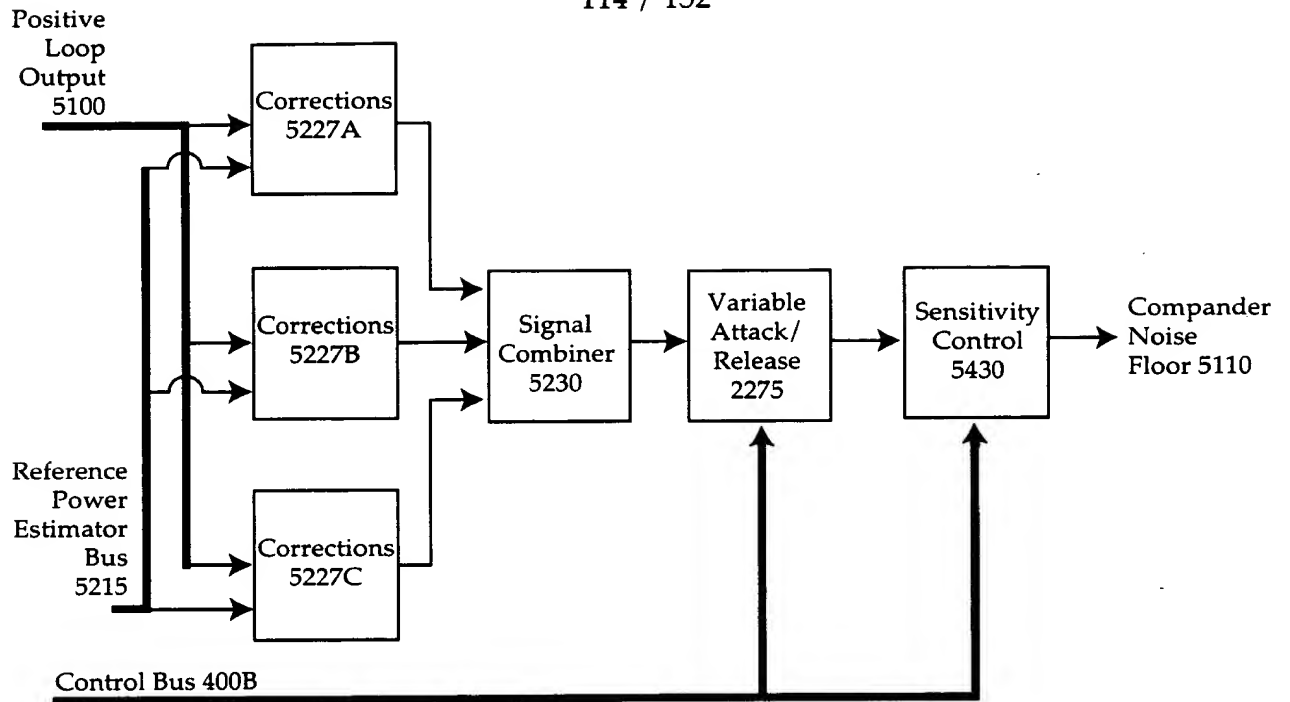


FIG. 54E

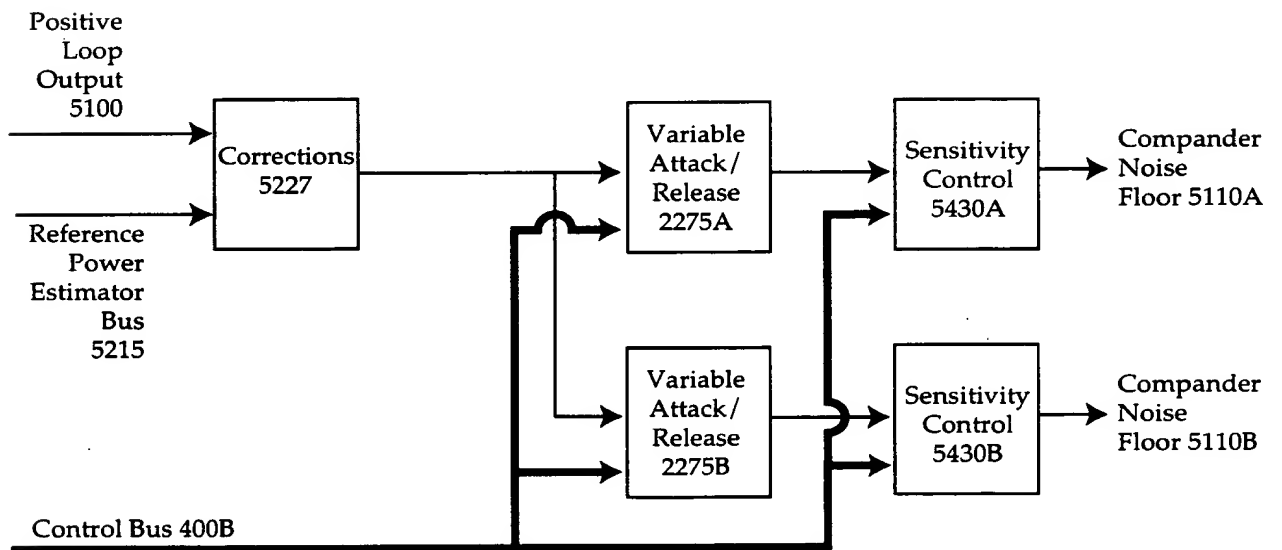


FIG. 54F

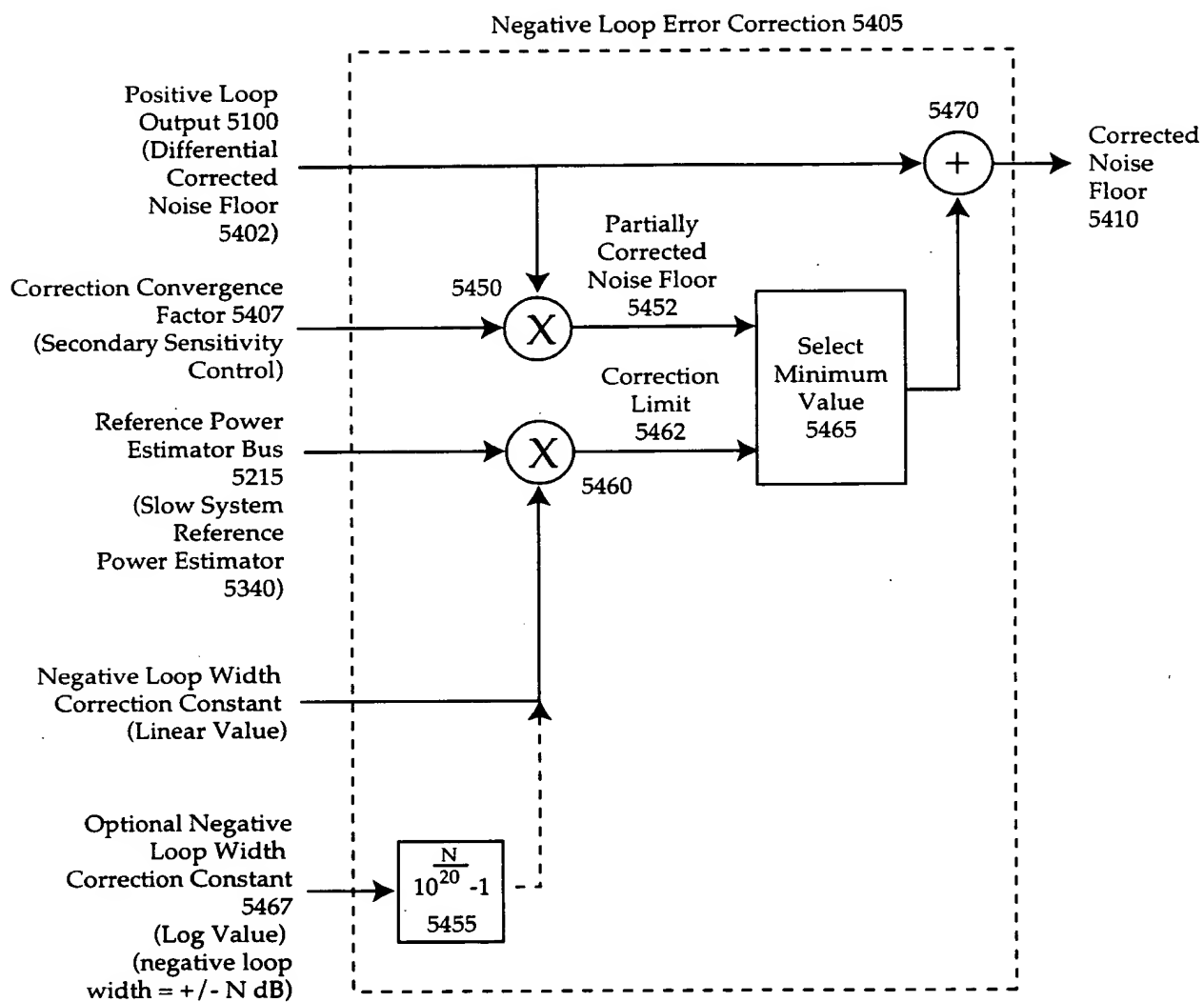


FIG. 54G

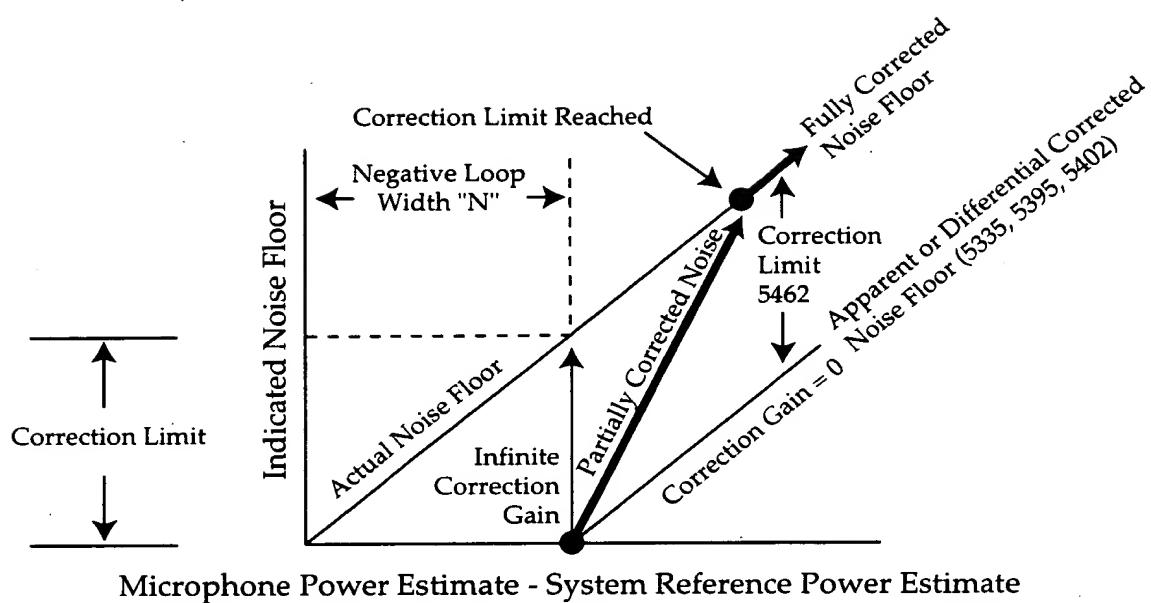


FIG. 54H

00627-512900

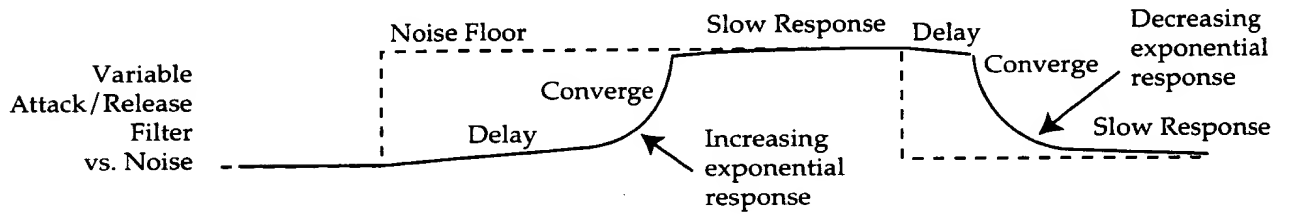
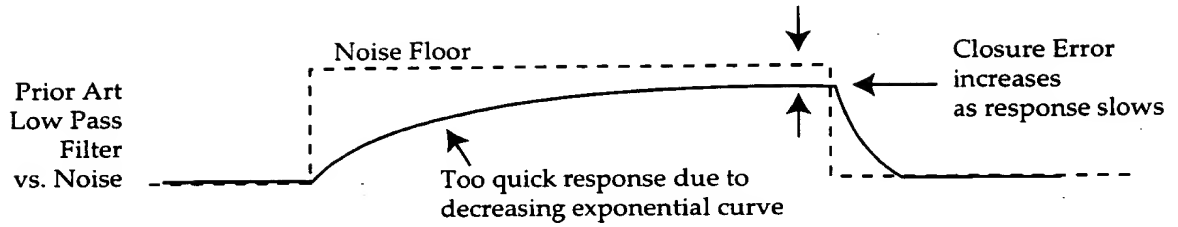


FIG. 55A

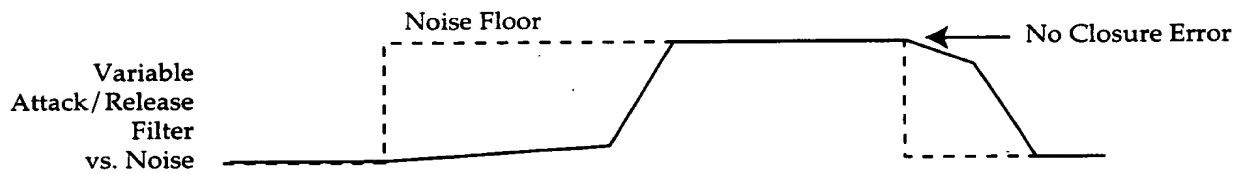
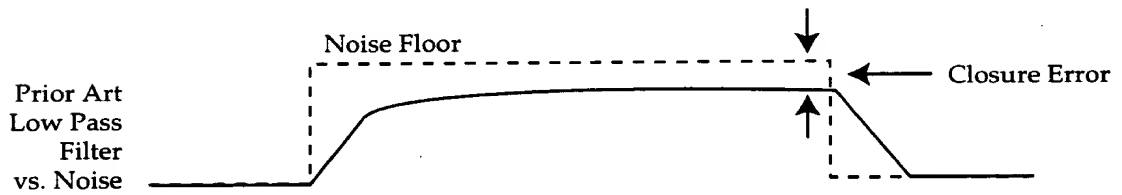


FIG. 55B

00621-5482/60

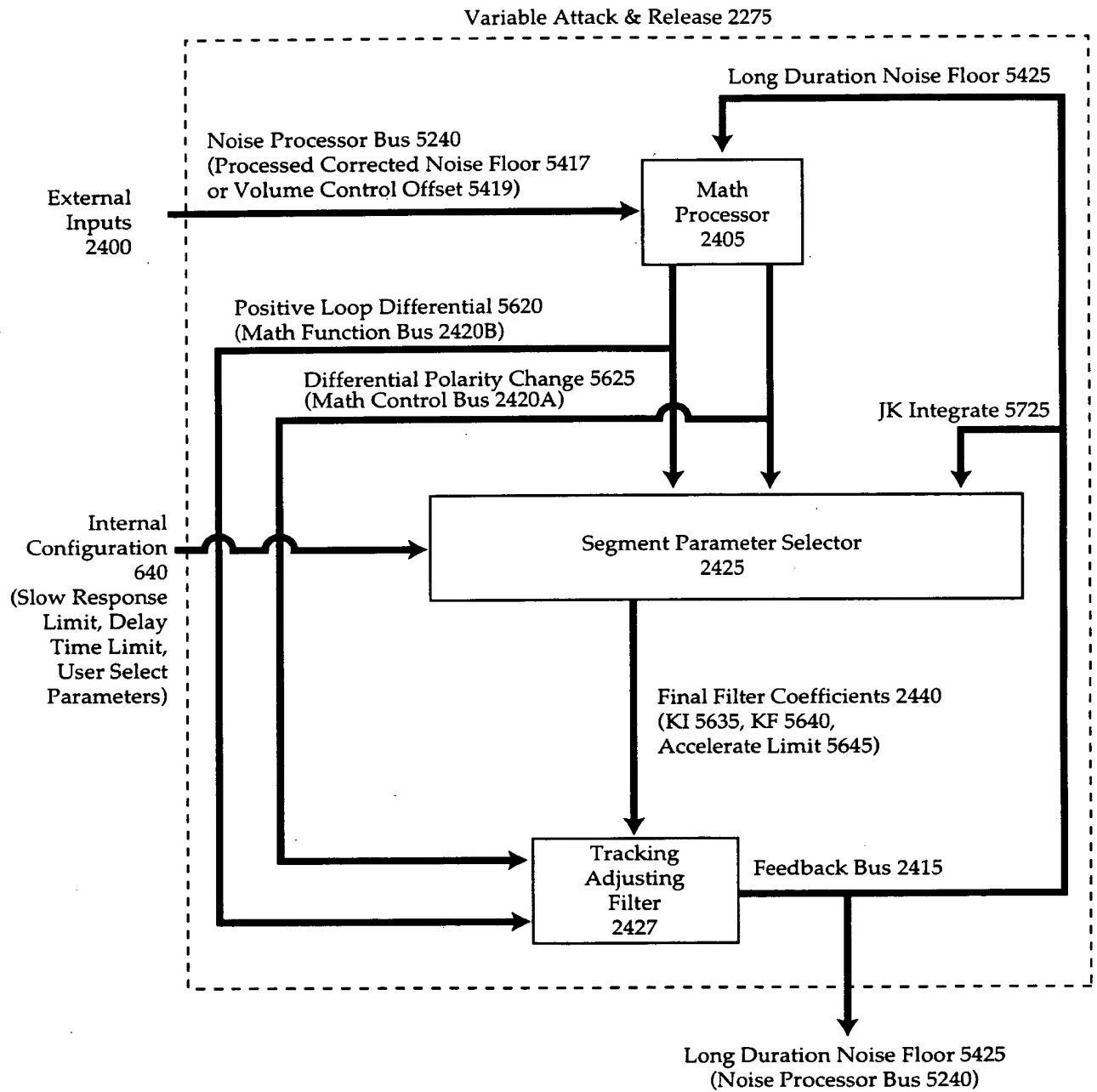


FIG. 55C

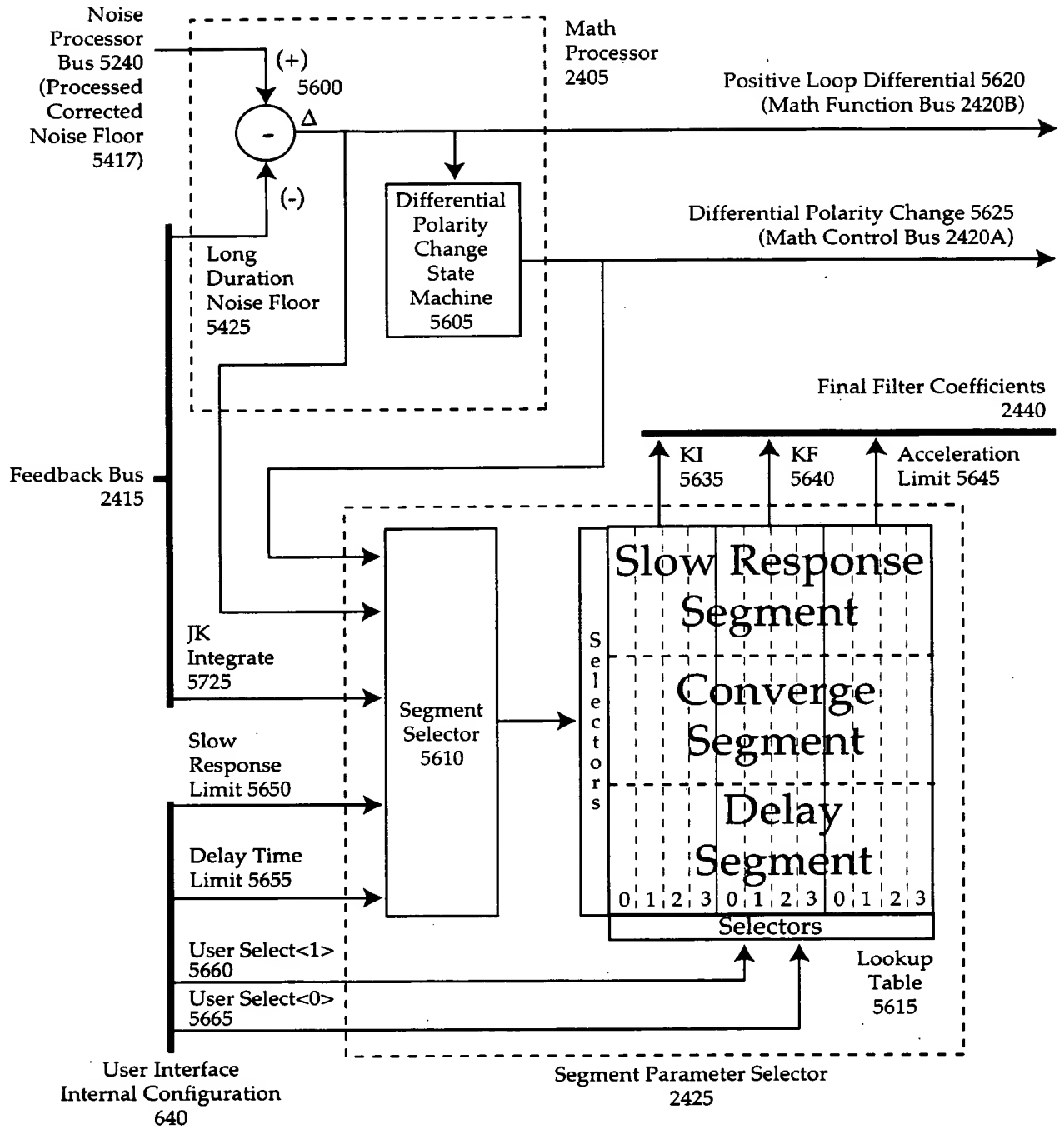
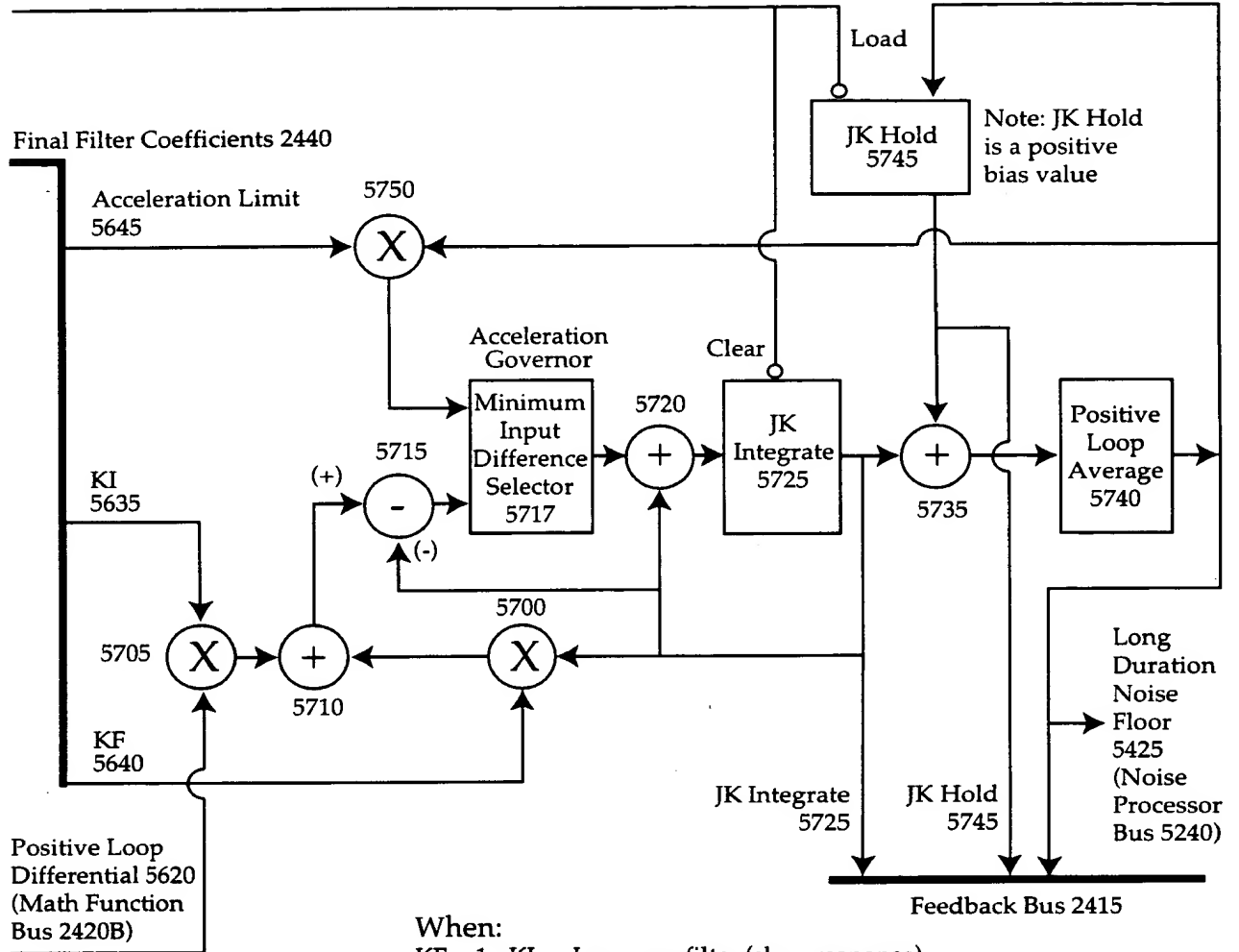


FIG. 56

Differential Polarity Change 5625  
(Math Control Bus 2420A)



When:

$KF = 1 - KI$  Low pass filter (slow response)  
 $KF = 1$  Integrator (delay response)  
 $KF > 1$  Accelerating Integrator (converge)

FIG. 57A



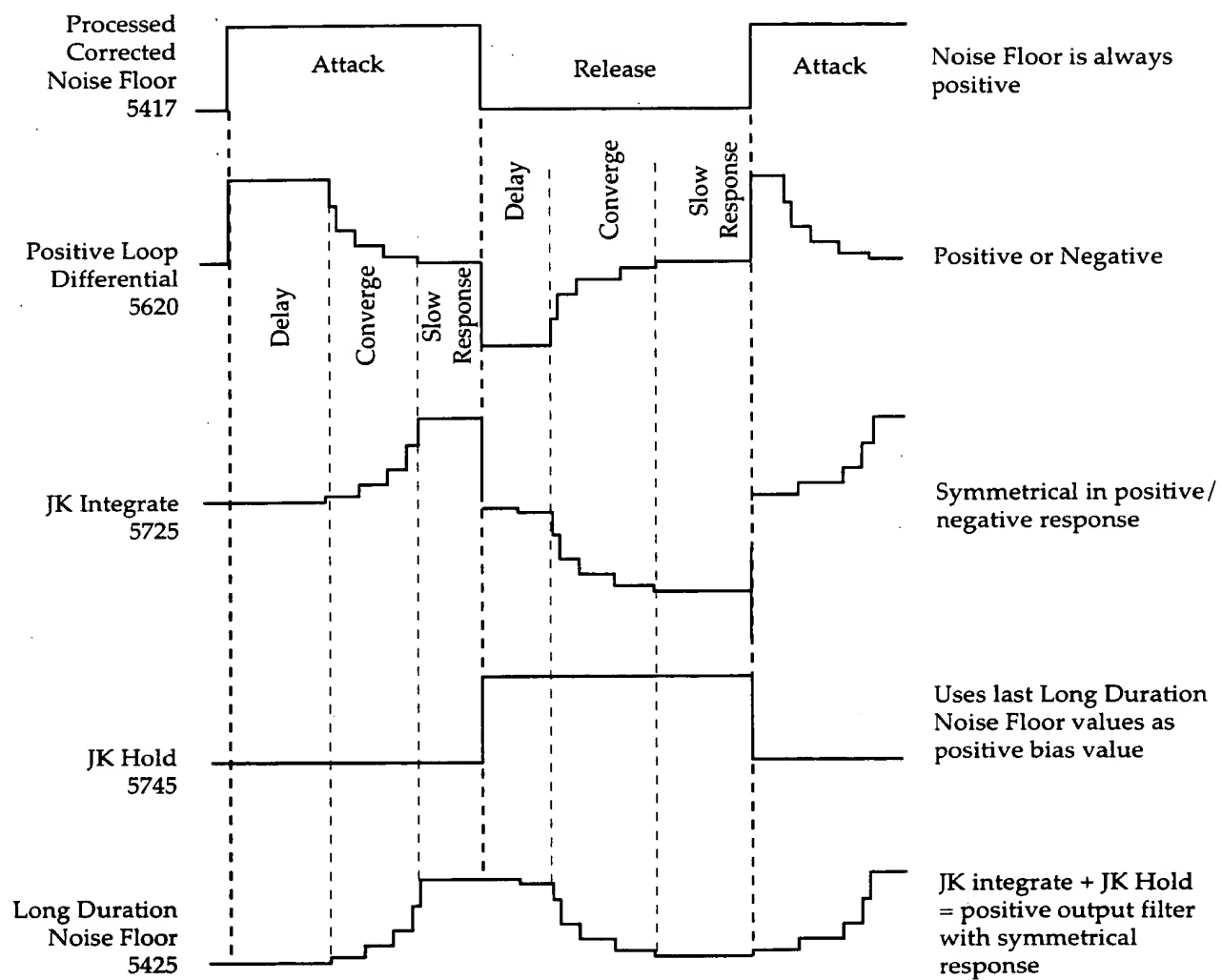


FIG. 57B

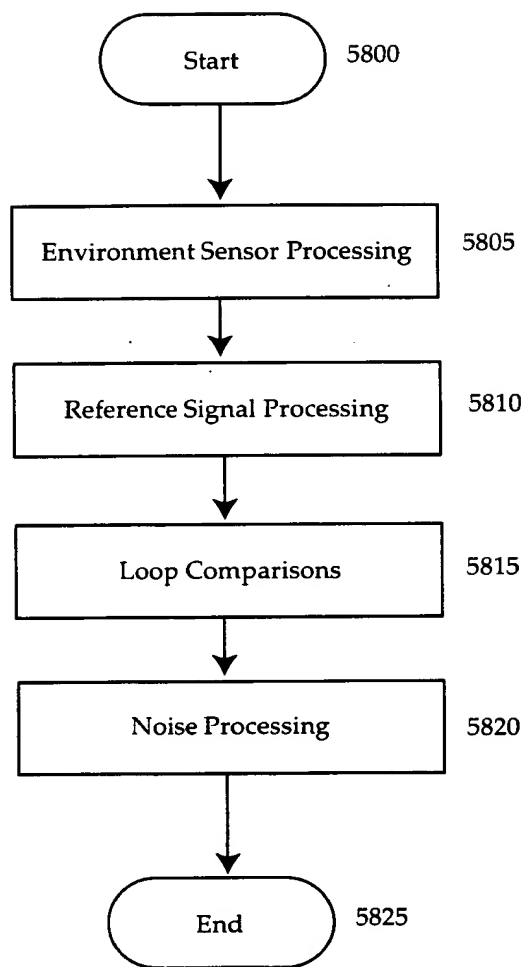


FIG. 58

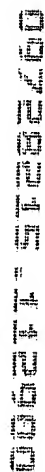


FIG. 59A

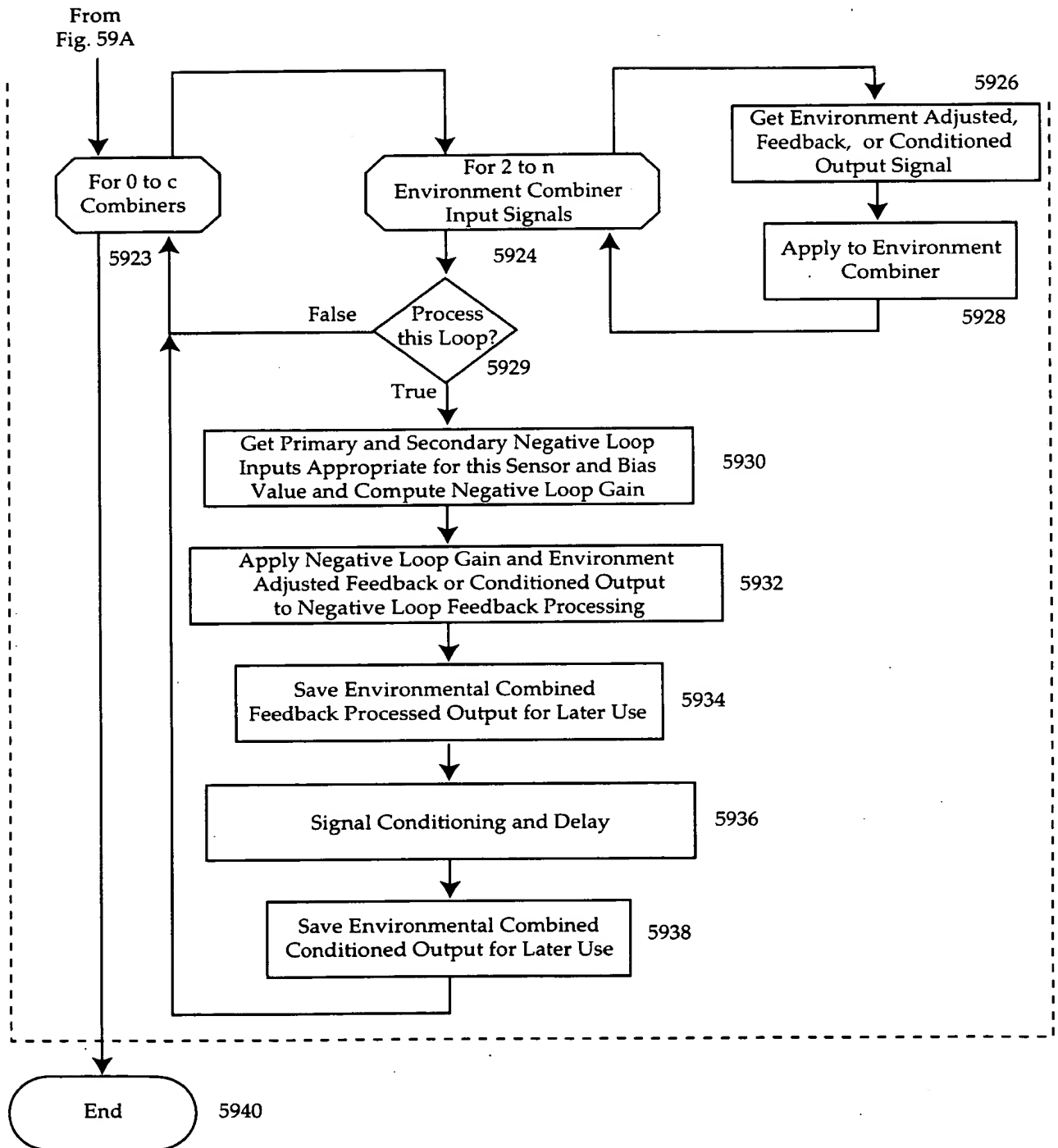
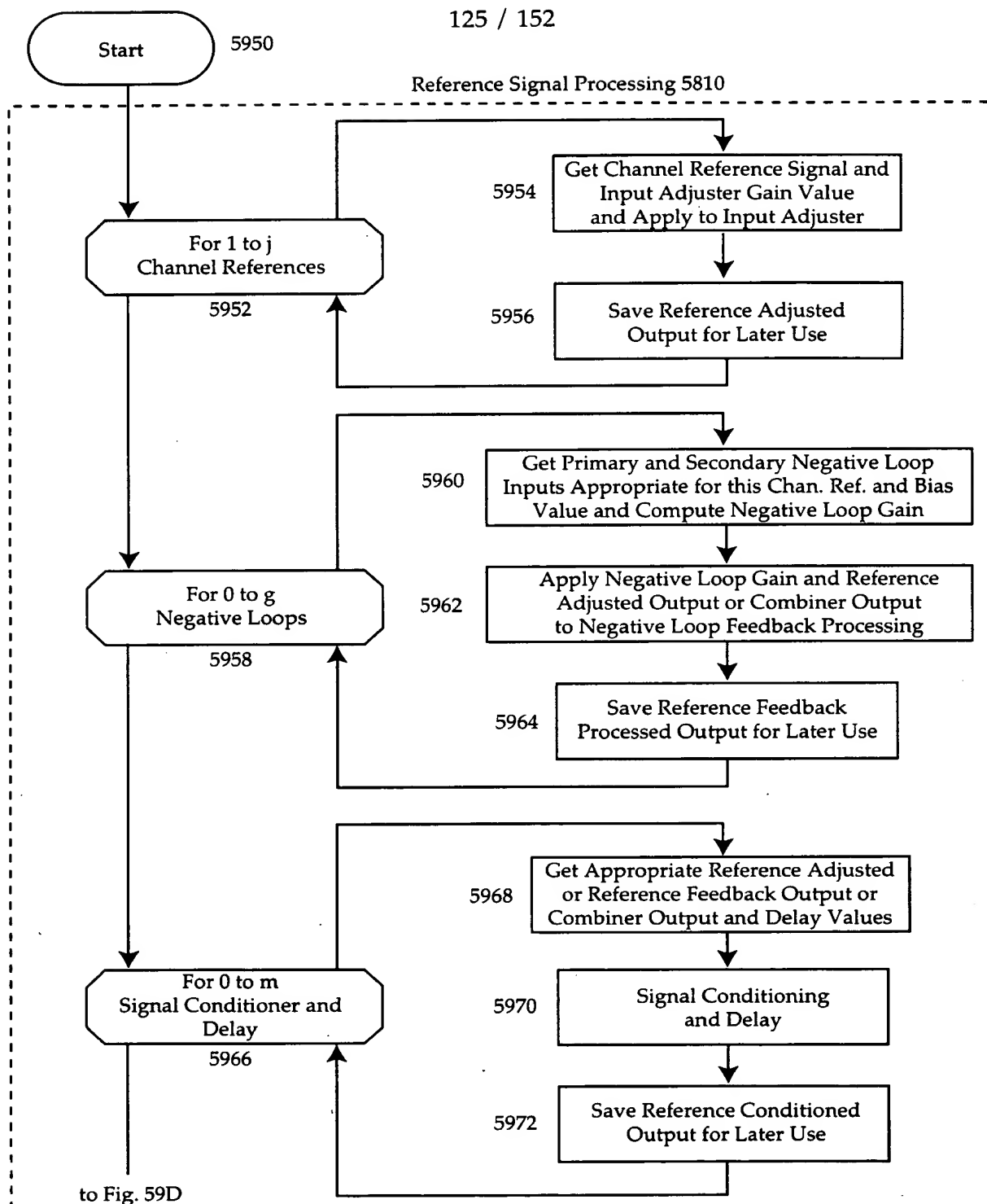


FIG. 59B

—



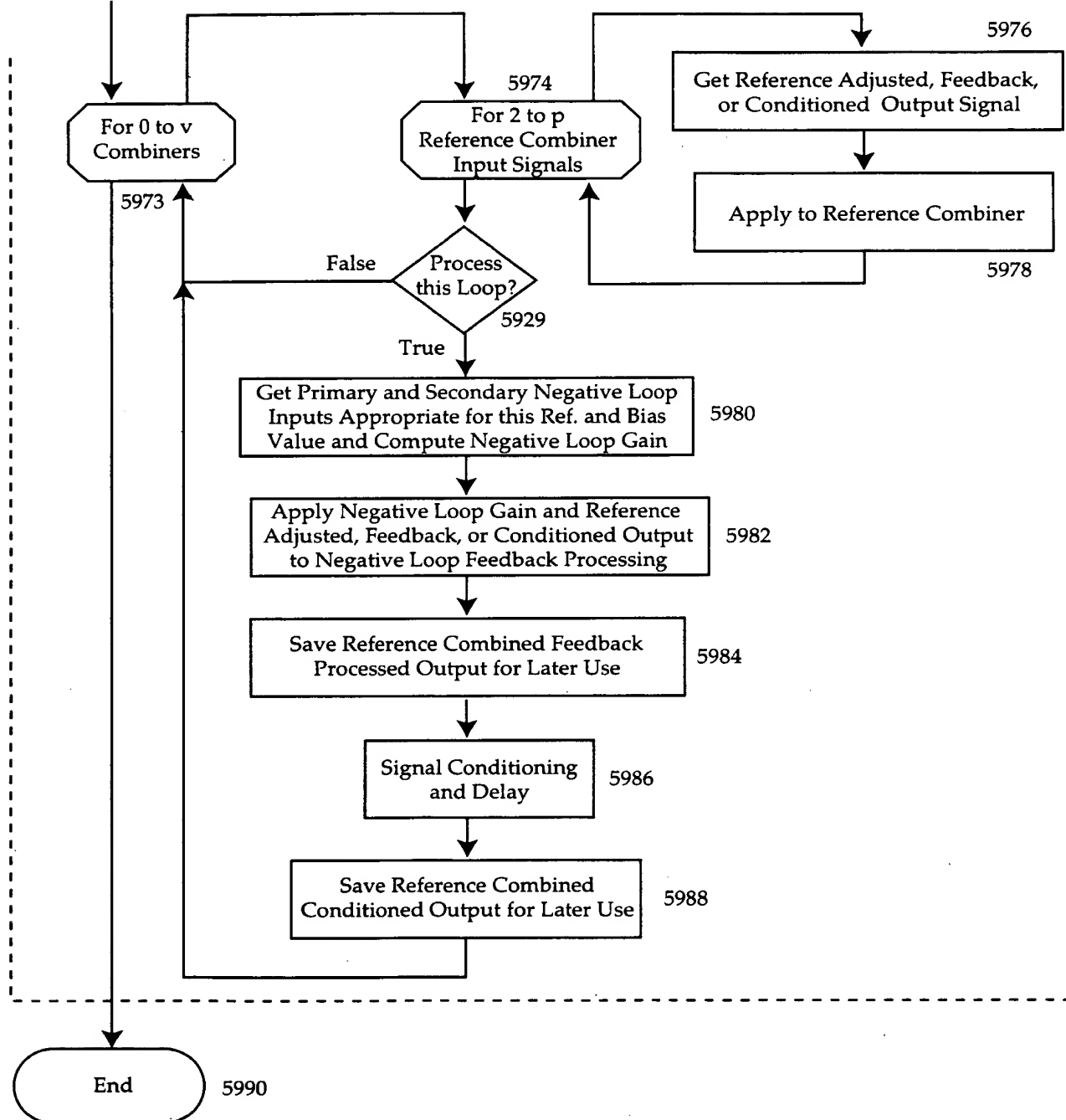
From  
Fig. 59A

FIG. 59D

006275-112900

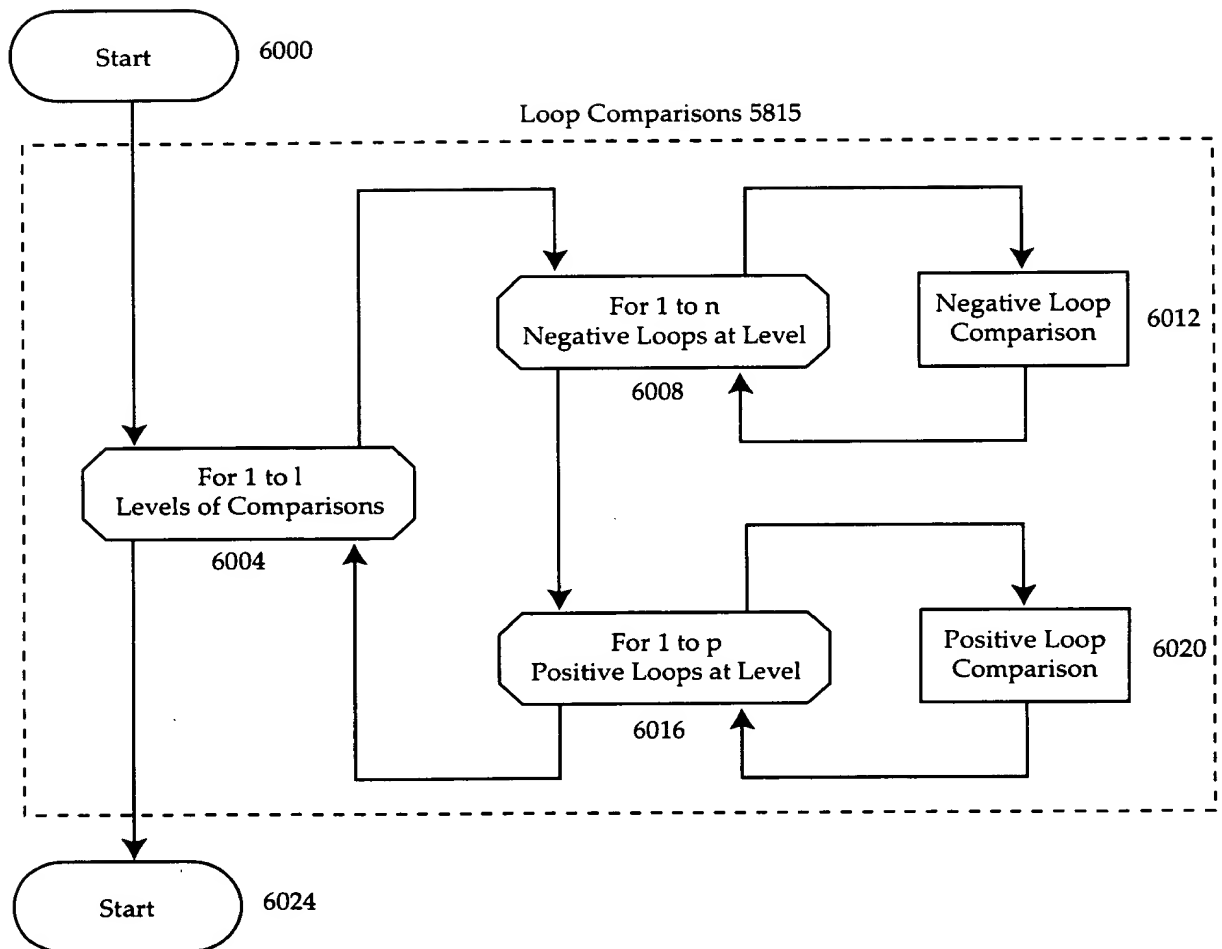


FIG. 60A

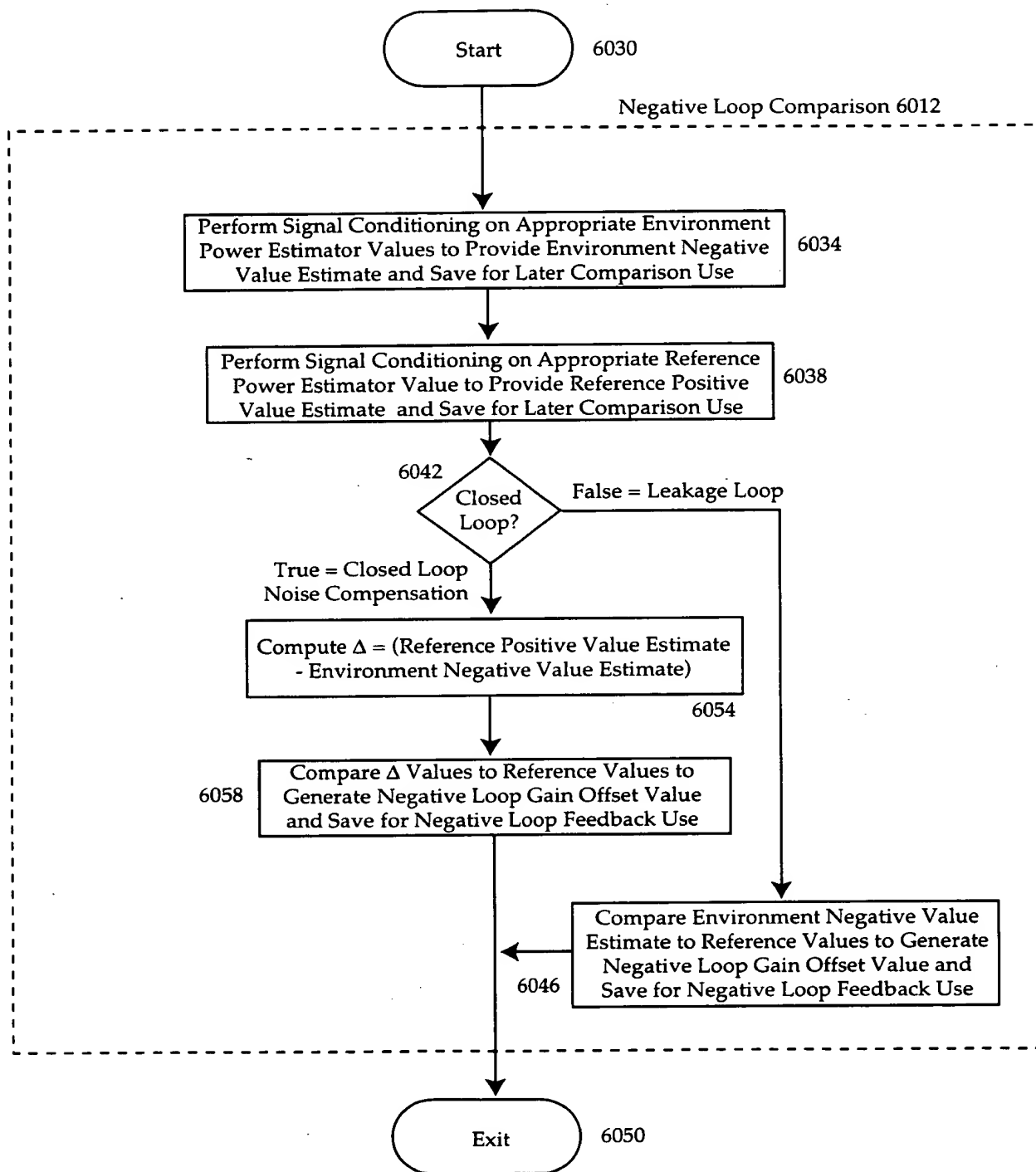


FIG. 60B



6060

Start

Positive Loop Comparison 6020

Perform Signal Conditioning on Appropriate Environment  
Power Estimator Value to Provide Environment Positive  
Value Estimate and Save for Later Comparison Use

6064

Perform Signal Conditioning on Appropriate Reference  
Power Estimator Value to Provide Reference Negative  
Value Estimate and Save for Later Comparison Use

6068

6072

Closed  
Loop?

False

True = Closed Loop  
Noise Compensation

6080

Compute  $\Delta$  = Environment Positive Value Estimate  
- Reference Negative Value Estimate

6084

 $\Delta > 0$  ?

True

False

$\Delta = 0$   
(No Noise)

6088

Exit

6076

FIG. 60C

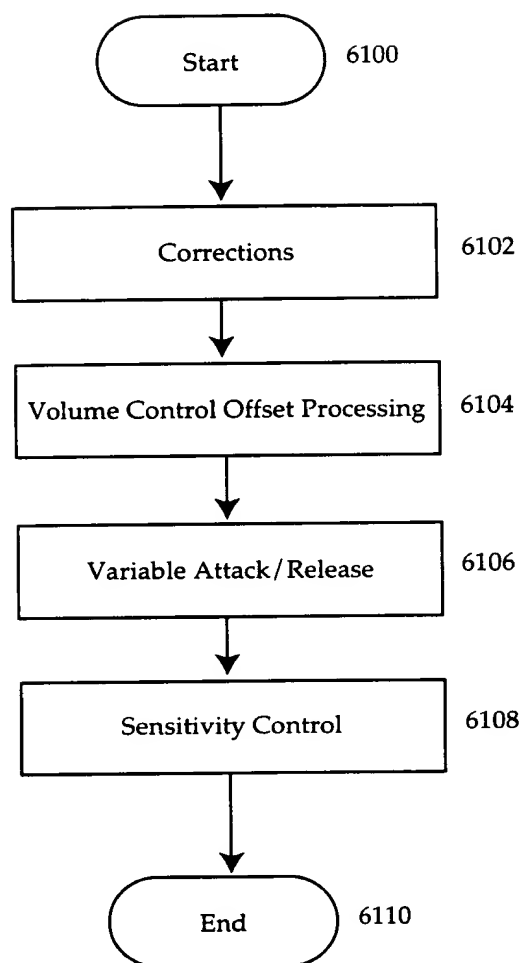


FIG. 61A

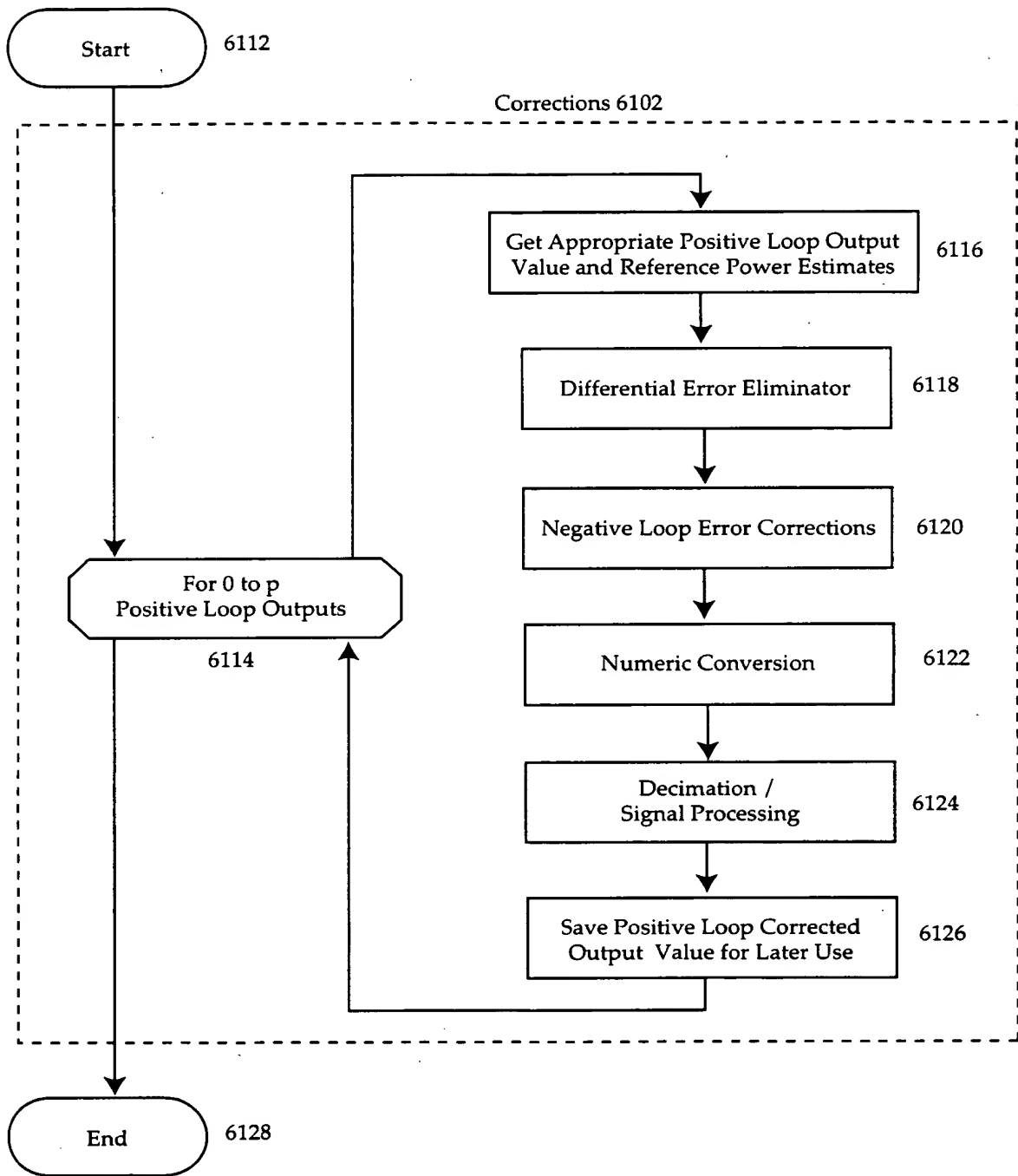


FIG. 61B

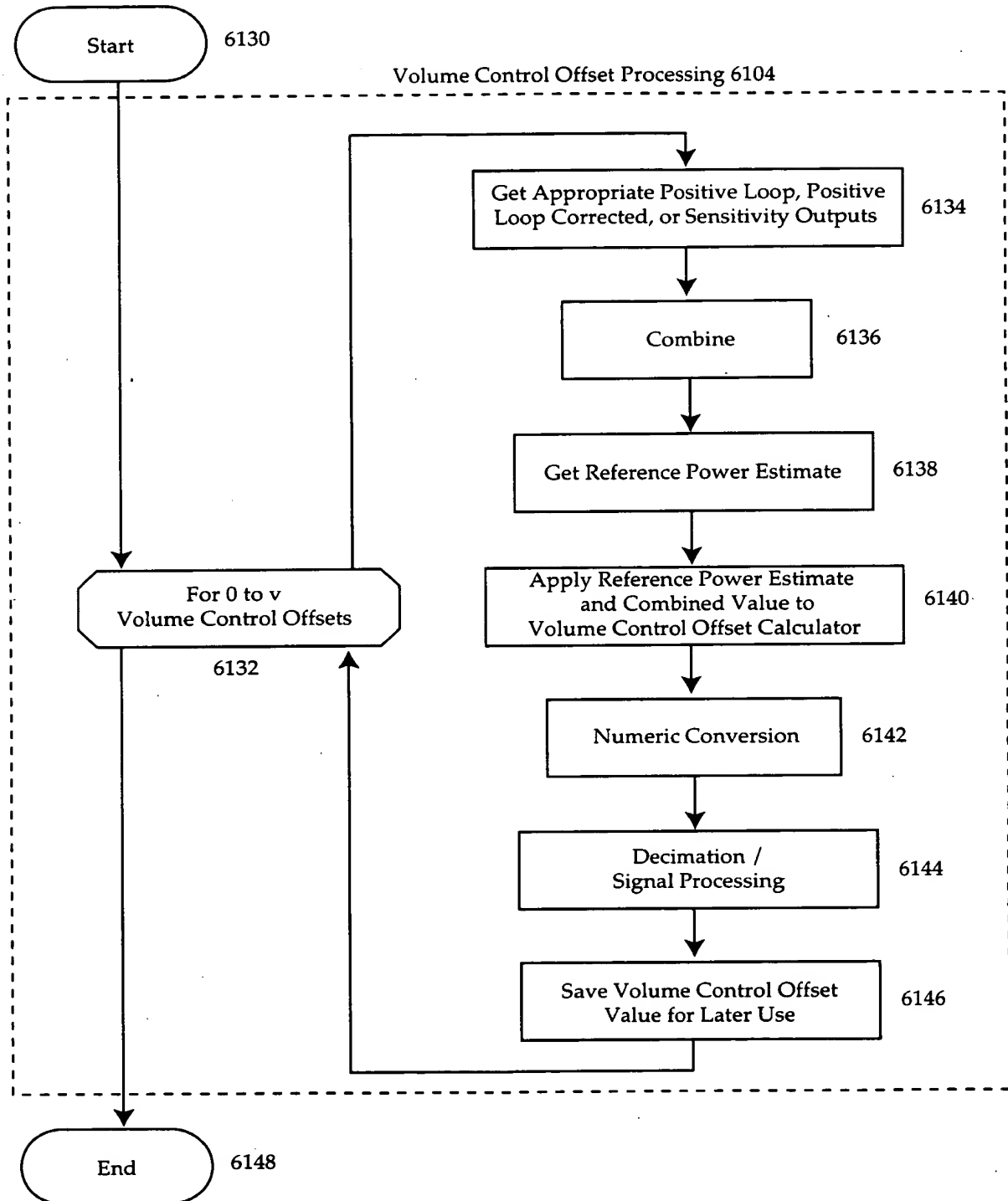


FIG. 61C

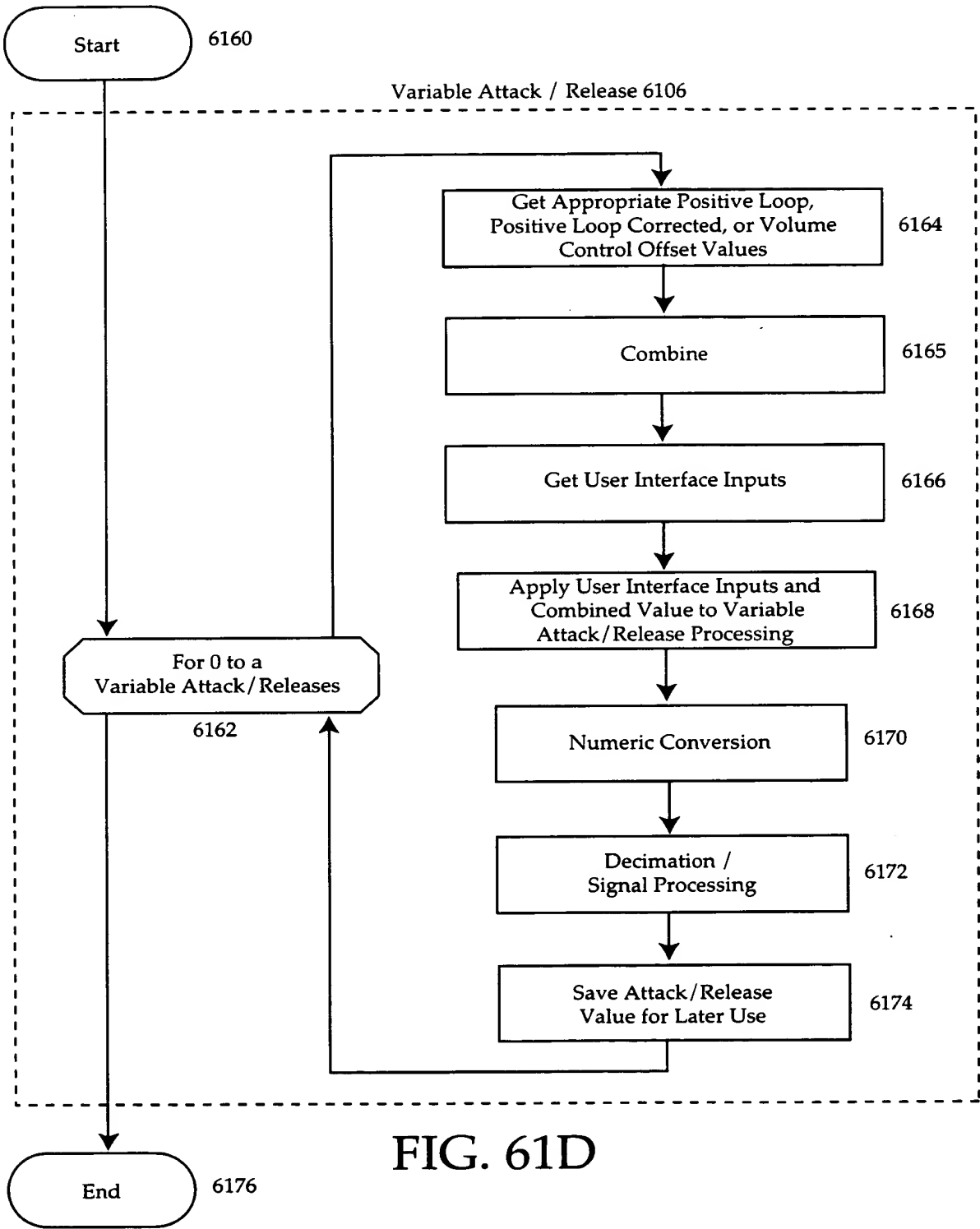


FIG. 61D

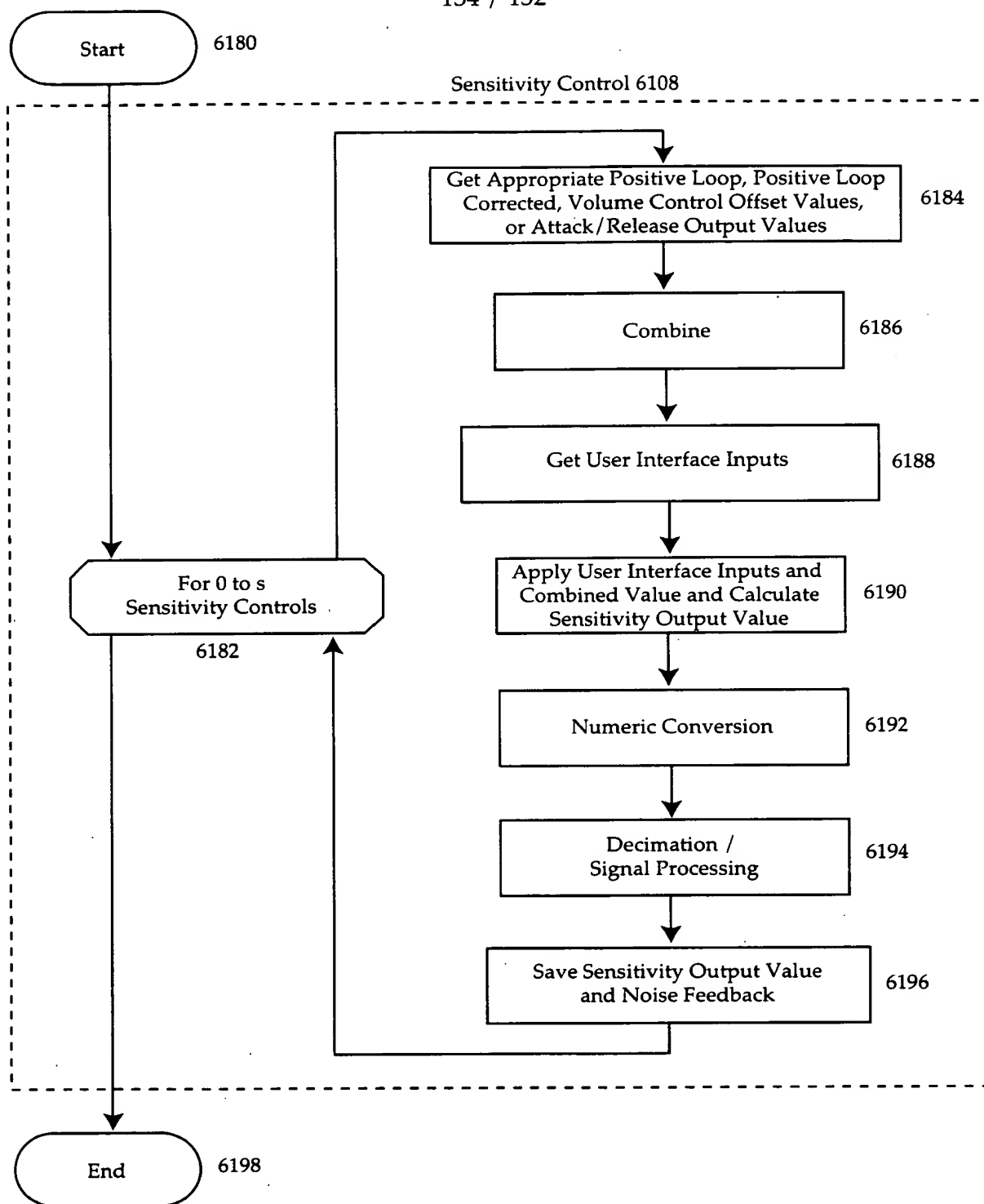
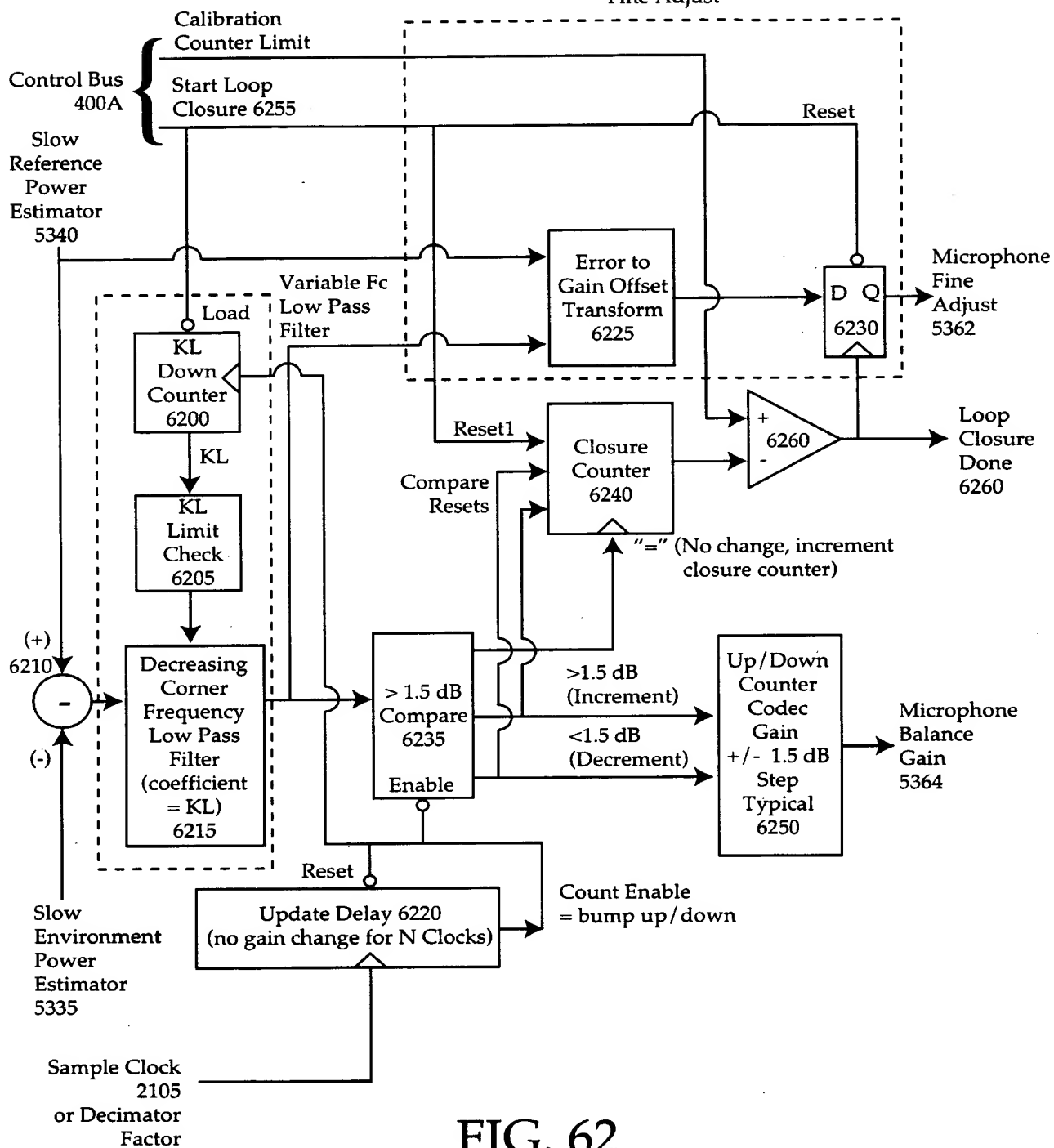


FIG. 61E



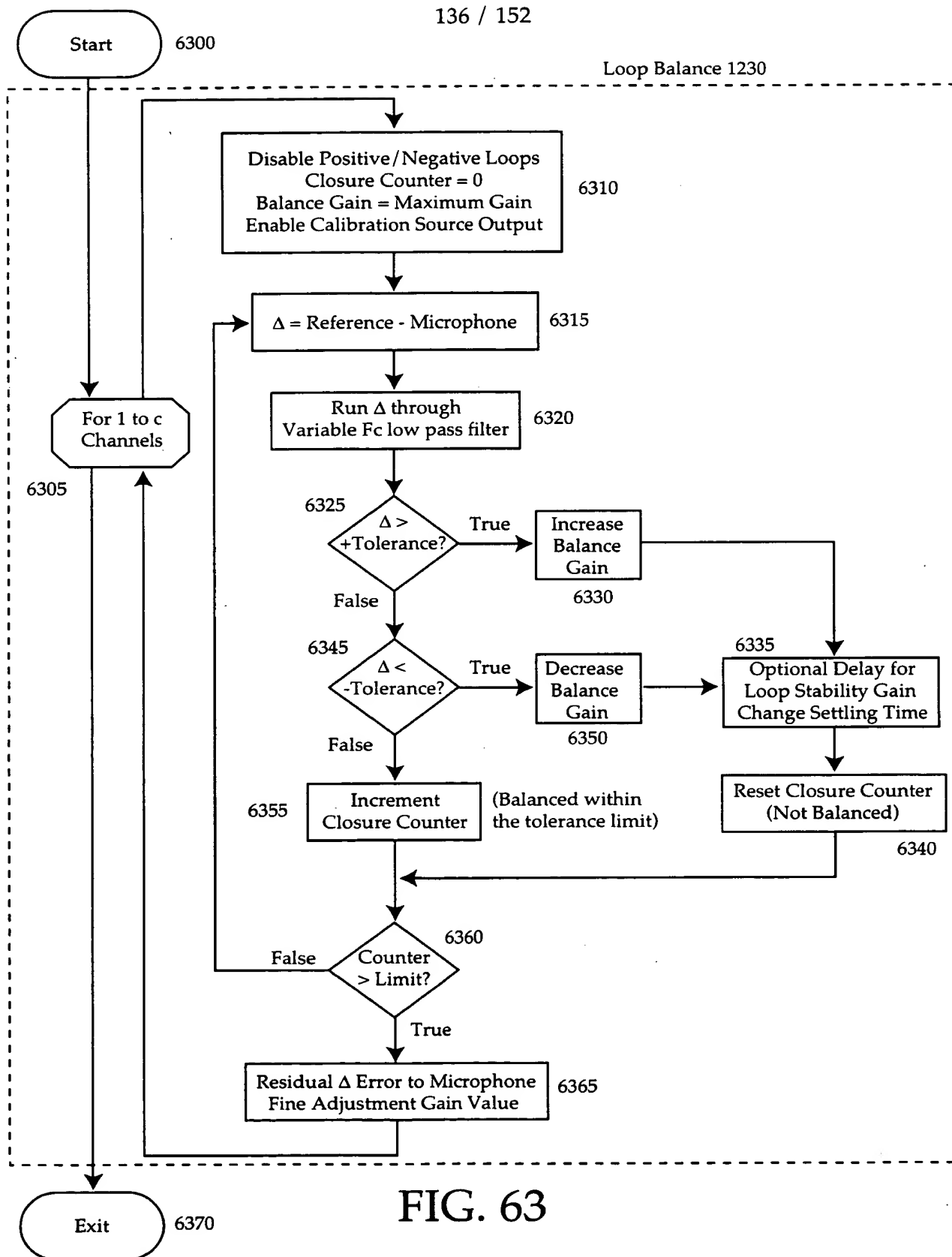


FIG. 63



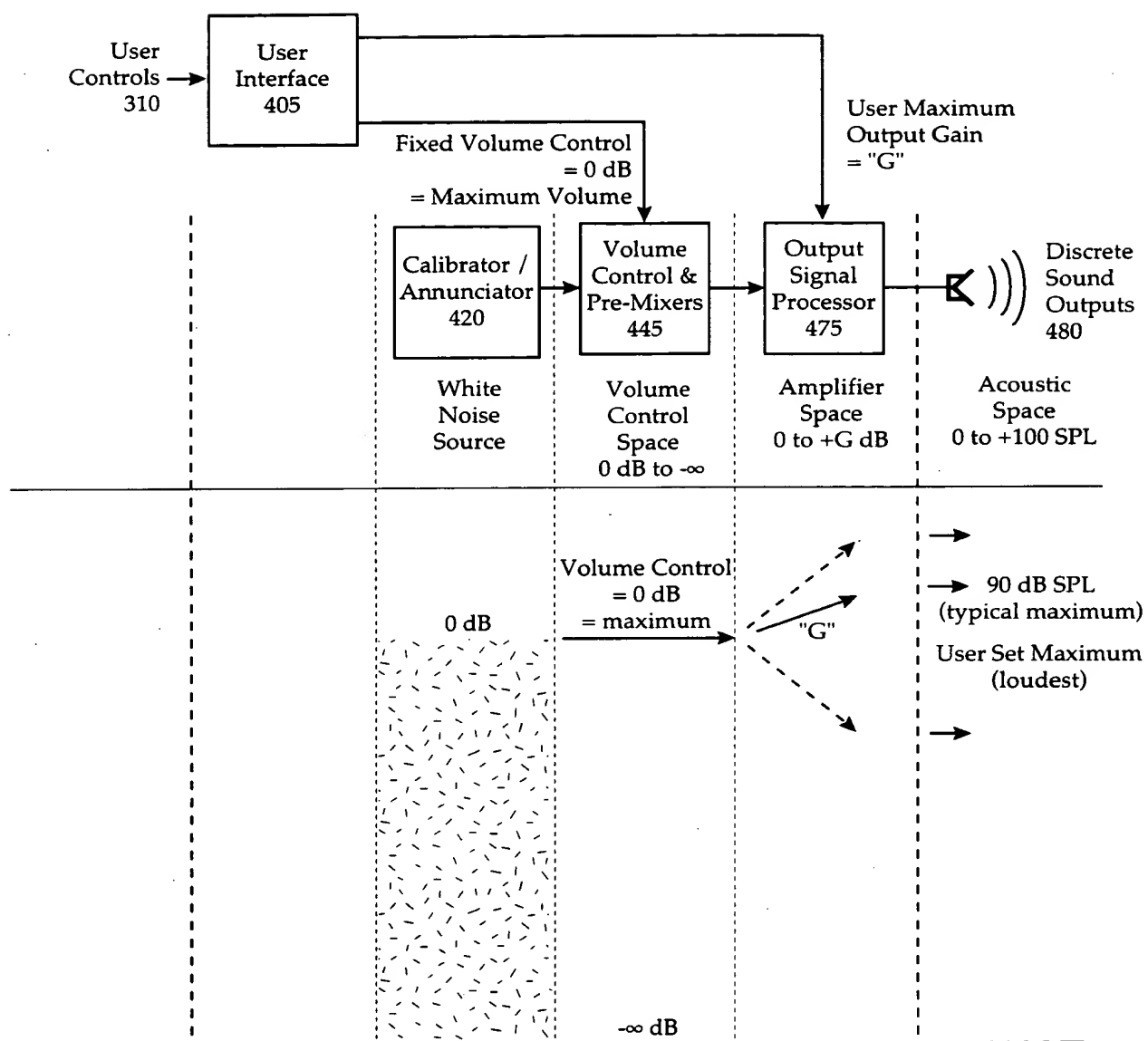


FIG. 64

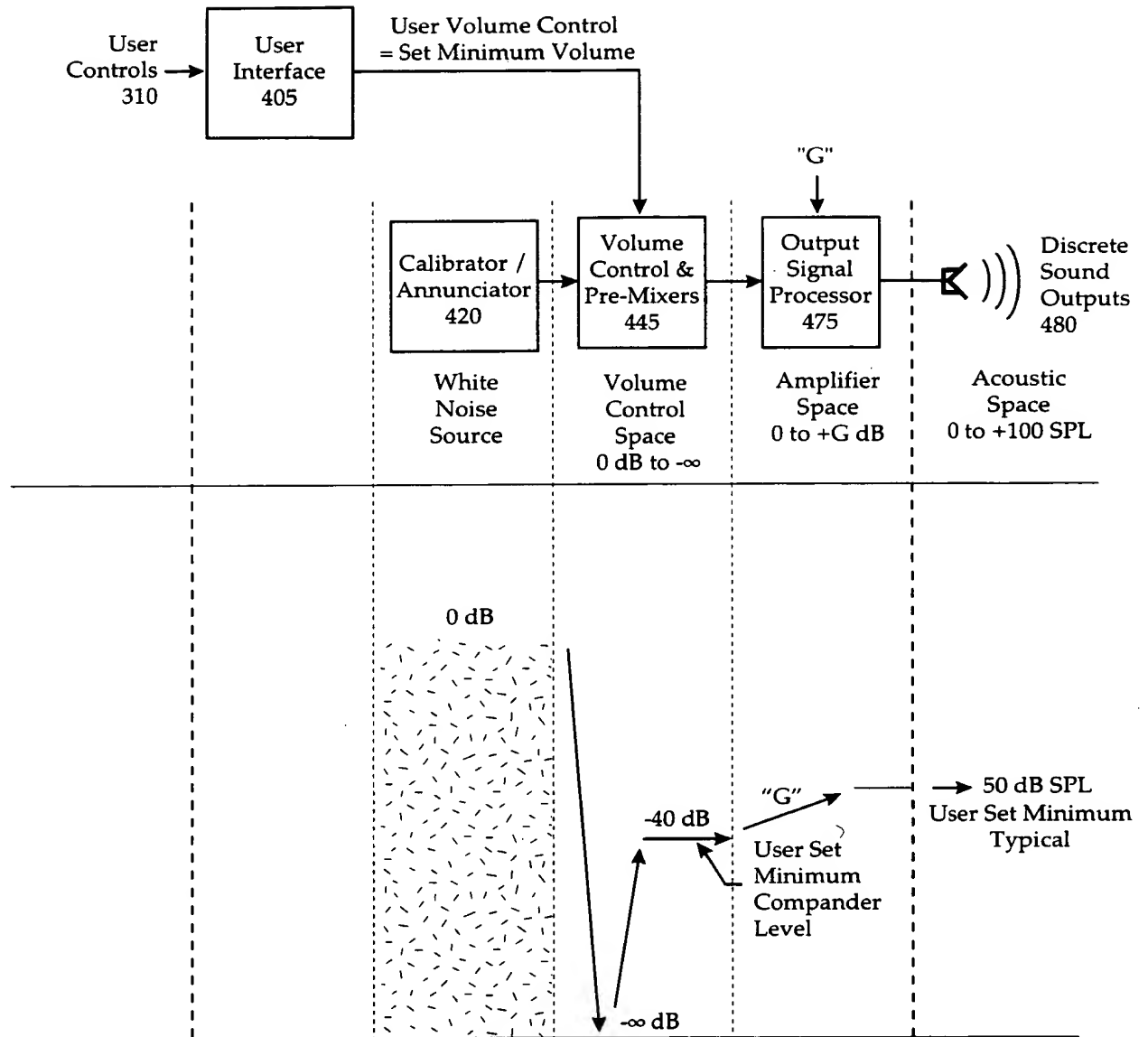


FIG. 65

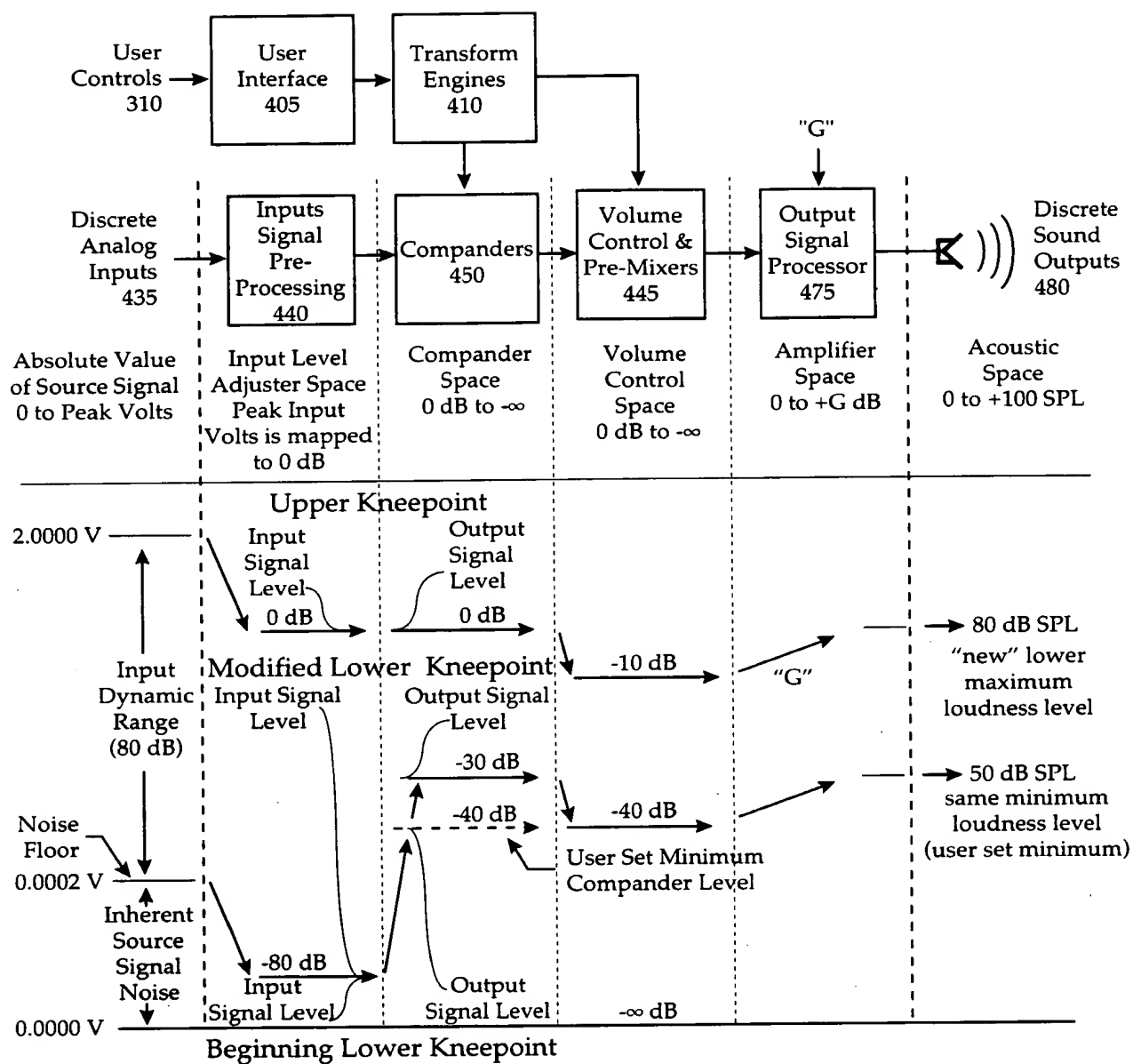


FIG. 66

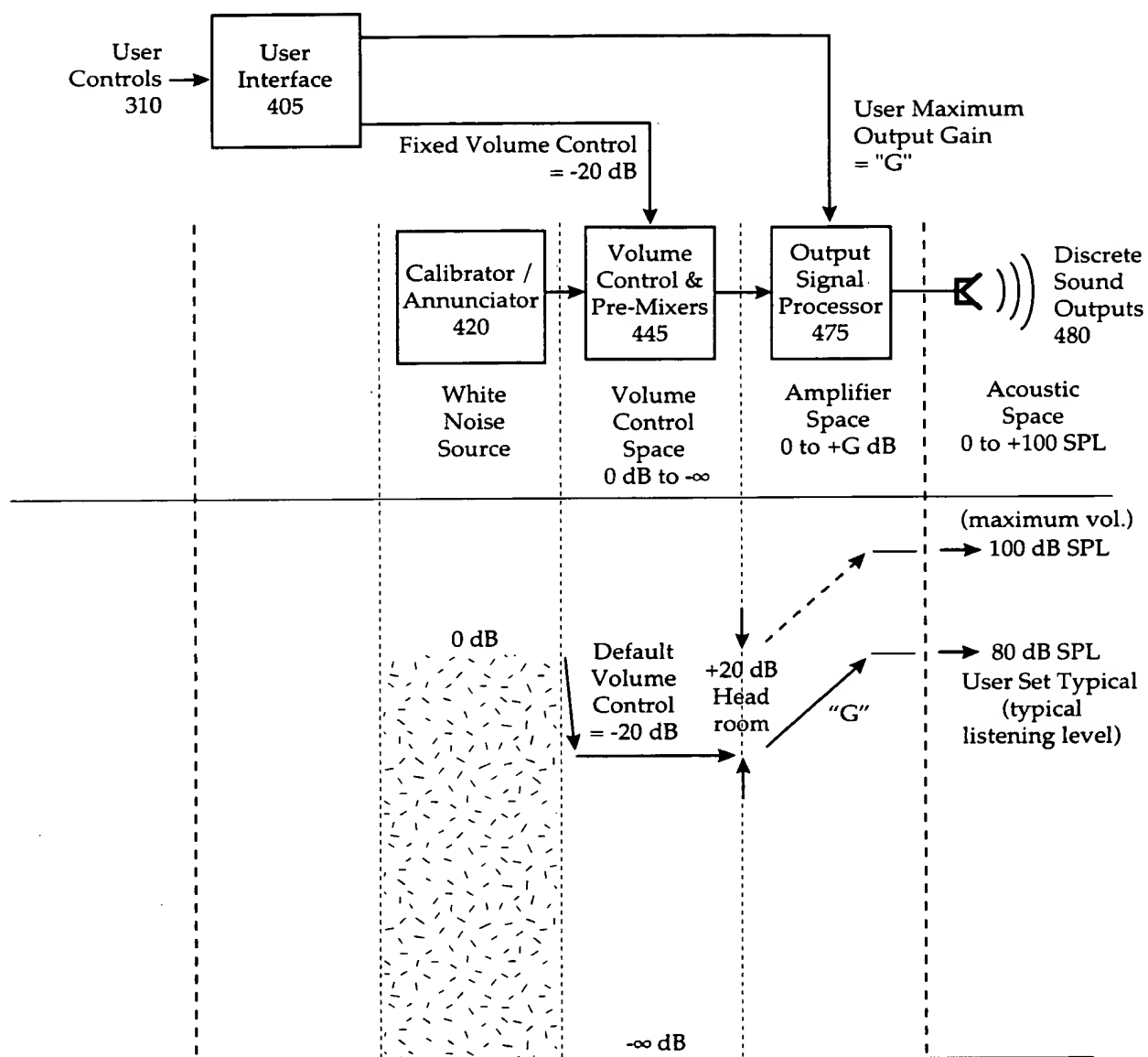


FIG. 67

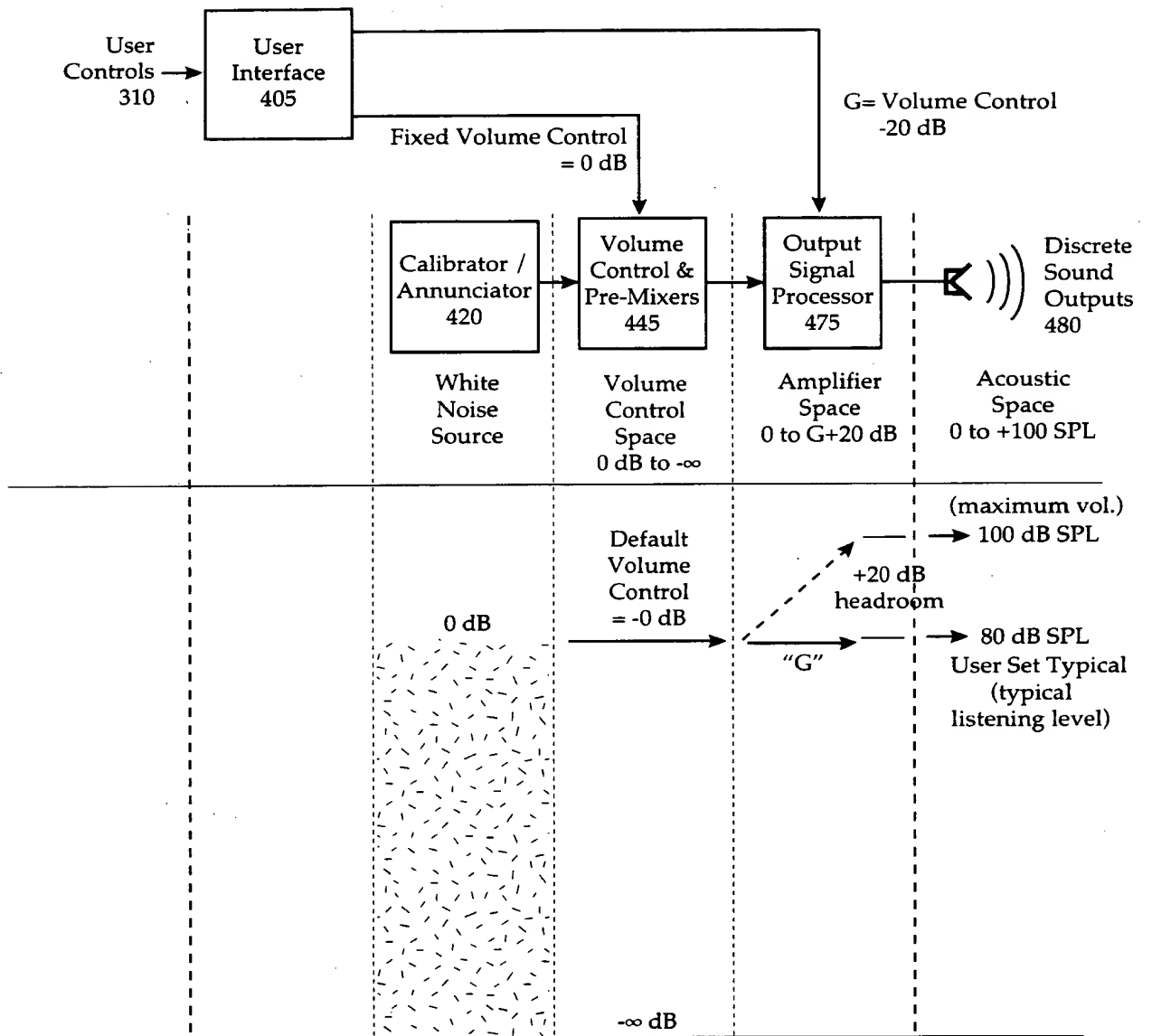


FIG. 68

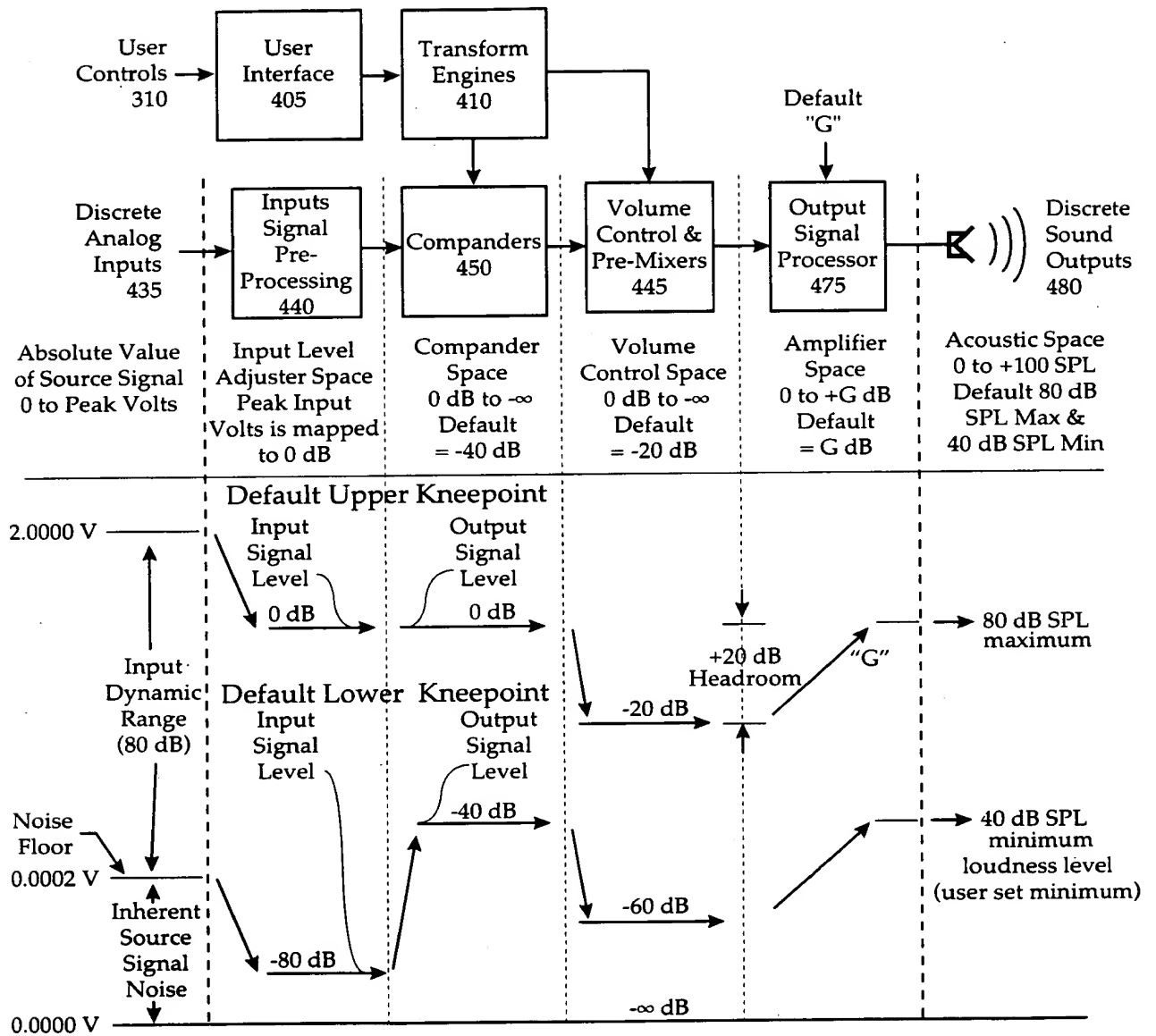


FIG. 69

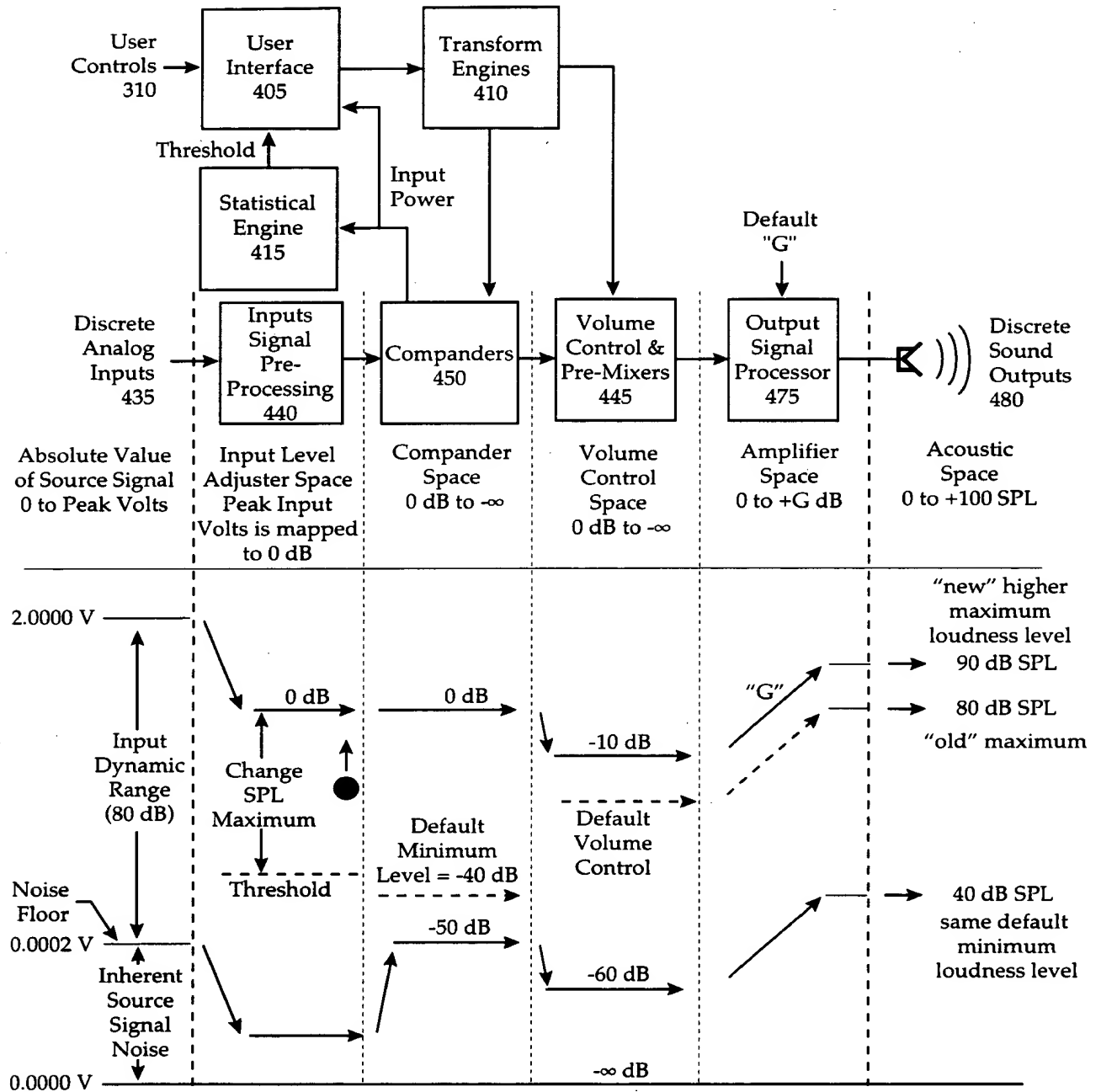


FIG. 70

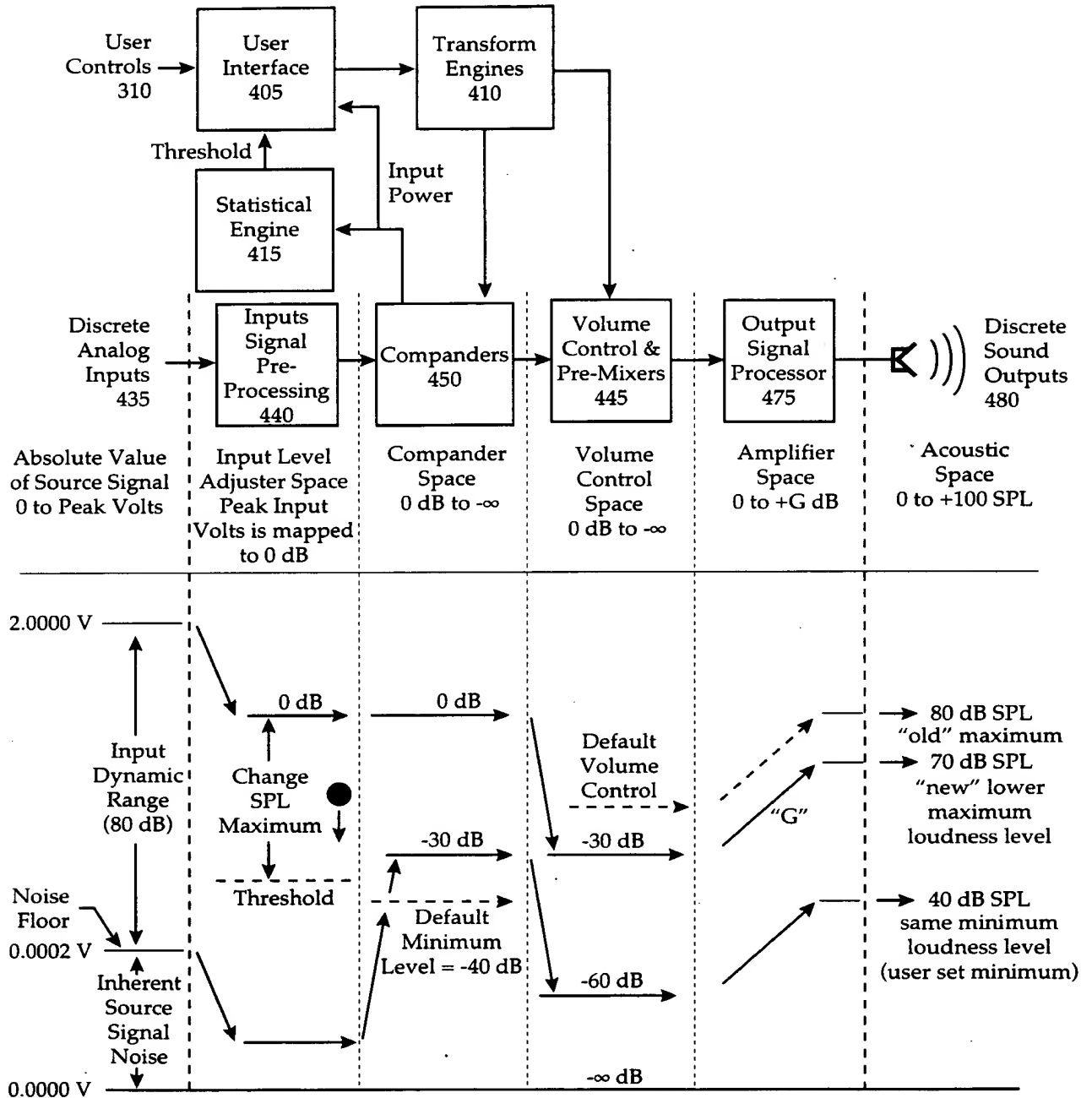


FIG. 71





FIG. 72

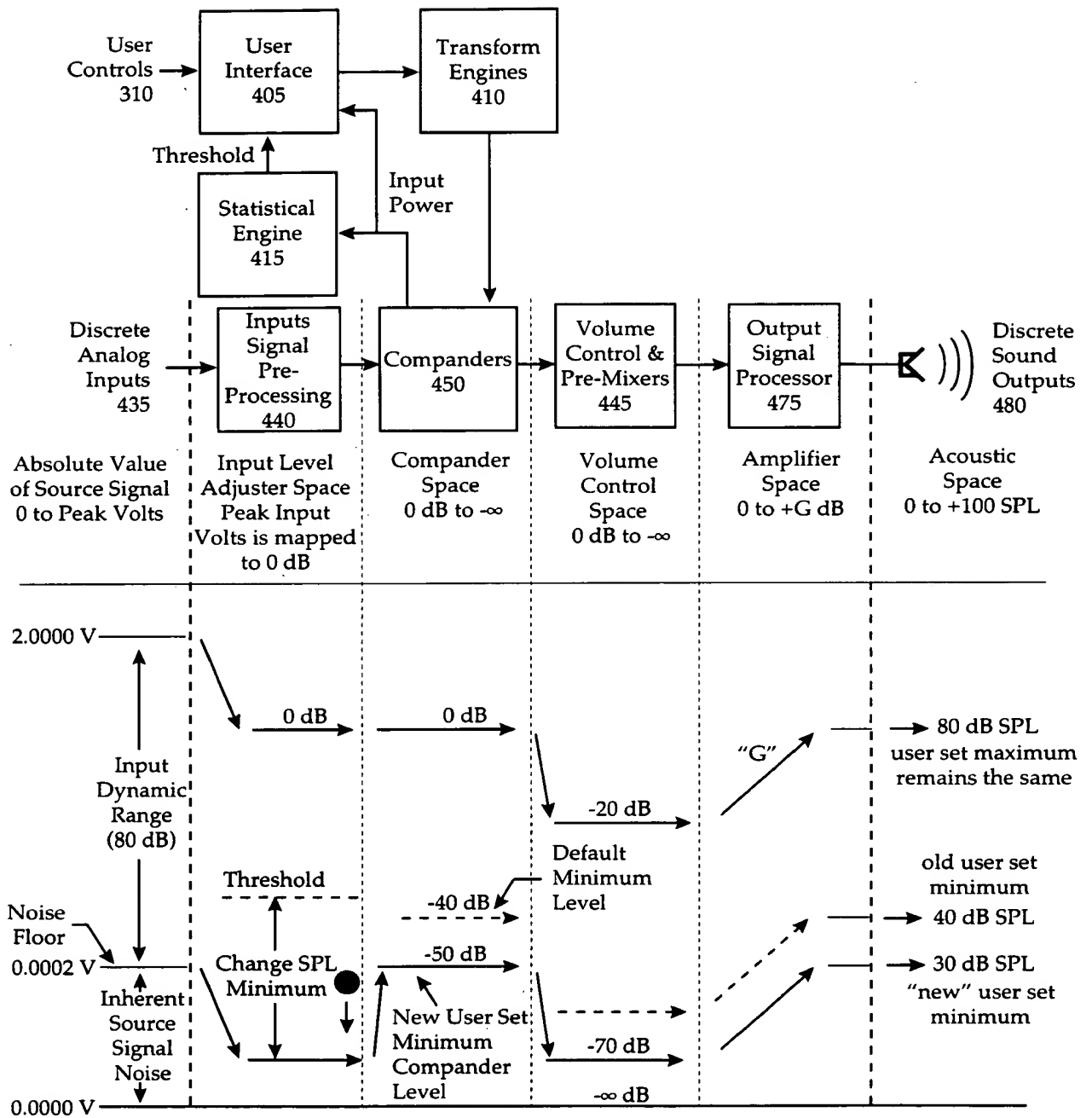


FIG. 73



FIG. 74

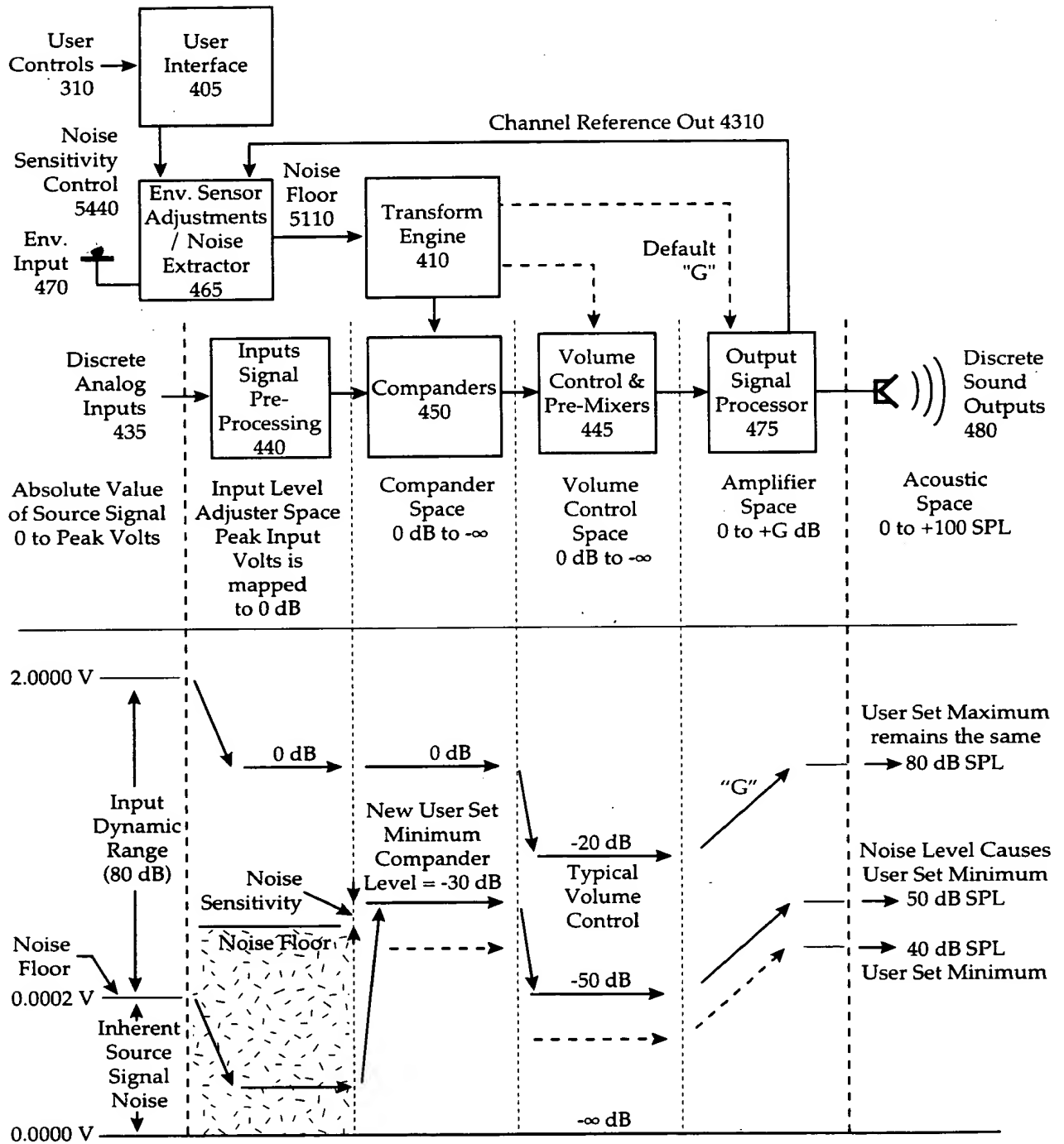


FIG. 75

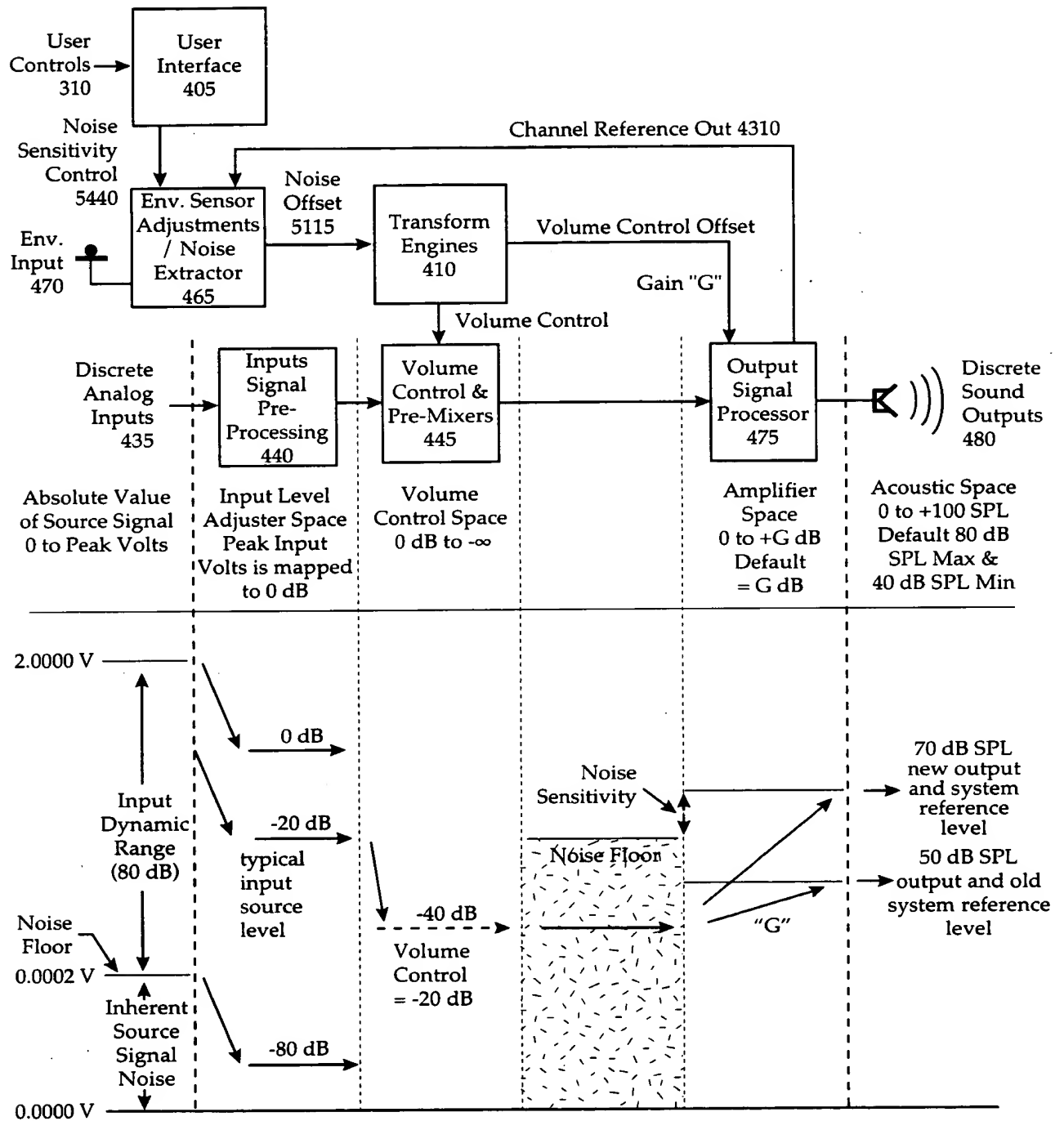


FIG. 76

Setup Command Decoder  
and Setup Command Execute  
660E and 660F

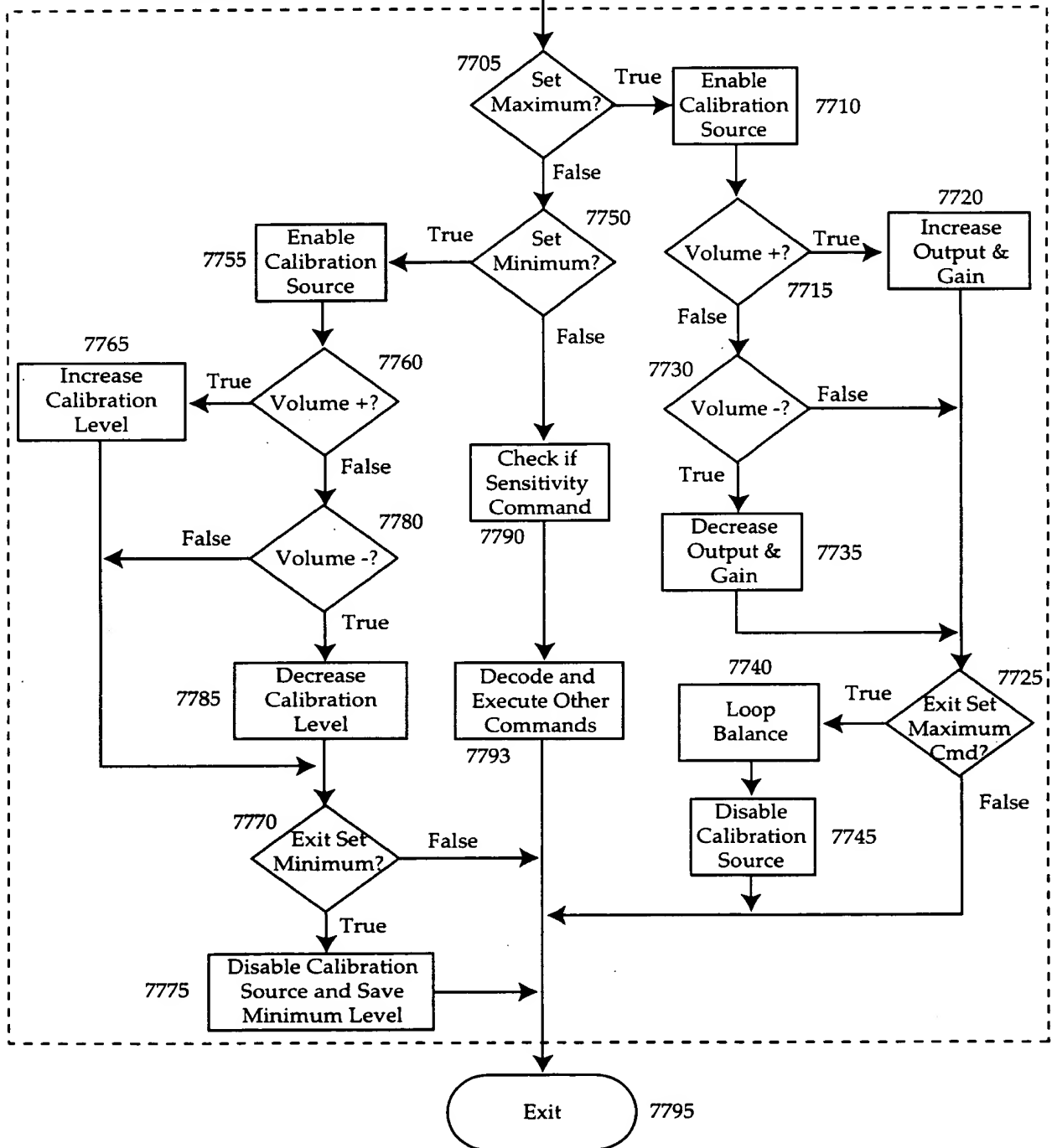


FIG. 77

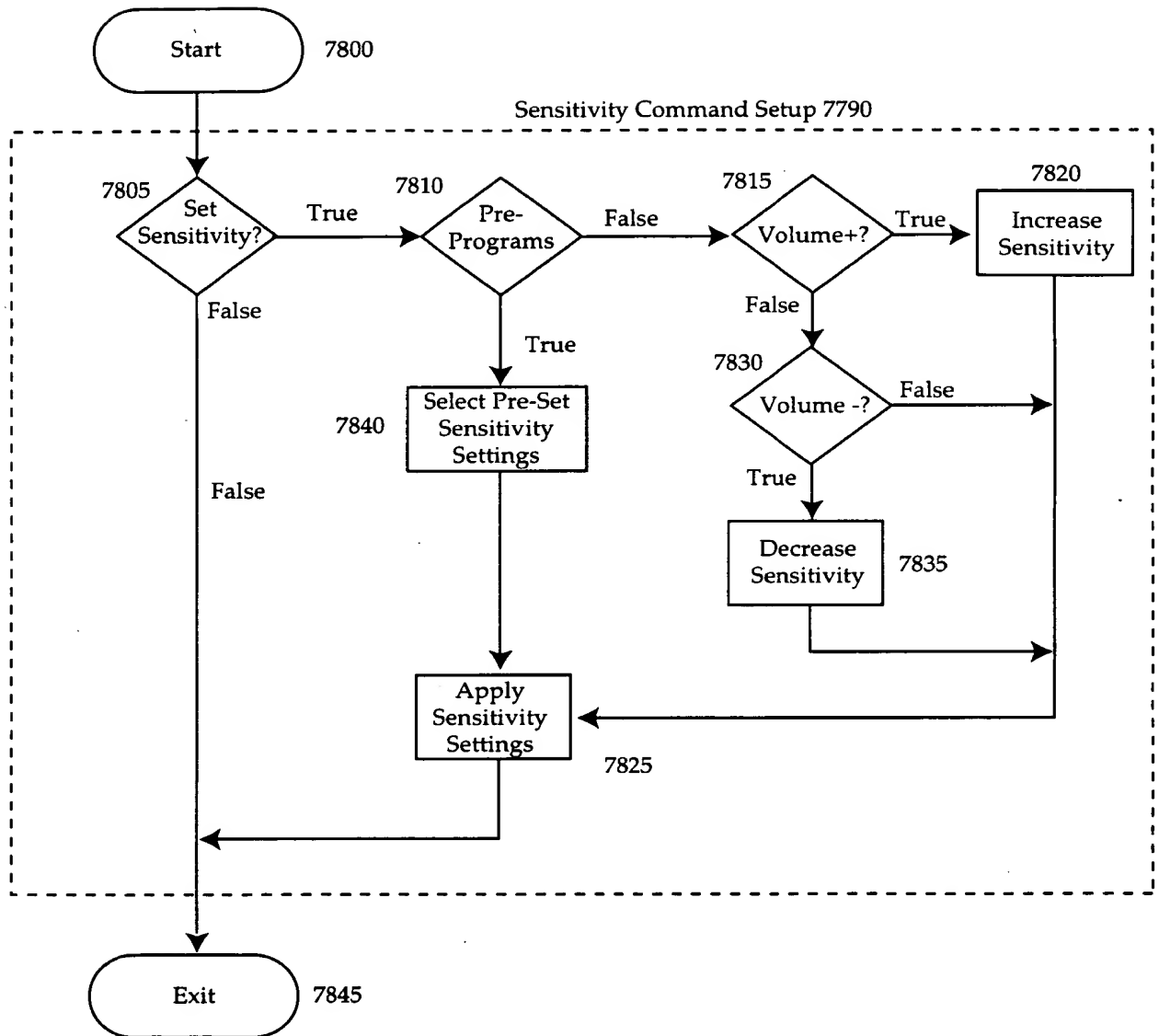


FIG. 78

Operation Command Decoder  
and Operation Code Execute  
660C and 660D

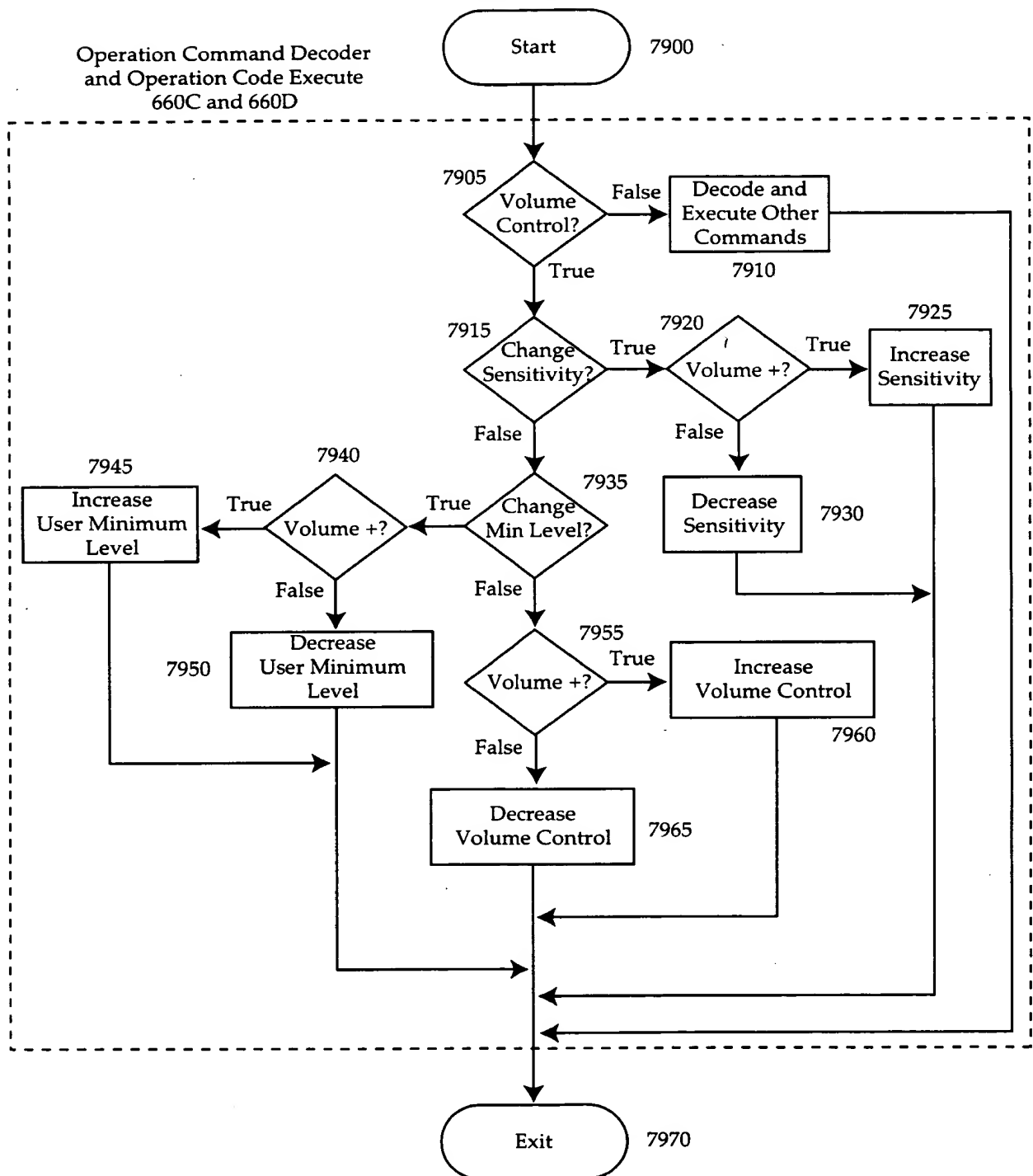


FIG. 79